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Hyperfast Rectifier, 75 A FRED Pt[®] G5



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS									
I _{F(AV)}	75 A								
V _R	1200 V								
V _F at I _F at 125 °C	1.85 V								
t _{rr}	40 ns								
T _J max.	175 °C								
Package	TO-247AD 2L								
Circuit configuration	Single								

FEATURES

- Hyperfast and optimized Qrr
- Best in class forward voltage drop and switching losses trade off
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Polyimide passivation
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant. Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

MECHANICAL DATA

Case: TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating **Terminals**: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Repetitive peak reverse voltage	V _{RRM}		1200	V						
Average rectified forward current	I _{F(AV)}	T _C = 103 °C, D = 0.50	75							
Non-repetitive peak surge current	I _{FSM}	T_{C} = 45 °C, t_{p} = 10 ms, sine wave	470	А						
Repetitive peak forward current	I _{FRM}	T _C = 103 °C, D = 0.50, f = 20 kHz	150							
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	1200	-	-				
Forward voltage	V _F	I _F = 75 A	-	2.0	2.6	V			
Forward voltage		I _F = 75 A, T _J = 125 °C	-	1.85	-				
		V _R = V _R rated	-	-	50				
Reverse leakage current	I _R	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA			
Junction capacitance	CT	V _R = 200 V	-	36	-	pF			
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH			

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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
		I _F = 1.0 A, dI _F /dt =	100 A/ μ s, V _R = 30 V	-	40	-				
Reverse recovery time	t _{rr}	T _J = 25 °C		-	145	-	ns			
		T _J = 125 °C		-	220	-				
Peak recovery current	1	T _J = 25 °C	I _F = 50 A dI _F /dt = 600 A/μs V _R = 400 V	-	24	-	A nC			
Feak recovery current	IRRM	T _J = 125 °C		-	43	-				
	0	T _J = 25 °C		-	1710	-				
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	4820	-				
Reverse recovery time	+	T _J = 25 °C		-	115	-	20			
Reverse recovery lime	t _{rr}	T _J = 125 °C		-	165	-	ns			
Peak recovery current		T _J = 25 °C	I _F = 75 A dI _F /dt = 1000 A/μs	-	42	-	А			
Feak recovery current	IRRM	T _J = 125 °C	$V_{\rm R} = 800 \rm V$	-	72	-				
	0	T _J = 25 °C		-	2780	-	nC			
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	7100	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Thermal resistance, junction-to-case	R _{thJC}		-	-	0.36	°C/W				
Weight			-	5.5	-	g				
Weight			-	0.2	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C				
Marking device		Case style TO-247AD 2L	E5PH7512L							

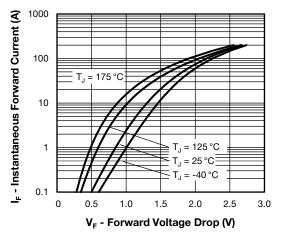


Fig. 1 - Forward Voltage Drop Characteristics

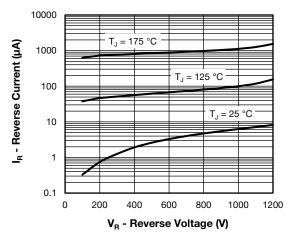


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



VS-E5PH7512L-N3

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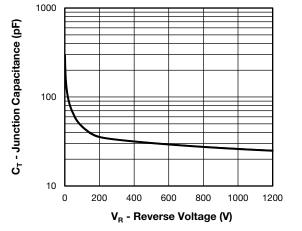


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

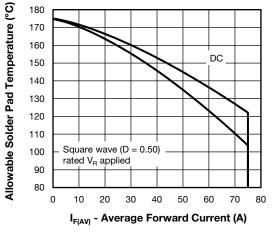


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

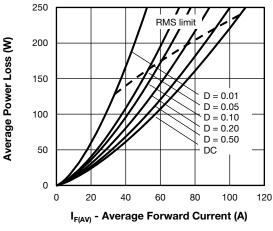


Fig. 5 - Forward Power Loss Characteristics

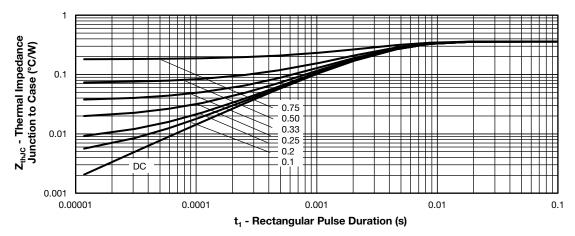


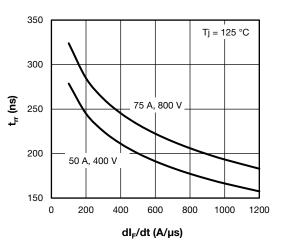
Fig. 6 - Transient Thermal Impedance, Junction to Case

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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

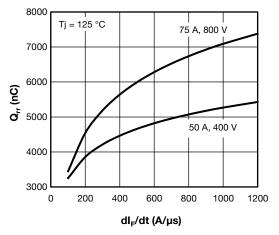


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt

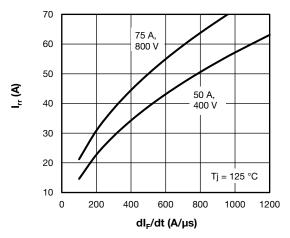


Fig. 9 - Typical Reverse Recovery Current vs. dI_F/dt

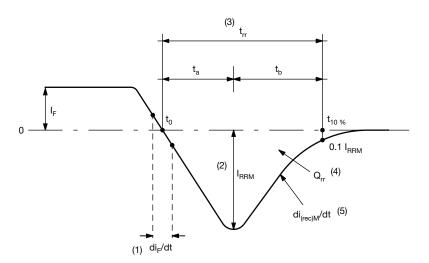


Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code	VS-	E	5	Р	Н	75	12	L	N3	
	1	2	3	4	5	6	7	8	9	
	1 -	- Visł	nay Sem	niconduc	ctors pro	oduct				
	2 - Circuit configuration:									
		E = single diode, 2 pins								
	3 - FRED Pt Gen 5									
	4 -	P =	TO-247	' packag	je					
	5 -	Pro	cess typ	be:						
		H =	hyperfa	st recov	very					
	6 -	Cur	rent rati	ng (75 =	= 75 A)					
	7 - Voltage rating (12 = 1200 V)									
	8 -	L =	long lea	ld						
	9 -	Env	ironmer	ntal digit	:					
		N3 :	= haloge	en-free,	RoHS-c	ompliar	nt, and t	otally le	ad (Pb)	

ORDERING INFORMATION (Example)									
PREFERRED P/N	ED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION								
VS-E5PH7512L-N3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95536						
Part marking information	www.vishay.com/doc?95648					



TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	IETERS	INCHES		NOTES SYMBO	SYMBOL	MILLIMETERS		INCHES		NOTES	
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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