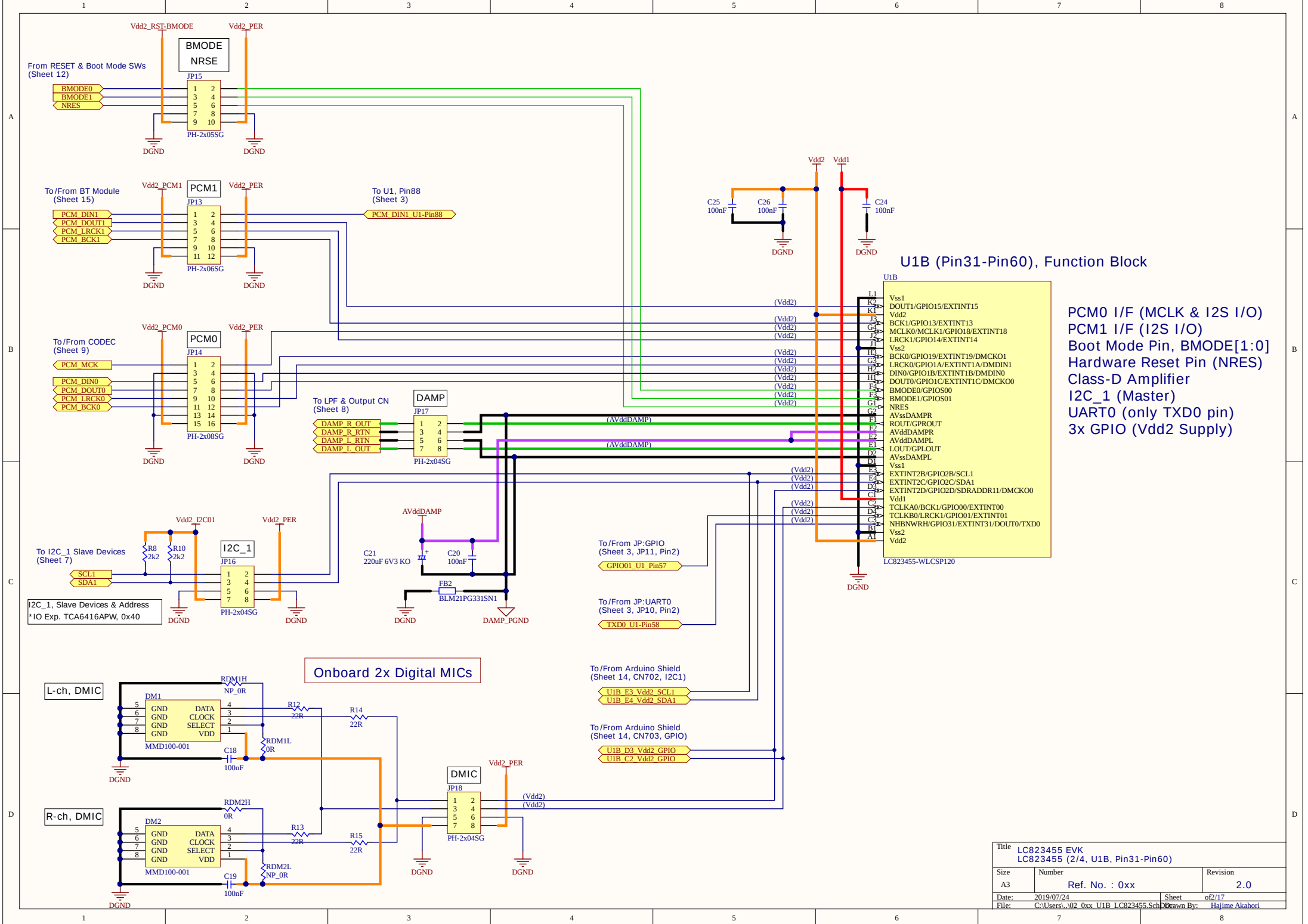
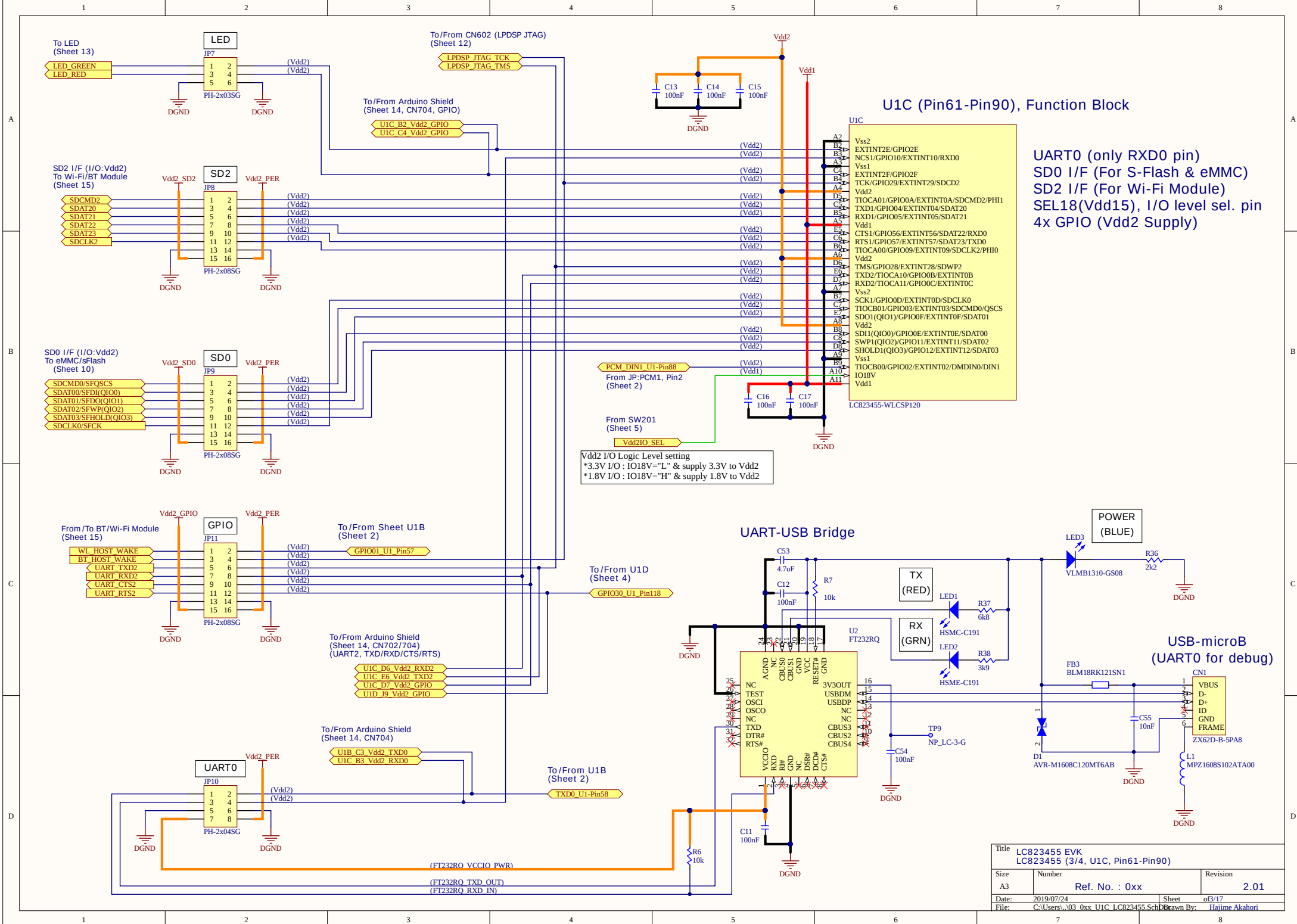
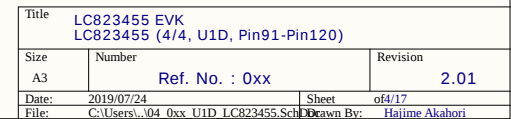
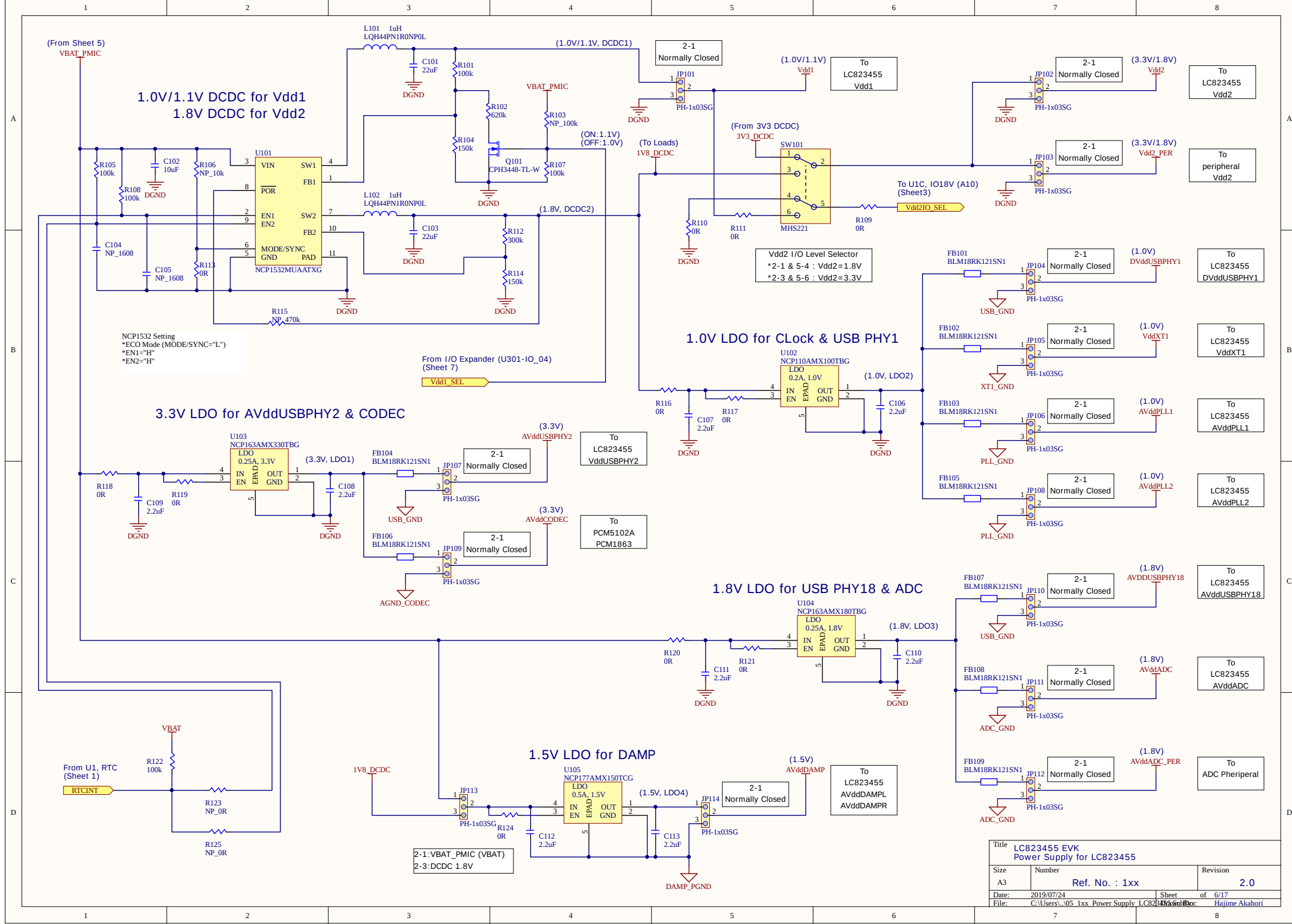




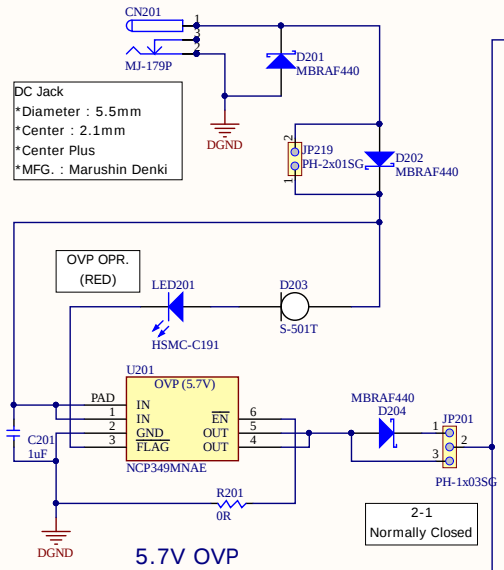




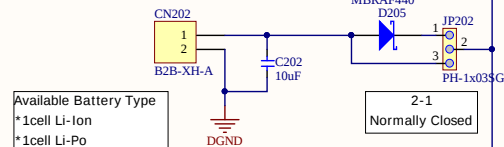


Title				LC823455 EVK	
				Power Supply for LC823455	
Size	A3	Number	Ref. No. : 1xx	Revision	2.0
Date:	2019/07/24	Sheet	6/17		
File:	C:\Users\...05_1xx Power Supply_LC823455_SchDoc			Hajime Akahori	

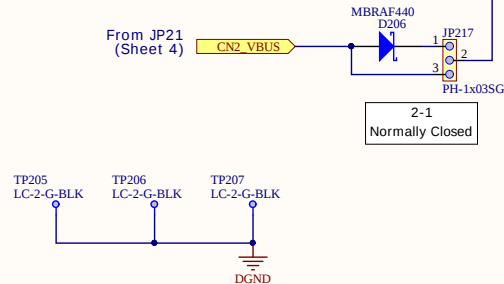
DC5V Adaptor / Power Supply Input (Protected from over or reverse voltage)



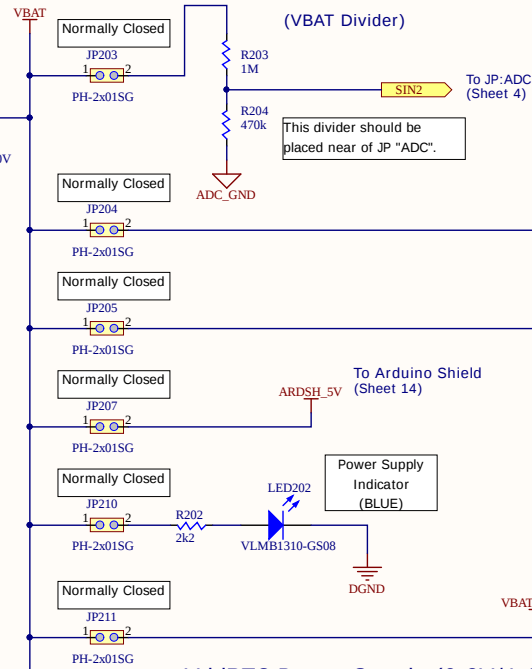
Battery / Direct Input (No Over Voltage Protection)



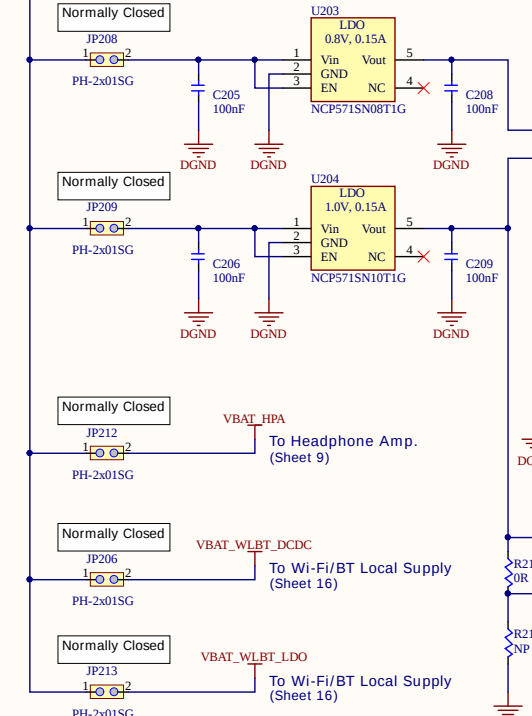
USB(CN2) Bus Power Input



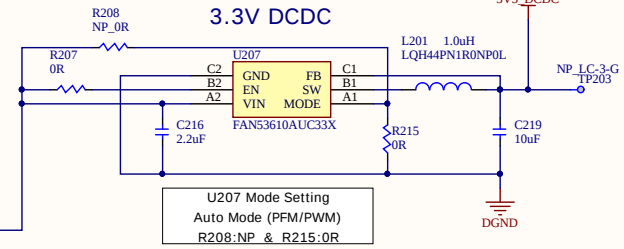
BATTERY CHECK (VBAT Divider)



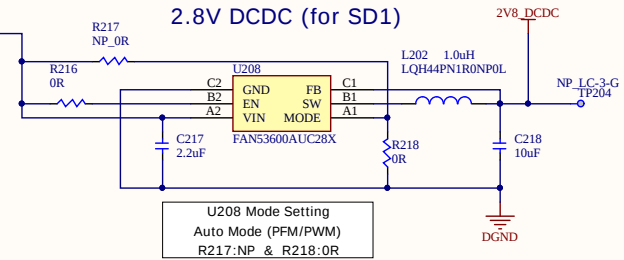
VddRTC Power Supply (0.8V/1.0V)



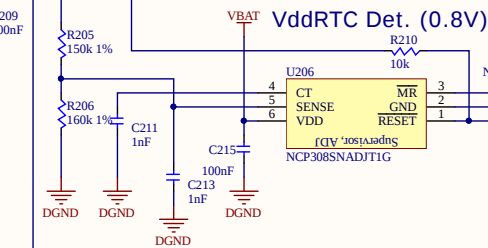
3.3V DCDC



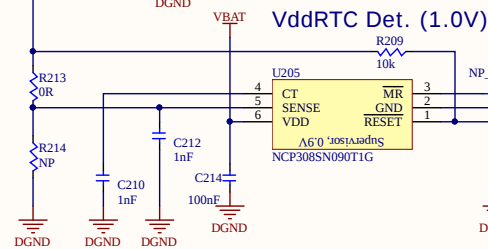
2.8V DCDC (for SD1)



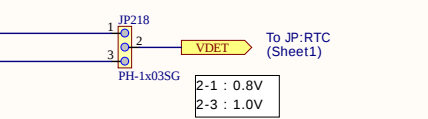
VddRTC Det. (0.8V)



VddRTC Det. (1.0V)

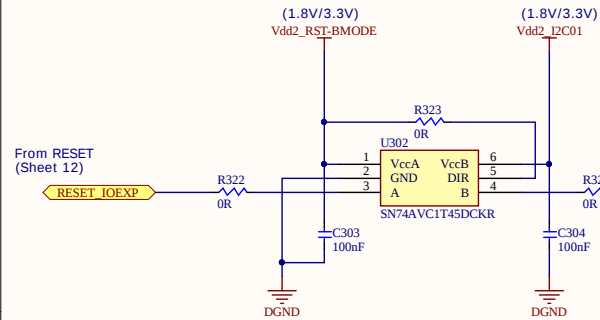


VddRTC Det. source



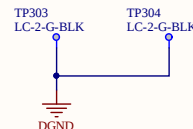
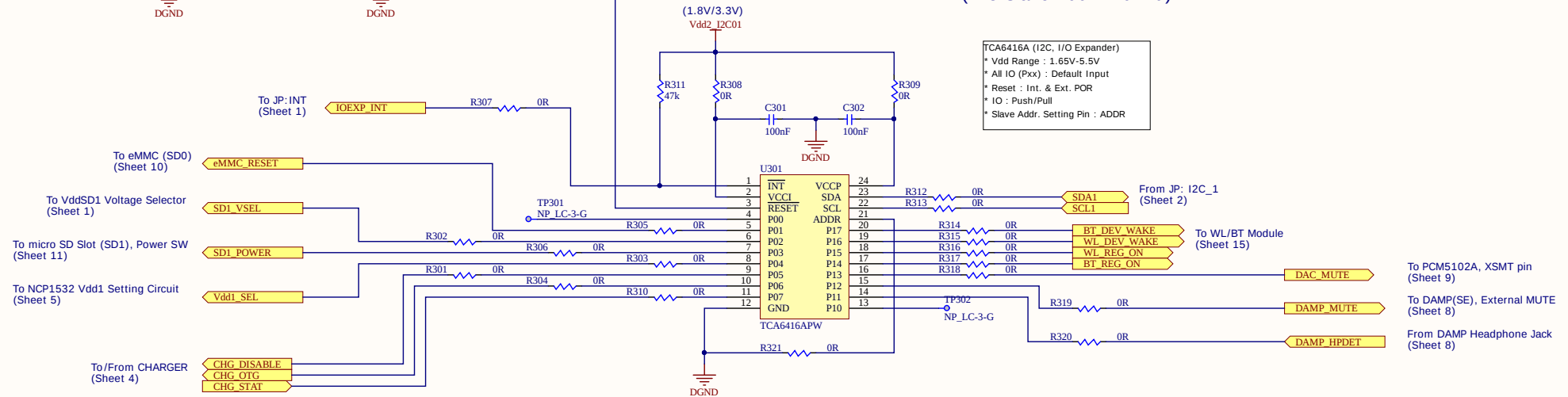
Title LC823455 EVK Power Distribution, 3.3/2.9V DCDC, RTC Power & Detection			
Size A3	Number Ref. No. : 2xx		Revision 2.0
Date: 2019/07/24	Sheet off/17		Hajime Akahori
File: C:\Users\06_2xx\Power Distribution & BDC.doc			

Level Shift for U301 Reset

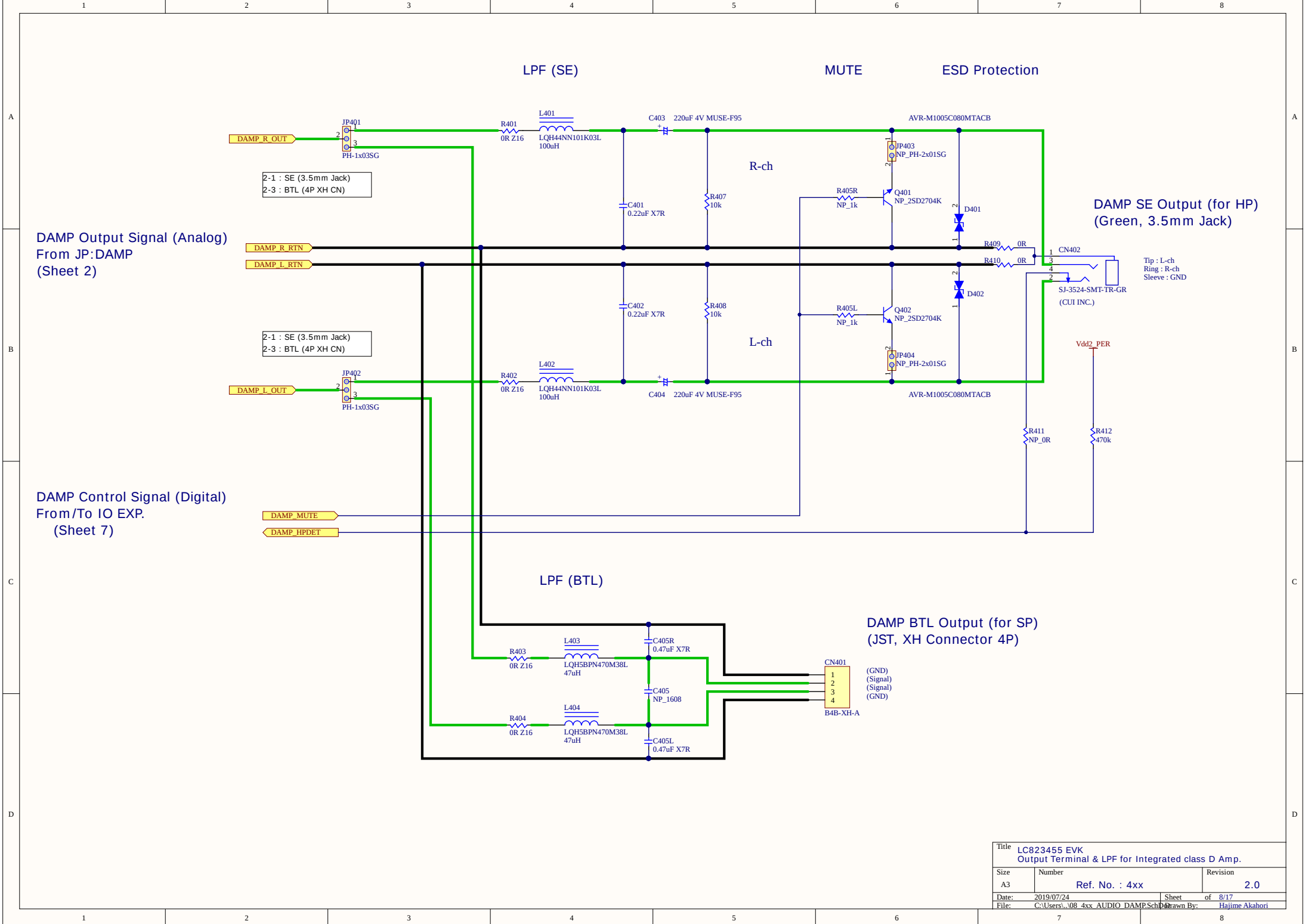


IO Expander (I2C-GPIO) (I2C Slave Addr. : 0x40)

TCA6416A (I2C, I/O Expander)
 * Vdd Range : 1.65V-5.5V
 * All IO (Pxx) : Default Input
 * Reset : Int. & Ext. POR
 * IO : Push/Pull
 * Slave Addr. Setting Pin : ADDR



Title LC823455 EVK I2C IO Expander for Onboard-Device Control			
Size A3	Number Ref. No. : 3xx	Revision 2.01	
Date: 2019/07/24	Sheet of	7/17	
File: C:\Users\..._07_3xx_I2C_IO_Expander_Sch	Drawn By: Hajime Akahori		



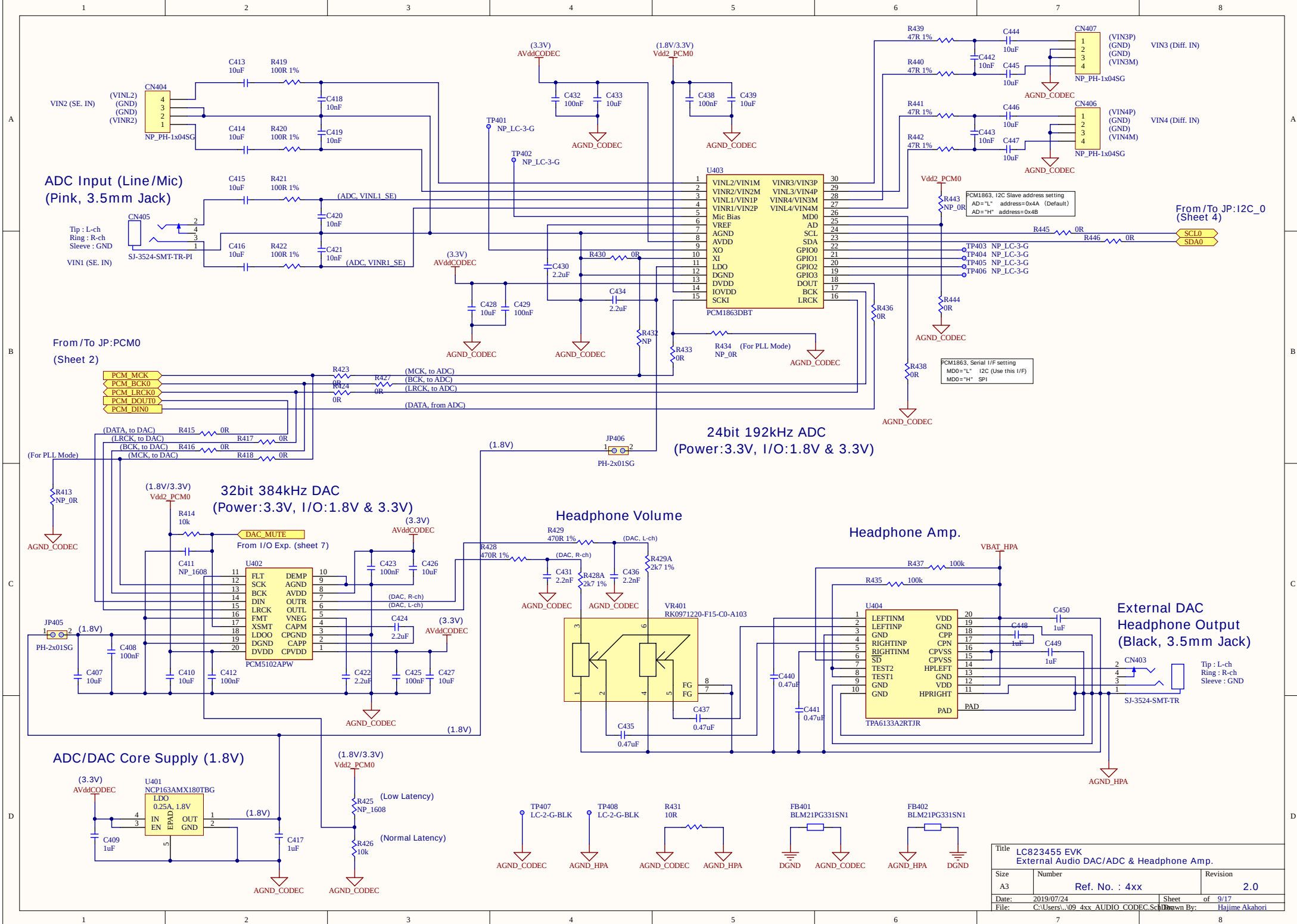
DAMP Output Signal (Analog)
From JP:DAMP
(Sheet 2)

DAMP Control Signal (Digital)
From/To IO EXP.
(Sheet 7)

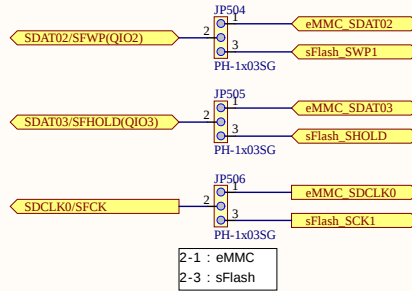
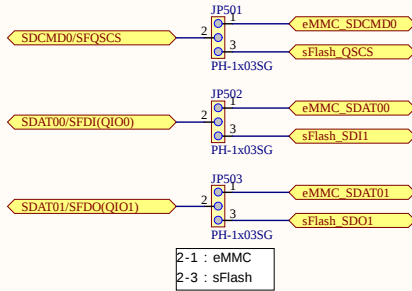
DAMP SE Output (for HP)
(Green, 3.5mm Jack)

DAMP BTL Output (for SP)
(JST, XH Connector 4P)

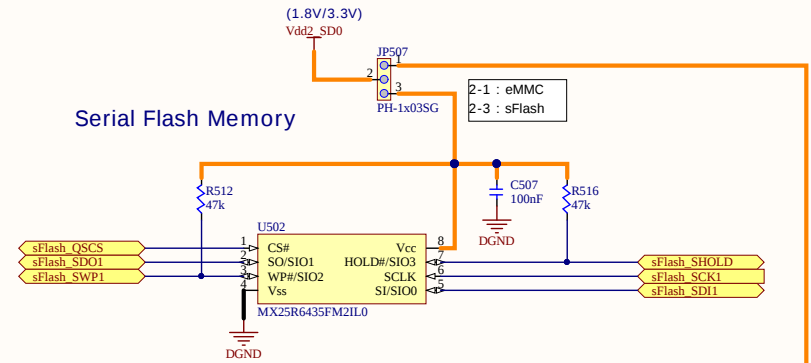
Title		LC823455 EVK Output Terminal & LPF for Integrated class D Amp.	
Size	Number	Revision	
A3	Ref. No. : 4xx	2.0	
Date:	2019/07/24	Sheet	8/17
File:	C:\Users\j08_4xx AUDIO DAMP.Sch	Drawn By:	Hajime Akahori



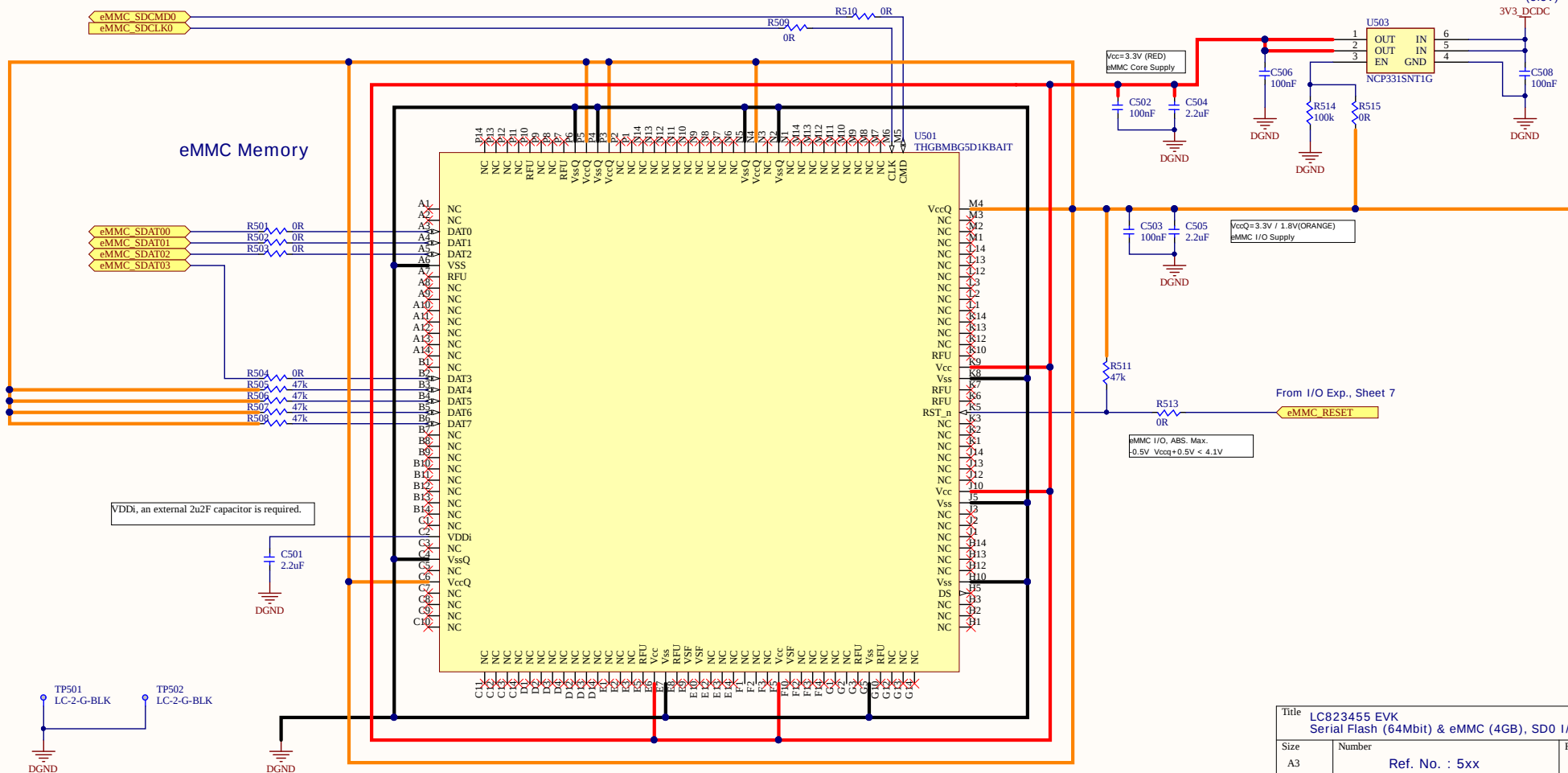
JP501-JP506
SD0 I/F, Strage device Setting
2-1:eMMC
2-3:Serial Flash



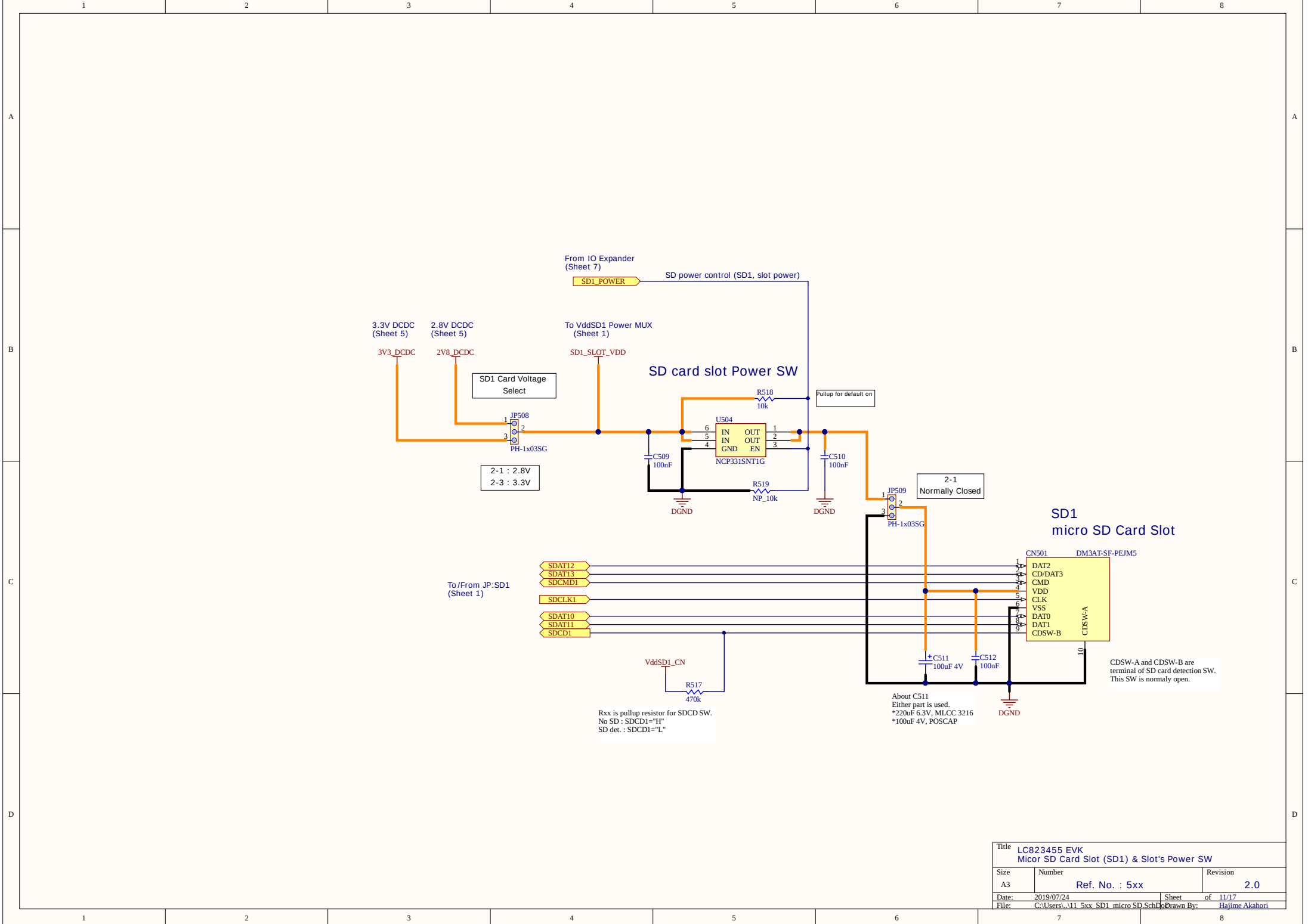
Serial Flash Memory



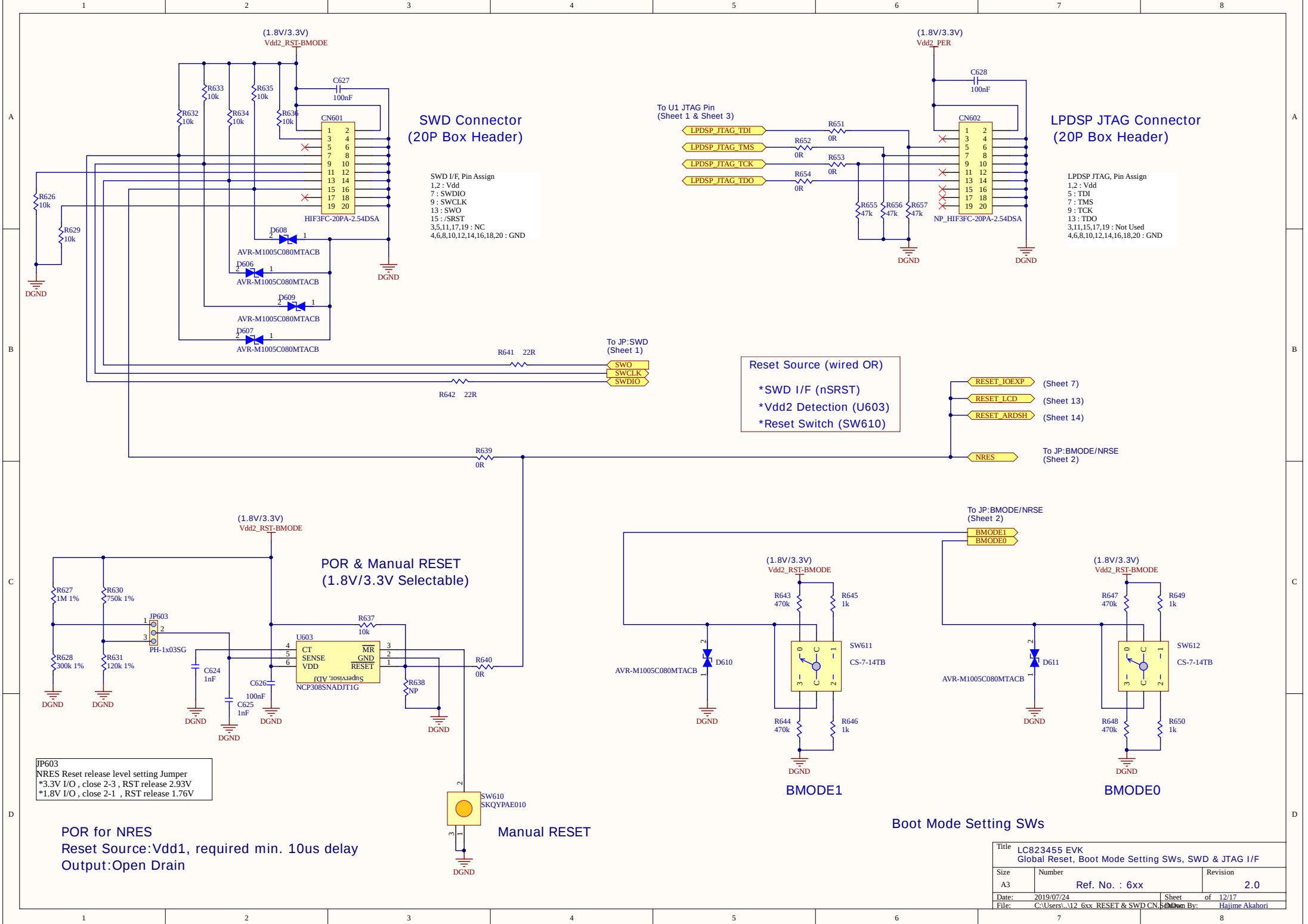
eMMC core, Power SW

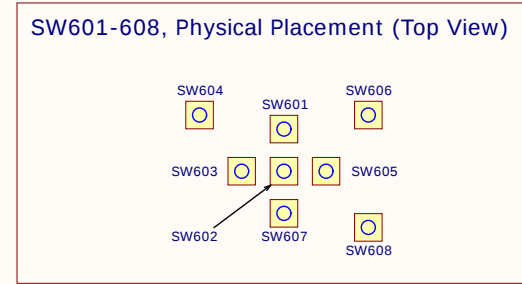
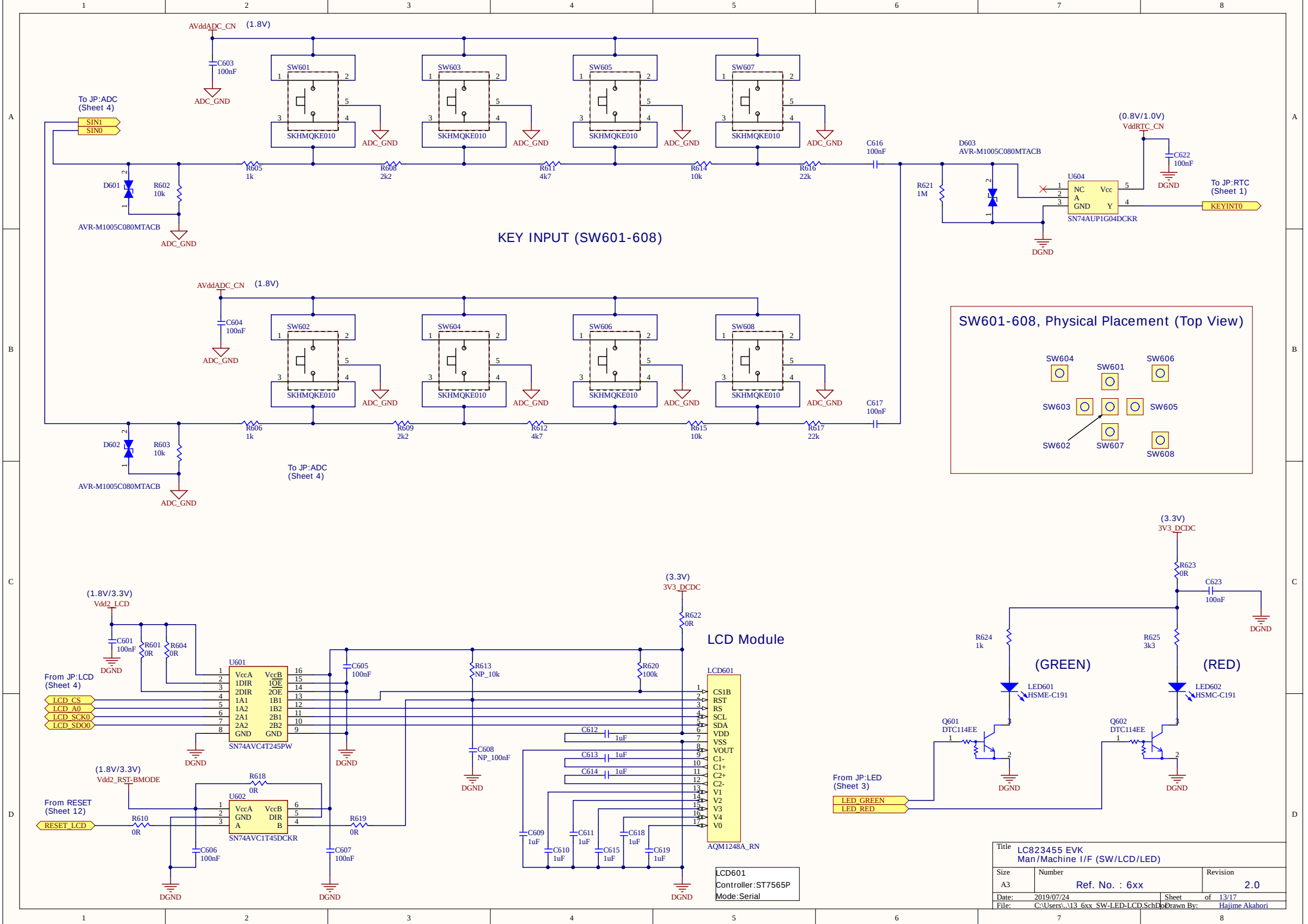


Title LC823455 EVK Serial Flash (64Mbit) & eMMC (4GB), SD0 I/F			
Size A3	Number Ref. No. : 5xx	Revision 2.0	Sheet of 10/17
Date: 2019/07/24	File: C:\Users\10_5xx\SD0 eMMC-sFlash-SD0	Drawn By: Hajime Akahori	

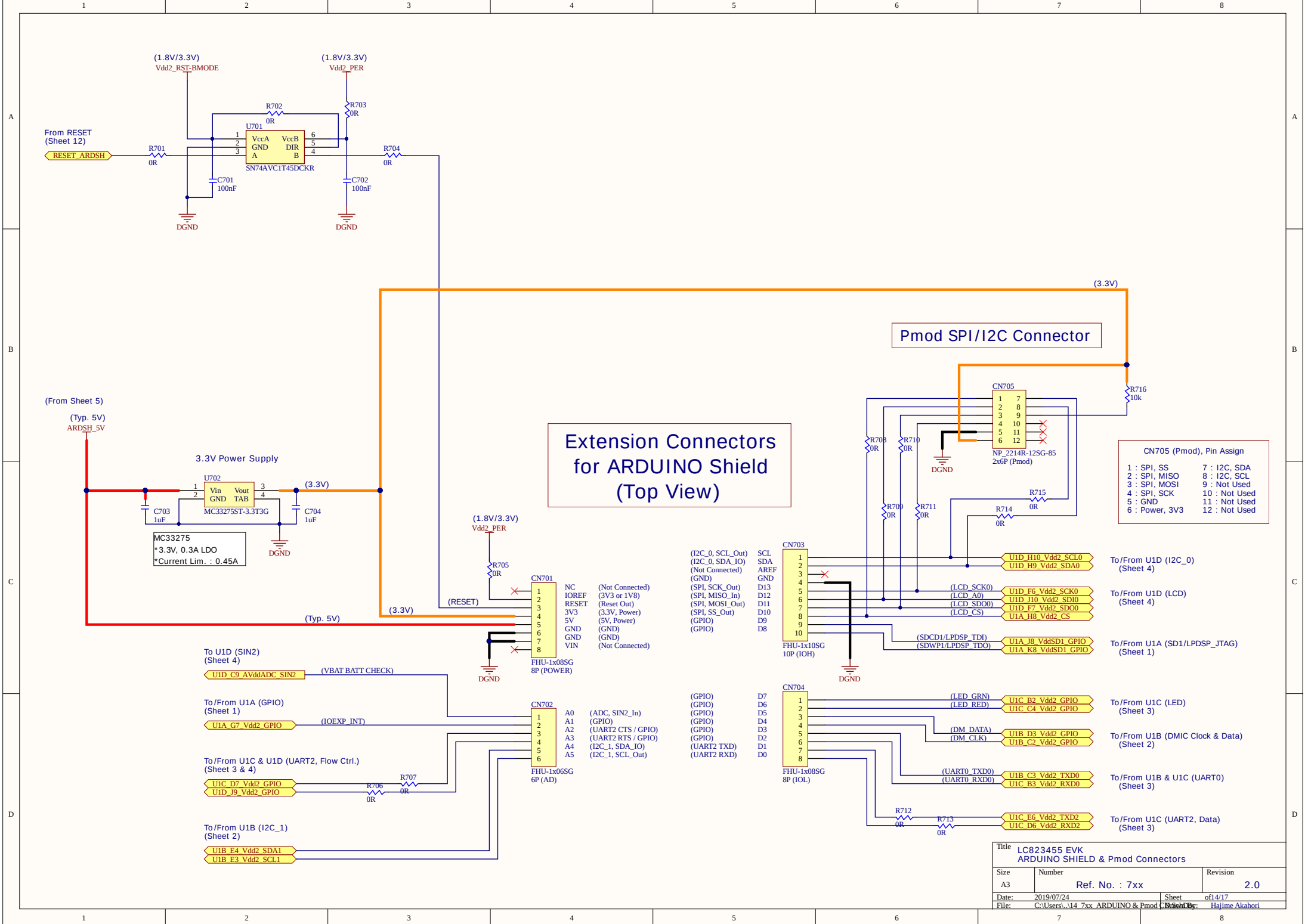


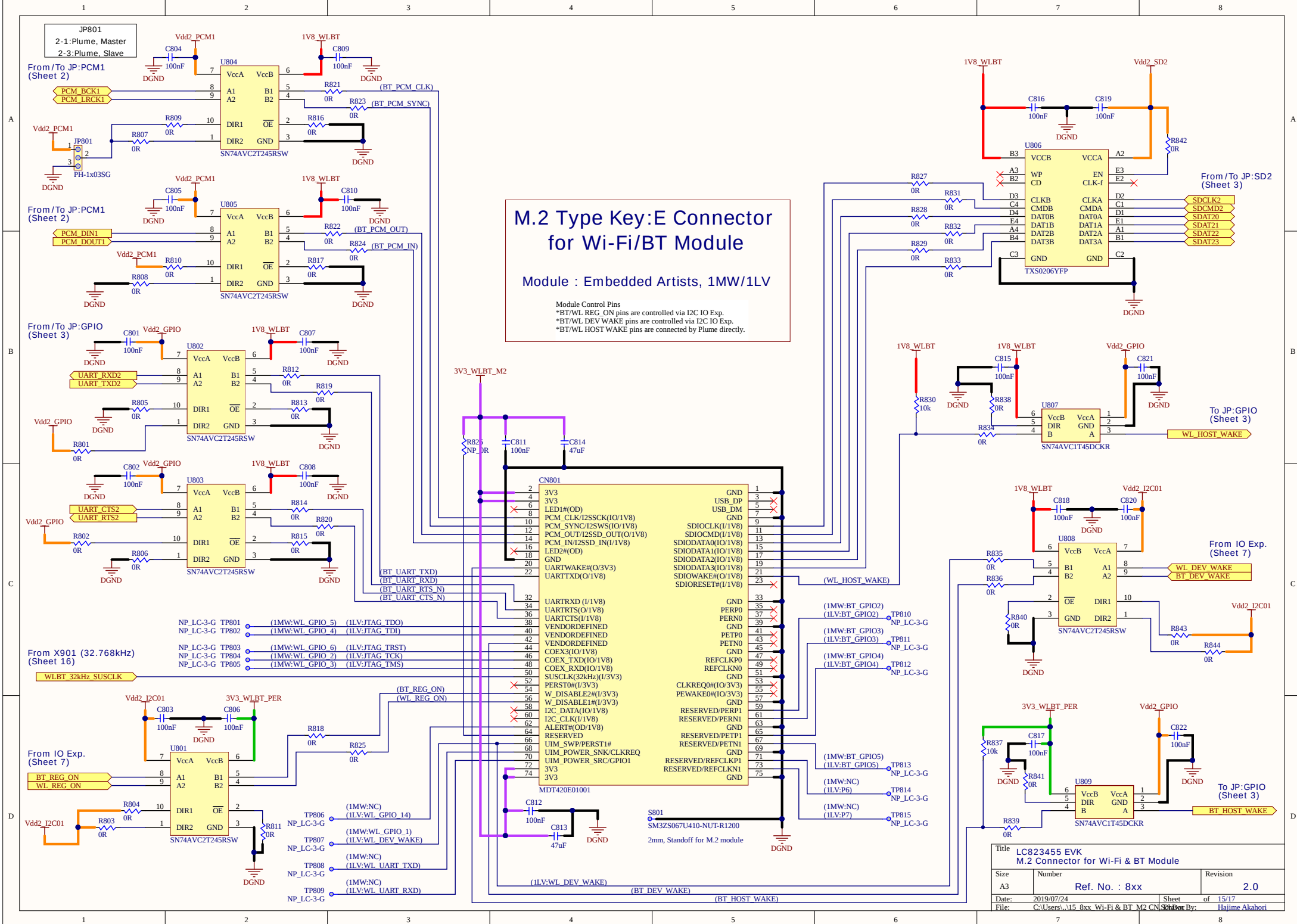
Title			
LC823455 EVK			
Micor SD Card Slot (SD1) & Slot's Power SW			
Size	Number	Revision	
A3	Ref. No. : 5xx	2.0	
Date:	2019/07/24	Sheet	of 11/17
File:		C:\Users\...\\11 5xx SD1 micro SD Slot	
Drawn By:		Hajime Akahori	

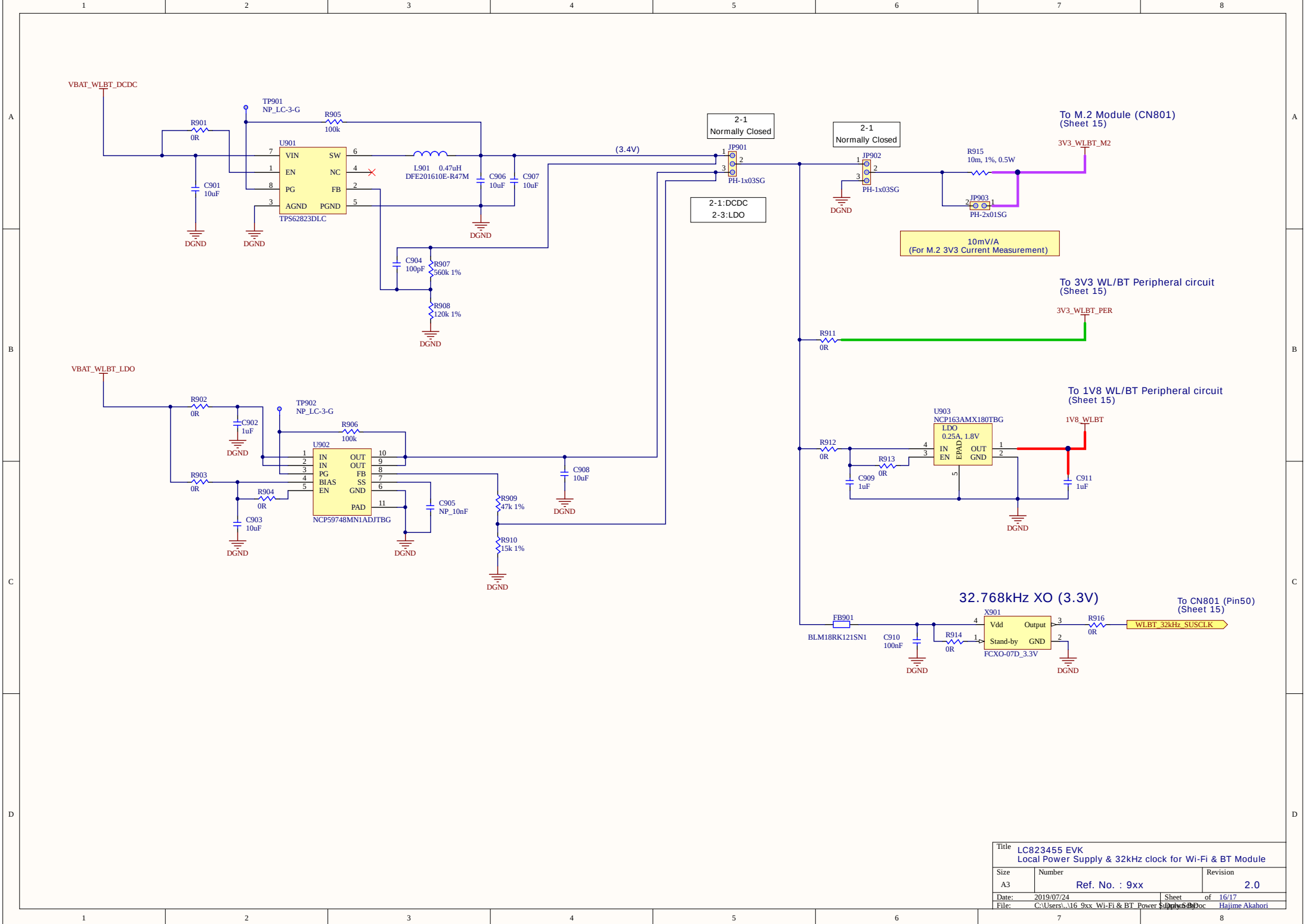




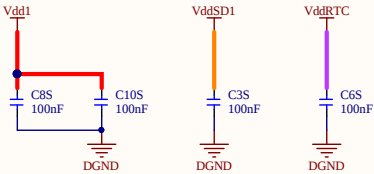
Title			LC823455 EVK	
Man/Machine I/F (SW/LCD/LED)				
Size	Number	Revision		2.0
A3	Ref. No. : 6xx			
Date:	2019/07/24	Sheet	of	13/17
File:	C:\Users\131\31\6xx\SW-LED-LCD-SchDoc\Drawn By:	Hajime Akahori		



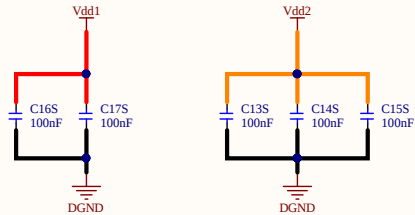




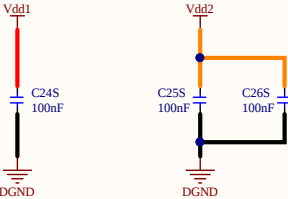
U1A (Pin01-Pin30), Bypass Capacitors



U1C (Pin61-Pin90), Bypass Capacitors



U1B (Pin31-Pin60), Bypass Capacitors



U1D (Pin91-Pin120), Bypass Capacitors

