

Change Log:

Rev 1.0

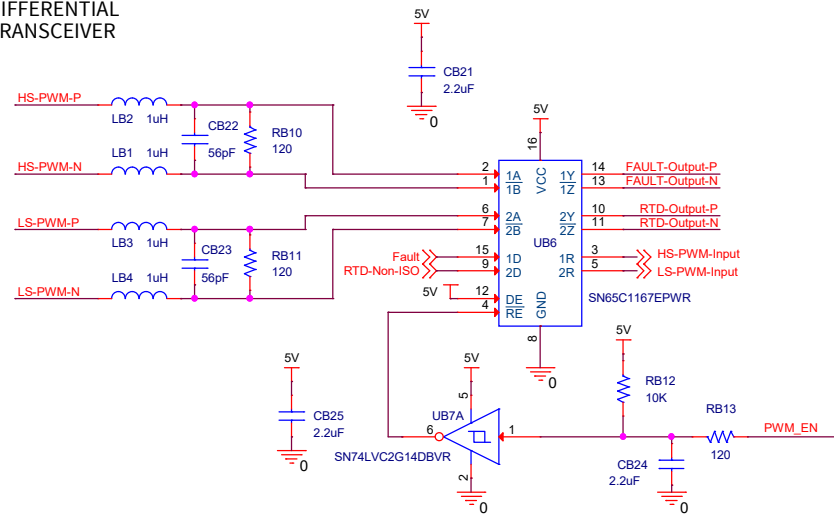
1. Based on the Rev 4 Si828x-AW-GDB.
2. The Si828x-BW-GDB has X-PIN gate connections to support WolfSpeed XM3 module with crossed pin gate connections.
3. Deleted TP307, TP304, TP305, TP301, TP306. Added TP315

Rev 2.0

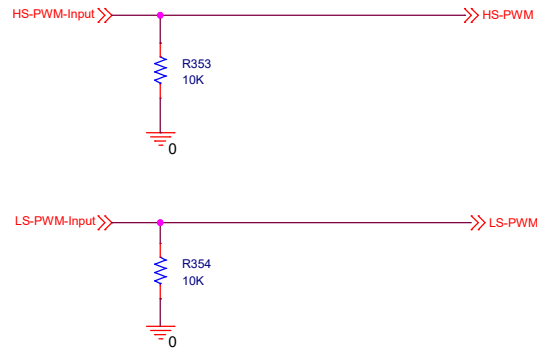
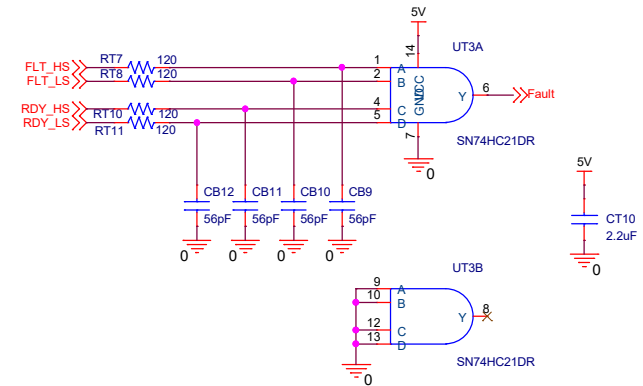
1. Fix polarity issue on the half-bridge gate drivers
2. Implement IN+/IN- interlock for top and bottom gate drivers
3. Replace U203 ST LDO (LDK320AM50R) with TI LP2985AIM5X-5 to fix LDO failure at 78 C
4. Replace Schottky diodes for market availability
5. Update Si8285 part number
6. Adding GND test point

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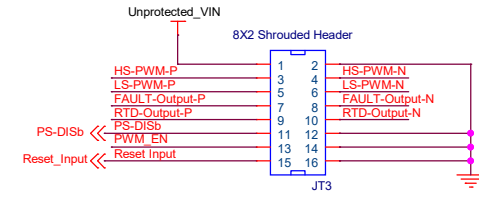
DIFFERENTIAL TRANSCIVER

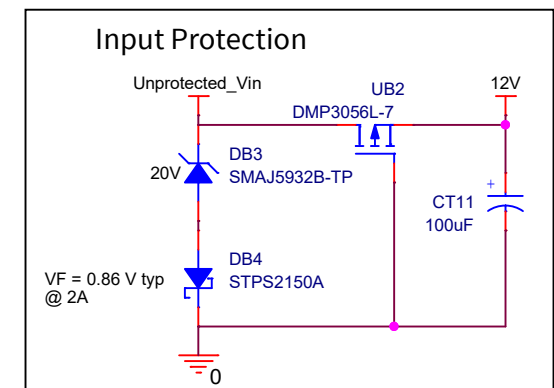
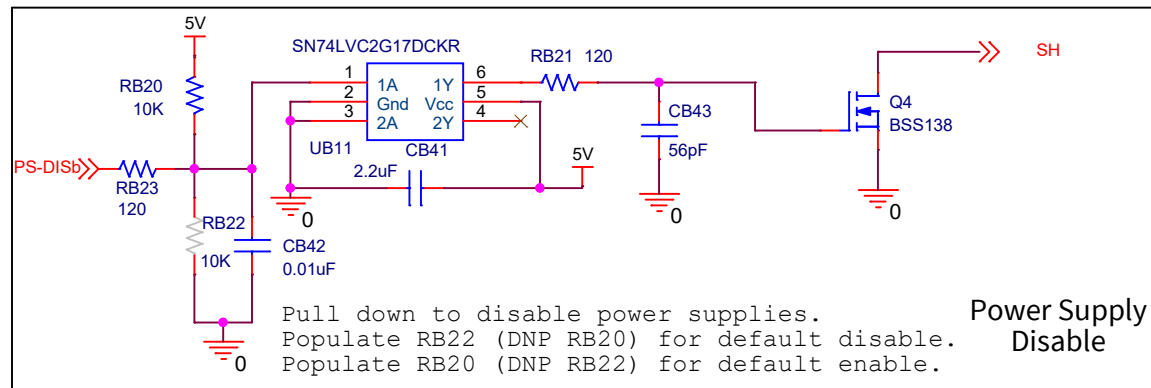
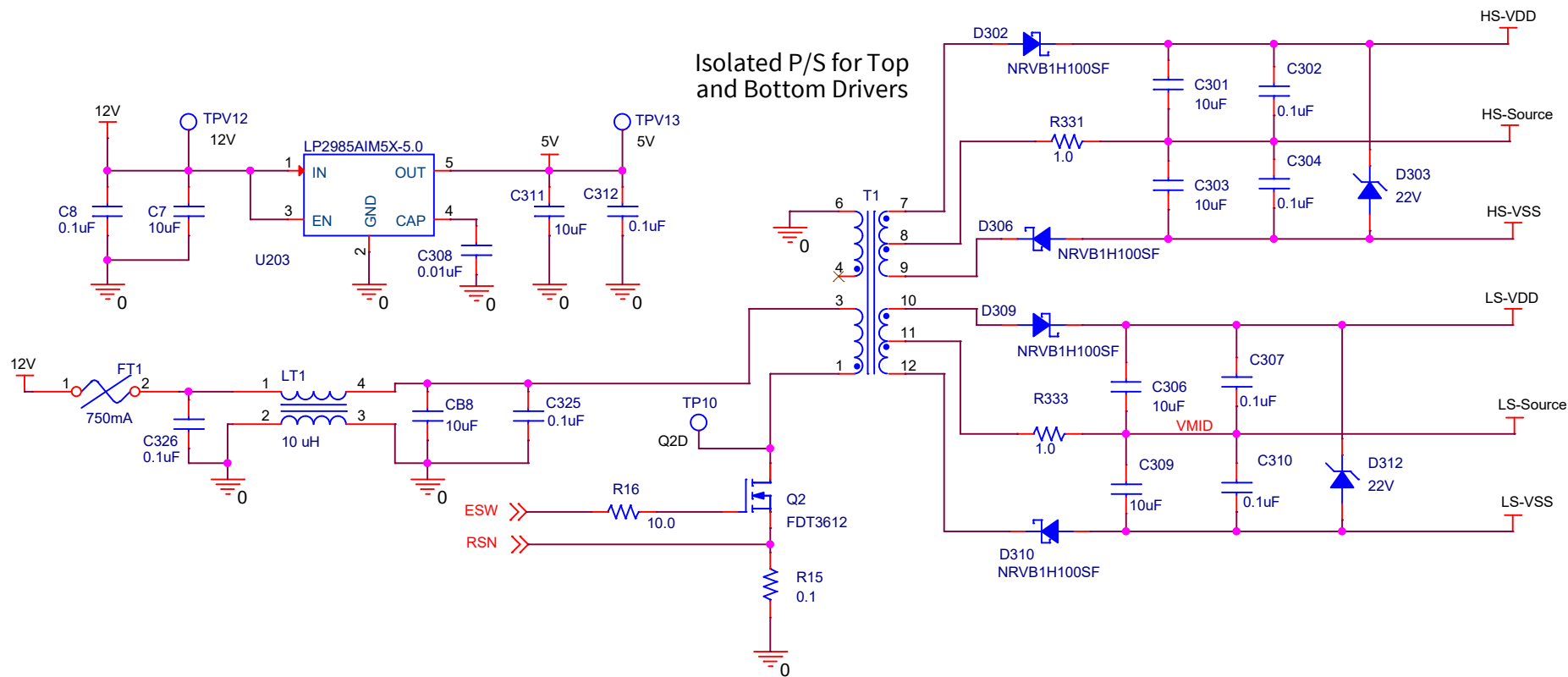


FAULT COMBINATION

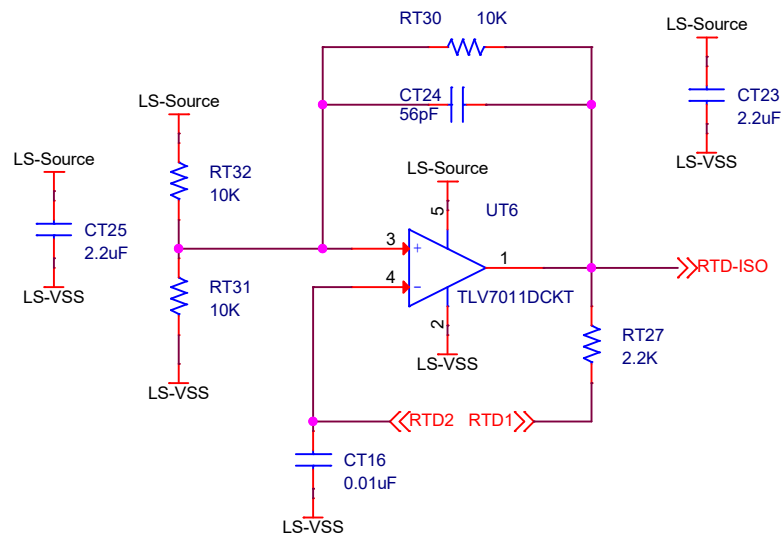


I/O

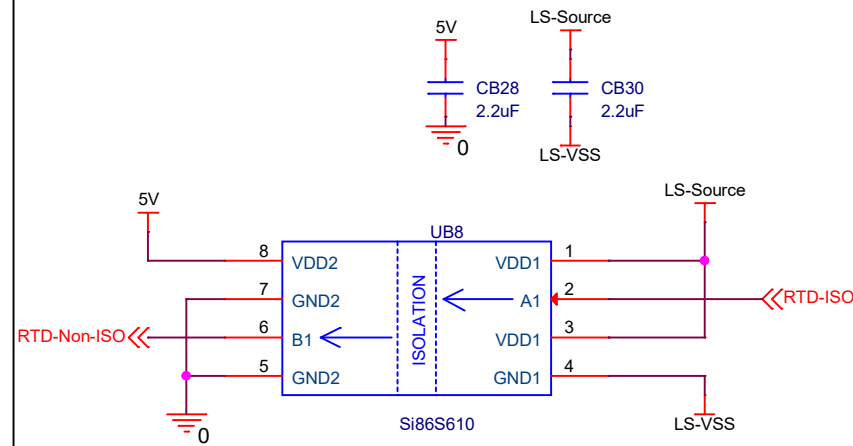




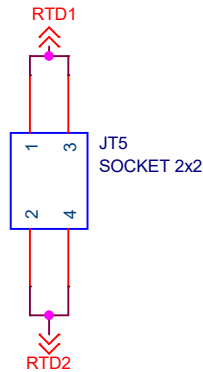
Resistance to Frequency
Converter



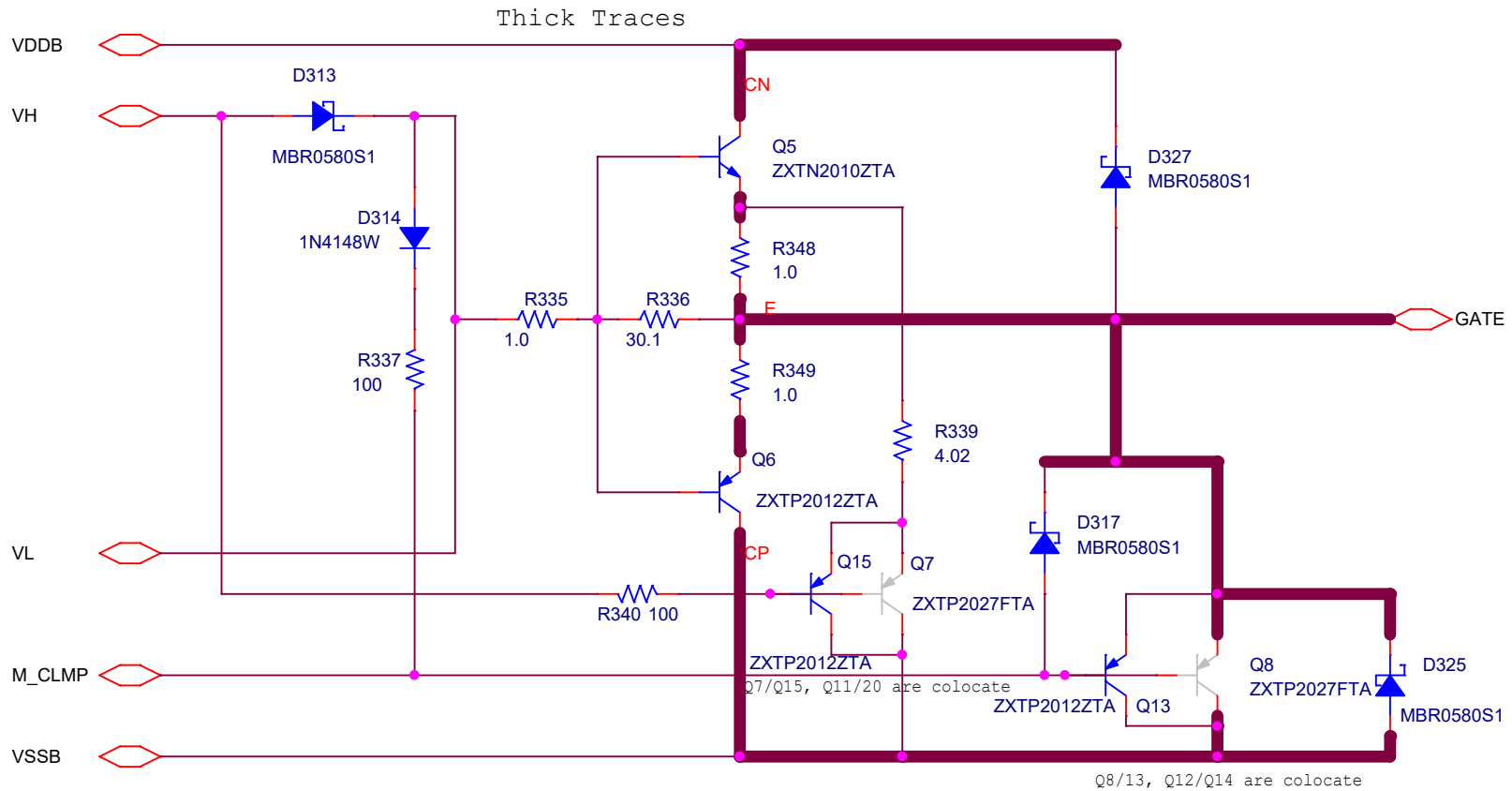
Digital Isolator



RTD (NTC)
Connector

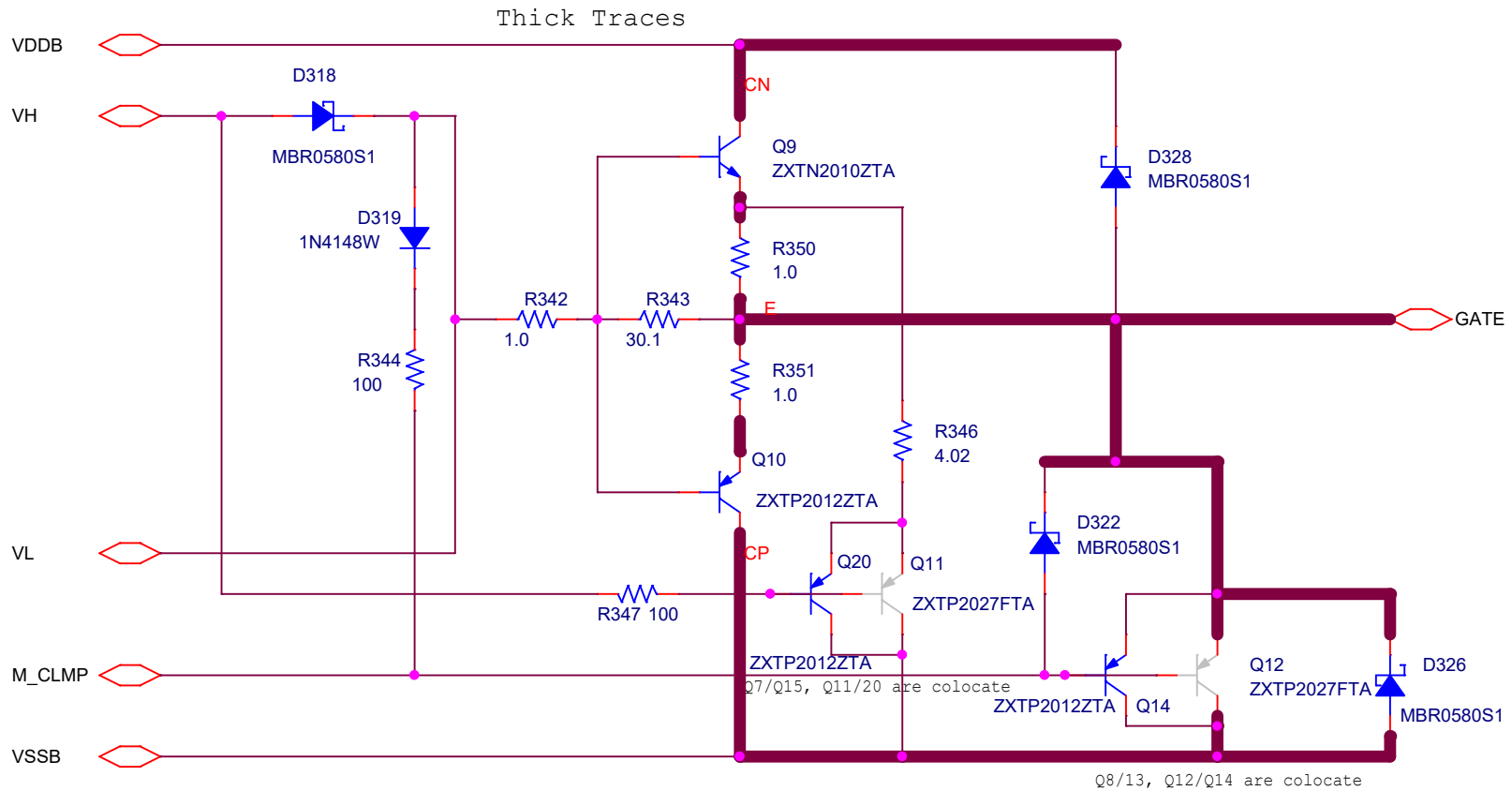


0°C	= 13491	= 4.6 kHz
25°C	= 4700	= 10.3 kHz
50°C	= 1928	= 17.1 kHz
75°C	= 898	= 22.8 kHz
100°C	= 464	= 26.4 kHz
125°C	= 260	= 28.3 kHz
150°C	= 156	= 29.5 kHz
175°C	= 99	= 30.1 kHz



Populate on Bottom of card

 200 W Cesar Chavez Austin, TX 78701			
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Populate on Bottom of card

 200 W Cesar Chavez Austin, TX 78701			
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