Secure Authentication Reference Design

MAXREFDES43

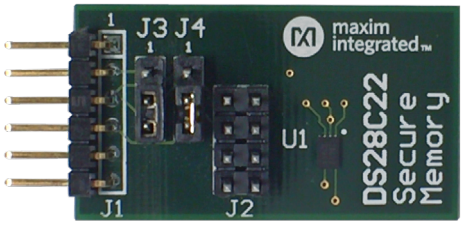
Product Overview

05-23-2022

For the most up-to-date information, visit www.mouser.com or the supplier's website.

Description

Maxim Integrated MAXREFDES43 Secure Authentication Reference Design is an I²C-based authentication reference design built to authenticate peripherals to Xilinx® FPGAs. The FPGA executes a challenge-response sequence with the DS28C22 to ensure the authenticity of a module, peripheral, or subsystem. The DS28C22 communicates on an I²C bus, providing a standard communication interface. The MAXREFDES43 is equipped with a Pmod™ connector for immediate testing using an Avnet ZedBoard™. The simplicity of this design enables rapid adoption into any peripheral requiring security.



Features

- SHA-256 authentication
- I²C interface
- Example source code
- Crypto-strong authentication
- Fast performance with hardware acceleration
- Pmod-compatible form factor

Applications

- Crypto-strong authentication
- Fast performance with hardware acceleration

Mouser Part Number

View Part

To learn more, visit https://www.mouser.com/new/maxim-integrated/maxim-maxrefdes43/