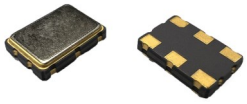
PLETRONICS VHA6 Series
CMOS Clock Oscillator



VHA6
7.0 x 5.0 x 1.7 mm
LCC Ceramic Package

Features

- Pletronics' VHA6 Series is a quartz crystal controlled precision square wave oscillator
- CMOS Output
- Vcontrol on pin 1
- Enable/Disable Function on pin 2
- Low Jitter
- 3.3V nominal Supply Voltage
- 1-108 MHz Frequency Range

Applications

Driving A/Ds, D/As, FPGAs
Digital Video
Ethernet, GbE
Medical
Storage Area Networking
COTS
Broad Band Access
SONET/ SDH/ DWDM
Base Stations/ Picocell
Test & Measurement

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range [2]	1	-	108	MHz	Consult factory for other options
Frequency Stability [1,2]	-	-	±50	ppm	Not specified if APR is specified
Operating Temperature Range [2]	-40	-	+105	°C	(-40 to +85°C only ≥ 80 MHz)
Supply Voltage [2] V_{CC}	2.97	3.30	3.63	V	3.3V ± 10%
Supply Current I_{CC} (1-80MHz)	-	3	5	mA	C_{LOAD} = 15 pF
Supply Current I_{CC} (>80-108MHz)	-	16	20	mA	C_{LOAD} = 15 pF
Output Waveform		CMOS			
Duty Cycle	45	-	55	%	At 50%Vcc level
Output V_{HIGH} (for I_{OH} -3mA)	V_{CC} - 0.4	-	-	V	
Output V_{LOW} (for I_{OH} +3mA)	-	-	0.4	V	See Load Circuit
Output T_{RISE} and T_{FALL}	-	4	6	ns	C_{LOAD} = 15 pF, 10% to 90% of V_{CC}, See Load Circuit
Startup Time	-	1.5	10	ms	Time for output to reach specified frequency
$V_{DISABLE}$	-	-	30	%	Of V_{CC} applied to Pad 2
V_{ENABLE}	70	-			
Startup Time	-	1.5	10	ms	Time for output to reach specified frequency
Enable Time	-	-	250	ns	Time for output to reach a logic state
Disable Time	-	-	250	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	50	-	-	kΩ	To V_{CC}
Control Voltage	0	1.65	3.3	V	
Linearity	-	-	±10	%	
Vcontrol Input Impedance	5	-	-	MΩ	Pad 1 to ground
Modulation Bandwidth	15	20	-	kHz	@-3dB
Output Leakage $V_{OH} = V_{CC}$ V_{OL} = Gnd	- -10	-	+10 -	µA	Pad 2 low
Phase Noise 10 Hz 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 10 MHz	-	-69 -95 -121 -140 -149 -157 -160	-	dBc/Hz	25°C ± 2°C at 100 MHz
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 2 E/D open circuit
[1] Includes supply change, load changes, aging at 25°C for 1 year, shock, vibration and temperatures.
[2] Specified by part number

Part Number					
Series Model	**Lowest Specified Operating Temp**	**Highest Specified Operating Temp**	**Stability in ppm (*10)**	Pullability in ppm	**Frequency in MHz**
VHA6029036	**E**	**G**	**500**	**100**	**-80.0M**
Series (Part type, logic, and package)	**C** = 0°C **E** = -10°C **G** = -20°C **J** = -30°C **L** = -40°C	**G** = +70°C **J** = +80°C **K** = +85°C **P** = +105°C	**000** = APR **250** = ±25ppm **500** = ±50ppm (typical values shown)	**050** = ±50ppm min **100** = ±100ppm min (typical values shown)	1.0 - 108.0 MHz

Device Marking

PLE VHA6 *FF.FFF*M • *YMDxxx*	VH*YWWXX* *FF.FFF*M · P*XXXXX*

PLE or P = Pletronics
VH or VHA6 = Part Series
FF.FFF = Frequency in MHz
YMD or *YWW* = Date Code (see table below)
All other markings are internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

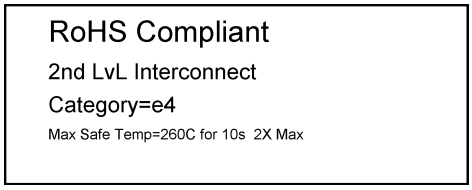
Code	3	4	5	6	7	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2023	2024	2025	2026	2027	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

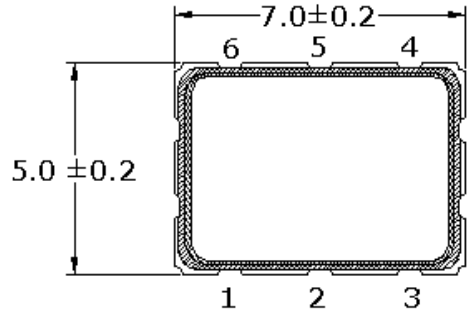
RoHs Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial



Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.16 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

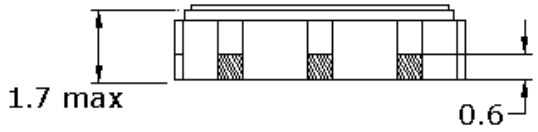
Mechanical Dimensions (mm)

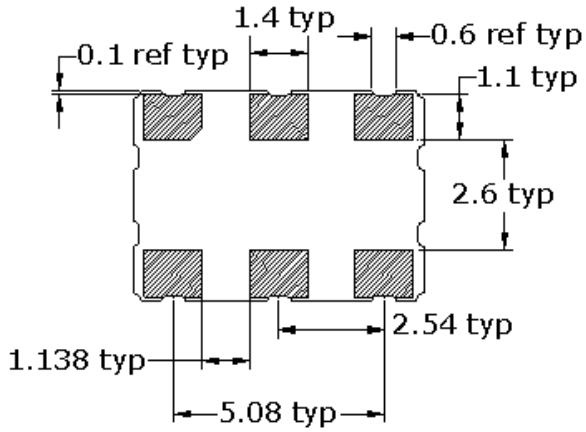


Pad Connections

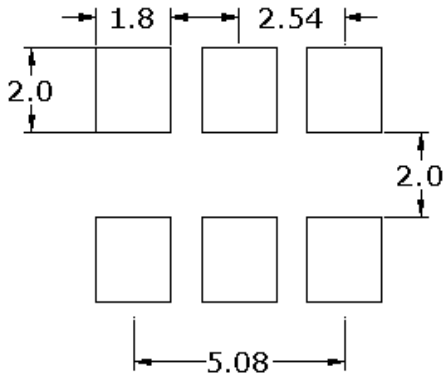
Pad	Function
1	Voltage Control
2	Enable/Disable
3	Ground
4	Output
5	No Connect
6	Vcc

ENABLE/DISABLE	
Pad 2	Output
Logic 1/Open	Active
Logic 0/Gnd	Disabled/Tristate





Dimensions in mm



Solder pad layout

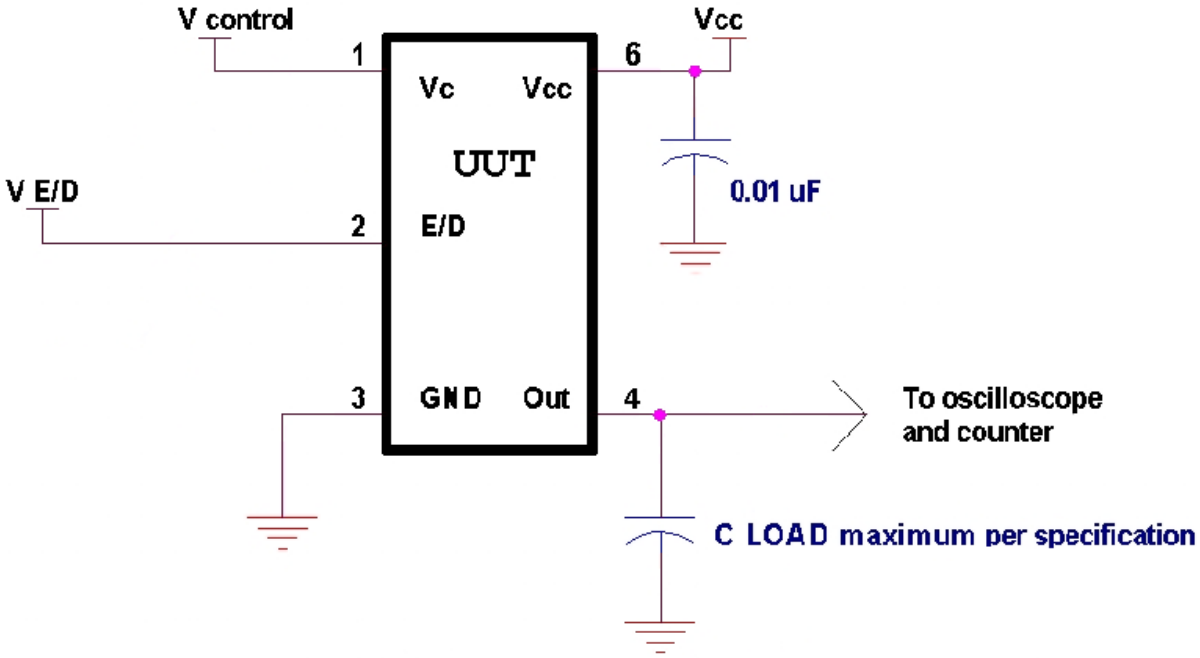
Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

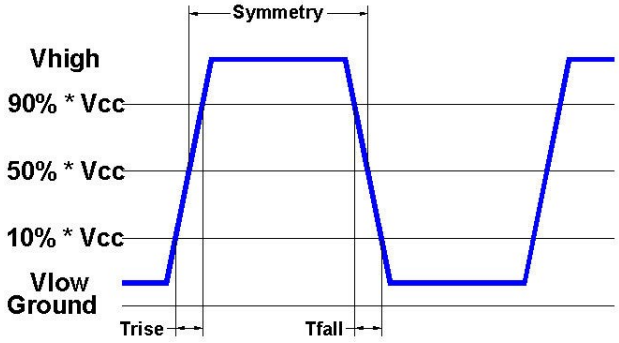
Pad Layout
Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

Electrical Test / Load Circuit





Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

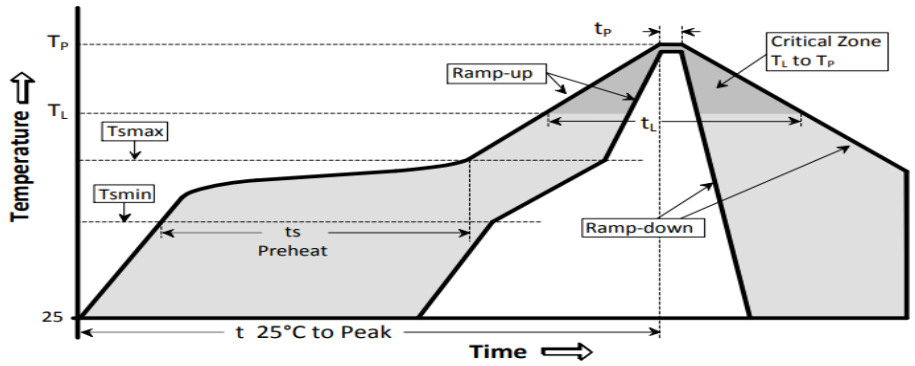
Thermal Characteristics:
The maximum die or junction temperature is 150°C

Absolute Maximum Ratings

Parameter	Unit
V_{CC} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V_{CC} + 0.5V
Vo Output Voltage	-0.5V to V_{CC} + 0.5V

Reflow Cycle

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"

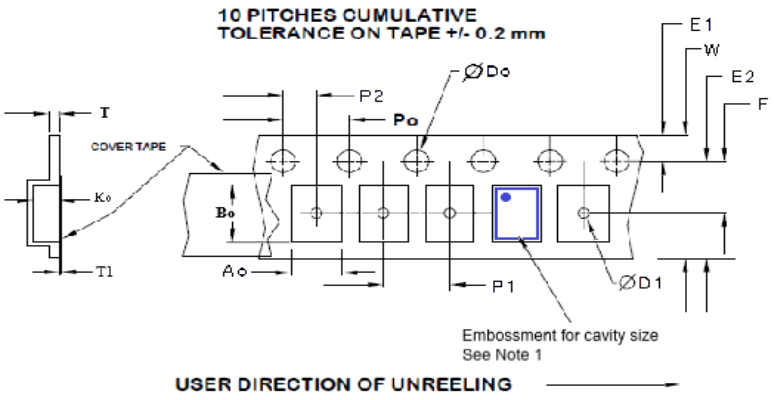


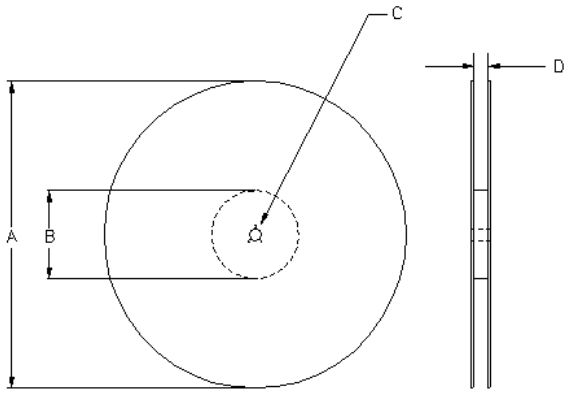
Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	(Ts_{max} to T_P)	3°C / second max	°C / s
Ramp down Rate	T_{cool}	6°C / second max	°C / s
Time 25°C to Peak Temperature	$T_{to\text{-}peak}$	8 minutes max	min
Preheat			
Temperature min	Ts_{min}	150	°C
Temperature max	Ts_{max}	200	°C
Time Ts_{min} to Ts_{max}	ts	60 – 180	sec
Soldering above liquidus			
Temperature liquidus	T_L	217	°C
Time above liquidus	t_L	60 – 150	sec
Peak temperature			
Peak Temperature	Tp	260	°C
Time within 5°C of peak temperature	tp	20 – 40	sec

The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 16mm tape, 8mm pitch.





Tape Variable Dimensions Table 2									
Part Size	Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko	Qty/reel standard
7050	16mm	14.25	7.5 ±0.05	8.0 ±0.1	16.3	5.56±0.1	7.85±0.1	2±0.1	1K

Dimensions in mm Drawings Not to scale
Note 1: Embossed cavity to conform to EIA– 481-B

Tape Constant Dimensions Table 1							
Tape Size	Do	D1 typ	E1	Po	P2	T typ	T1 max
16mm	1.5 +0.1 -0.0	1.5	1.75 ±0.1	4.0 ±0.1	2.0 ±0.1	0.3	0.1

Reel Dimensions (may vary) Table 3						
	A		B		C	D
Reel Size	Inches	mm	Inches	mm	mm	mm
7	7.0	177.8	2.50	63.5	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0
10	10.0	254.0	4.00	101.6		
13	13.0	330.2	3.75	95.3		

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Contacting Pletronics Inc.

Pletronics, Inc.
19013 36th Ave. West
Lynnwood, WA 98036-5761
U.S.A.

Tel: 425.776.1880
Fax: 425.776.2760
email: ple-sales@pletronics.com
URL: www.pletronics.com

