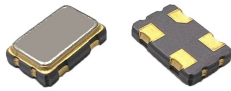




PLETRONICS SM55T Series 3.3V CMOS Clock Oscillator



SM55TV
5.0 x 3.2 x 1.2 mm
LCC Ceramic Package

Features

- Pletronics' SM55T Series is a quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitter
- 3.3V nominal Supply Voltage
- 1.0 - 165 MHz Frequency Range

Applications

Driving A/Ds, D/As, FPGAs
Digital Video
Ethernet, GbE
Medical
Storage Area Networking
COTS
Broad Band Access
SONET/ SDH/ DWDM
Base Stations/ Picocell
Test & Measurement

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range ²	1.0	-	165	MHz	Consult factory for other options
Frequency Stability ² ± 20 = 20* , ± 25 = 44 , ± 50 = 45	±20	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures. *limited frequencies, see page 3
Operating Temperature Range ²	-10 -20 -40	- - -	+70 +70 +85	°C	Standard range Extended range C option Extended range E option
Supply Voltage ^{1,2} (V _{CC})	2.97	3.30	3.63	V	3.3V ± 10%
Output Waveform	CMOS				
Duty Cycle	45	-	55	%	At 50% point of V _{CC}
Output V _{HIGH} (V _{OH})	90	-	-	%	of V _{CC}
Output V _{LOW} (V _{OL})	-	-	10	%	of V _{CC}
Startup Time	-	-	10	ms	Time for output to reach specified frequency
V _{DISABLE} (V _{IL})	-	-	30	%	Of V _{CC} applied to Pad 1
V _{ENABLE} (V _{IH})	70	-	-		
Enable Time	-	-	100	ns	Time for output to reach a logic state
Disable Time	-	-	100	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	30	70	150	KΩ	To V _{CC} , Pin 1 open or ≥0.7V _{CC}
Output Leakage V _{OUT} = V _{CC} V _{OUT} = 0V	- -10	-	+10 -	μA	Pad 1 low, device disabled
Standby Current	-	-	10	μA	
Phase Jitter	-	-	0.6	ps RMS	12kHz to 20MHz from specified frequency
	-	-	2.5	ps RMS	10Hz to 1MHz from specified frequency
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit

¹ Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



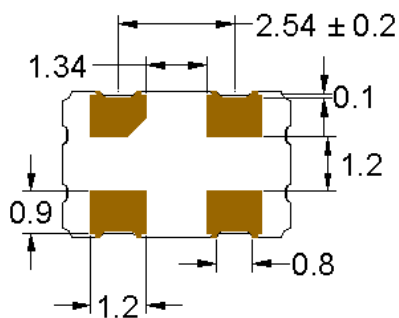
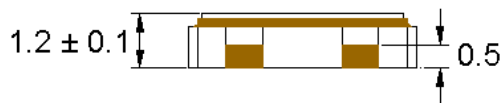
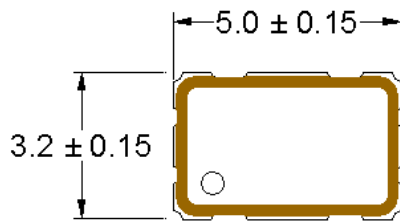
PLETRONICS SM55T Series 3.3V CMOS Clock Oscillator

Electrical Characteristics

Parameter	Typ	Max	Unit	Condition	
Output T _{RISE} and T _{FALL}	-	5	nS	< 35 MHz	C _{LOAD} = 15 pF 10% to 90% of V _{CC} See Load Circuit
	-	3		≥ 35 MHz and < 70 MHz	
	-	2.5		≥ 70 MHz and < 110 MHz	
	-	2		≥ 110 MHz	
Parameter	Typ	Max	Unit	Condition	
V _{CC} Supply Current (I _{CC})	-	9	mA	< 8 MHz	C _{LOAD} = 15 pF
	-	11		≥ 8 MHz and < 16 MHz	
	-	17		≥ 16 MHz and < 35 MHz	
	-	26		≥ 35 MHz and <70 MHz	
	-	50		≥ 70 MHz and <120 MHz	
	-	70		≥ 120 MHz	

Specifications with Pad 1 E/D circuit open

Mechanical Dimensions

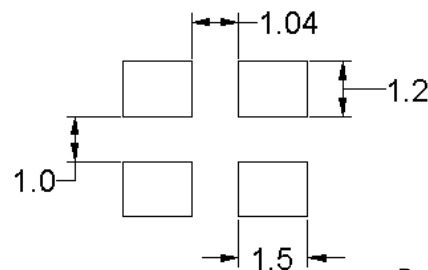


Pad Connections

Pad	Function
1	Enable/Disable
2	Ground
3	Output
4	V_{CC}

ENABLE/DISABLE

Pad 1	Output
$V_{IH}/Open$	Active
V_{IL}/Gnd	Disabled/Tristate



Pad Layout

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

Dimensions in mm

Contacts (pads): Gold (0.3 to 1.0 μ m) over Nickel (1.27 to 8.89 μ m)

For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



PLETRONICS SM55T Series 3.3V CMOS Clock Oscillator

Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V_{CC}	Frequency in MHz	Optional T&R Packaging code
SM55	45	T	E	V	- 125.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V $\pm 10\%$	1.0 - 165 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)

* Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

Device Marking

Pff.fff M • YMDxx	P ff.fff M • YYWWxx	P5xYWWx • ff.fff M	PLE SM55 ff.fff M • YMDxx	5xYWWxx ff.fff M • PLExx
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P or PLE = Pletronics
ff.fff = Frequency in MHz
YMD or YWW or YYWW = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3	4	5	6	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2022	2023	2024	2025	2026	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

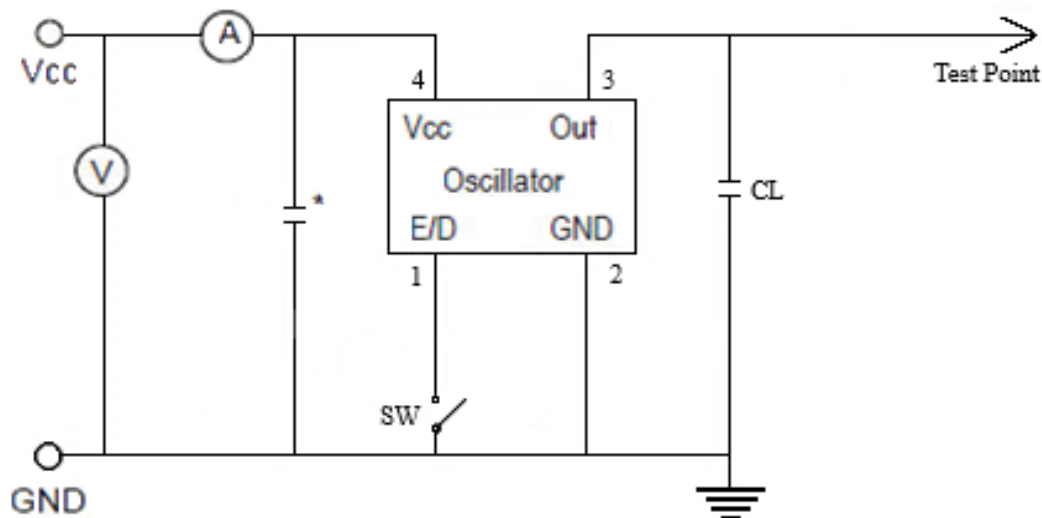
P/N:	
	PLE Part Number
Customer P/N:	
	12345678
Qty:	
	1000
D/C	
	2A1
MSL:	1

RoHS Compliant
2nd Lvl Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

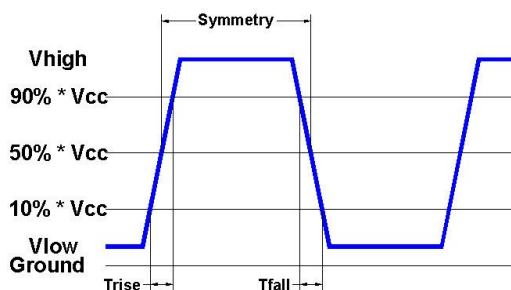
Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBBs, PBDE's
Weight of the Device: 0.06 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

Electrical Test / Load Circuit



Notes:

CL : 15 pF Includes the input capacitance of oscilloscope
* 0.01~0.1 μ F external by-pass filter is recommended



Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

Absolute Maximum Ratings

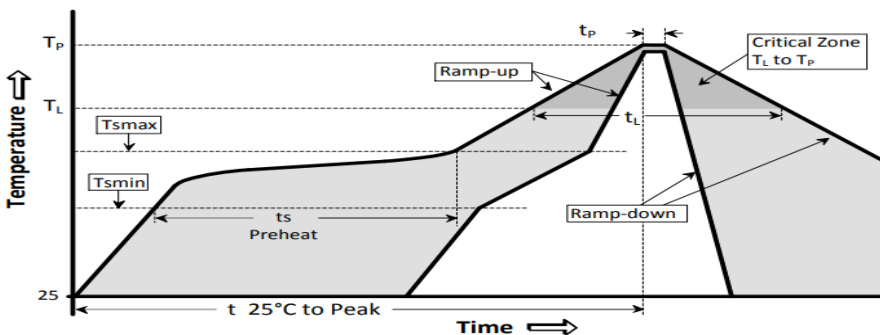
Parameter	Unit
V_{CC} Supply Voltage	-0.3V to +4.0V
V_i Input Voltage	-0.3V to $V_{CC} + 0.3V$
V_o Output Voltage	-0.3V to $V_{CC} + 0.3V$

Thermal Characteristics:

The maximum die or junction temperature is 150°C

Reflow Cycle

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"

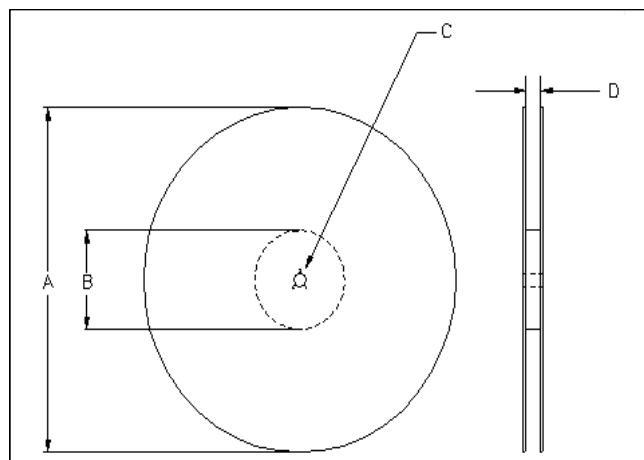
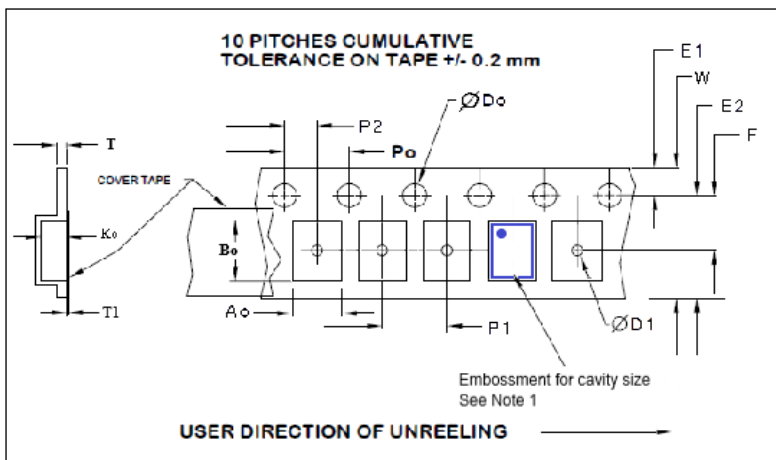


The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	(T _{Smax} to T _P)	3°C / second max	°C / s
Ramp down Rate	T _{cool}	6°C / second max	°C / s
Time 25°C to Peak Temperature	T _{to-peak}	8 minutes max	min
Preheat			
Temperature min	T _{Smin}	150	°C
Temperature max	T _{Smax}	200	°C
Time T _{Smin} to T _{Smax}	t _s	60 – 180	sec
Soldering above liquidus			
Temperature liquidus	T _L	217	°C
Time above liquidus	t _L	60 – 150	sec
Peak temperature			
Peak Temperature	T _P	260	°C
Time within 5°C of peak temperature	t _P	20 – 40	sec

Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm (or 16mm) tape, 8mm pitch.



Tape Variable Dimensions Table 2							
Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1
16mm	14.25	7.5 ±0.05	8.0 ±0.1	16.3	3.6±0.1	5.4±0.1	1.4±0.1

Dimensions in mm Drawing Not to scale
Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1							
Tape Size	Do	D1 min	E1	Po	P2	T max	T1 max
12mm	1.5 +0.1 -0.0	1.5	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.3	0.1
16mm		1.5			2.0 ±0.1		

Reel Dimensions (may vary) Table 3						
	A		B		C	D
Reel Size	Inches	mm	Inches	mm	mm	mm
7	7.0	180	2.50	60	13.0	Tape size +0.4
13	13.0	330	3.75	100	+0.5 -0.2	+2.0 -0.0



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