



# PLETRONICS SM44J Series 2.5V CMOS Clock Oscillator



SM44JW  
3.2 x 2.5 x 0.95 mm  
LCC Ceramic Package

## Features

- Quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitter
- 2.5V nominal Supply Voltage
- 1.25-170 MHz Frequency Range

## Applications

Driving A/Ds, D/As, FPGAs  
Digital Video  
Ethernet, GbE  
Medical  
Storage Area Networking  
COTS  
Broad Band Access  
SONET/ SDH/ DWDM  
Base Stations/ Picocell  
Test & Measurement

## Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition	
Frequency Range <sup>2</sup>	1.25	-	170	MHz	Consult factory for other options	
Frequency Stability <sup>2</sup> ± 20 = <b>20*</b> , ± 25 = <b>44</b> , ± 50 = <b>45</b>	±20	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures. *limited frequencies, see page 3	
Operating Temperature Range <sup>2</sup>	-10 -20 -40 -40	- - - -	+70 +70 +85 +105	°C	Standard range Extended range <b>C</b> option Extended range <b>E</b> option Extended range <b>G</b> option	
Supply Voltage <sup>1, 2</sup> (Vcc)	2.25	2.5	2.75	V	2.5V ± 10%	
Output Waveform	CMOS					
Duty Cycle	45	-	55	%	At 50% Vcc level	
Output V <sub>HIGH</sub> (VOH)	V <sub>CC</sub> - 0.4	-	-	V		
Output V <sub>LOW</sub> (VOL)	-	-	0.4	V		
Output T <sub>RISE</sub> and T <sub>FALL</sub>	-	1	5	ns	C <sub>LOAD</sub> = 15 pF 10% to 90% of V <sub>CC</sub> See Load Circuit	
Startup Time	-	-	10	ms	Time for output to reach specified frequency	
V <sub>DISABLE</sub> (VIL)	-	-	0.3Vcc	V		
V <sub>ENABLE</sub> (VIH)	0.7Vcc	-				
Output Enable Time	-	-	100	ns	Time for output to reach a logic state	
Output Disable Time	-	-	200	ns	Time for output to reach a high Z state	
Enable/Disable Internal Pull-up	30	70	150	KΩ	To V <sub>CC</sub> , Pad 1 open of ≥0.7Vcc	
Standby Current (Pad 1 low)	-	-	10 20	μA	-40 to +85°C -40 to +105°C	Output disabled to tristae
Output Leakage Current	- -10	-	+10 -	μA	Out = Vcc Out = Gnd	Pad 1 low
rms Phase Jitter	-	0.1	1.0	ps	Fo ≥ 40MHz; 12kHz ~ 20MHz	
Phase Noise 10 Hz 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 10 MHz	-	-78 -107 -132 -144 -151 -155 -158	-	dBc/Hz	25°C ± 2°C at 100 MHz	
Storage Temperature Range	-55	-	+125	°C		

Notes: Specifications with Pad 1 E/D open circuit

<sup>1</sup> Place an appropriate power supply bypass capacitor next to V<sub>CC</sub> for best performance

<sup>2</sup> Specified by part number



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## Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition	
Supply Current $I_{CC}$		0.6	1.2		3 MHz	no load
		0.9	1.8		5 MHz	
		0.9	1.8		10 MHz	
	-	1.1	2.2		20 MHz	
	-	3.0	6.0	mA	50 MHz	
	-	3.0	6.0		65 MHz	
	-	4.0	8.0		85 MHz	
		4.5	8.5		100 MHz	
		5.5	10.5		133 MHz	
		9.0	18.0		170 MHz	

Specifications with Pad 1 E/D circuit open



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## Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V <sub>CC</sub>	Frequency in MHz	Optional T&R Packaging code
SM44	45	J	E	W	- 125.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C G = -40 to +105°C**	W = 2.5V ± 10%	1.25 - 170	T250 = 250 per Reel T500 = 500 per Reel T3K = 3000 per Reel (Std)

\* Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

\*\* ± 50ppm

## Device Marking

<b>PFF.FFM</b> • YMDxx
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<b>PFF.FFM</b> • YMxxx
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PLE or P = Pletronics  
FF.FF = Frequency in MHz  
YMD or YM = Date Code, All other marking is internal code

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	3	4	5	6	7	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2023	2024	2025	2026	2027	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

## Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)  
Font is Courier New  
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)  
Font is Arial

<b>P/N:</b>
<b>PLE Part Number</b>
<b>Customer P/N:</b>
<b>12345678</b>
<b>Qty:</b>
<b>3000</b>
<b>D/C</b>
<b>2A1</b>
<b>MSL: 1</b>

RoHS Compliant

2nd Lvl Interconnect

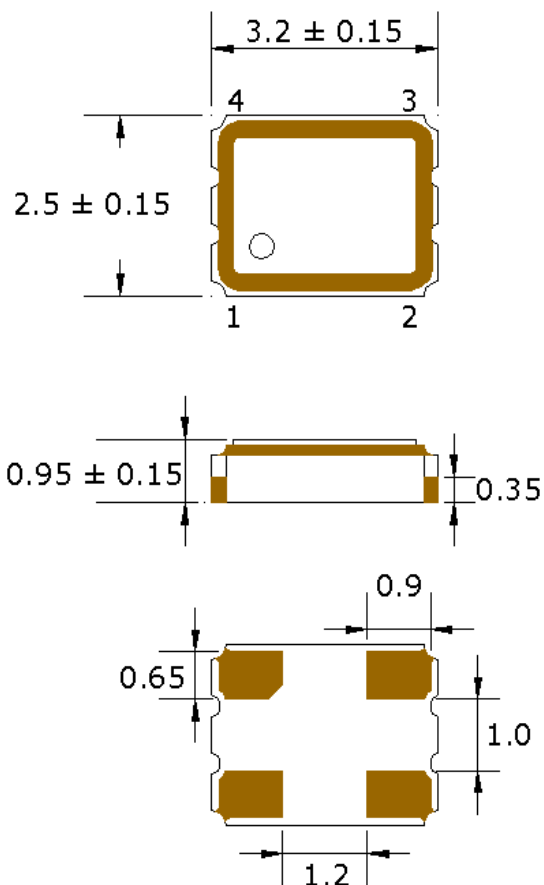
Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's  
Weight of the Device: 0.024 grams  
Moisture Sensitivity Level: 1 As defined in J-STD-020D  
Second Level Interconnect code: e4

## Mechanical Dimensions

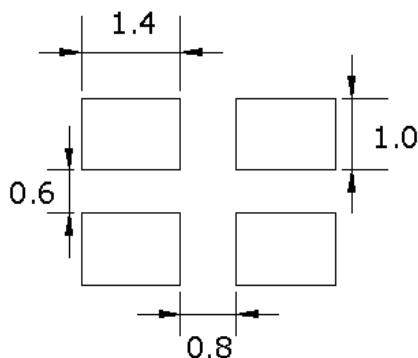


## Pad Connections

Pad	Function
1	Enable/Disable
2	Ground
3	Output
4	Vcc

## ENABLE/DISABLE

Pad 1	Output
V <sub>IH</sub> /Open	Active
V <sub>IL</sub> /Gnd	Disabled/Tristate



**Pad Layout**  
Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

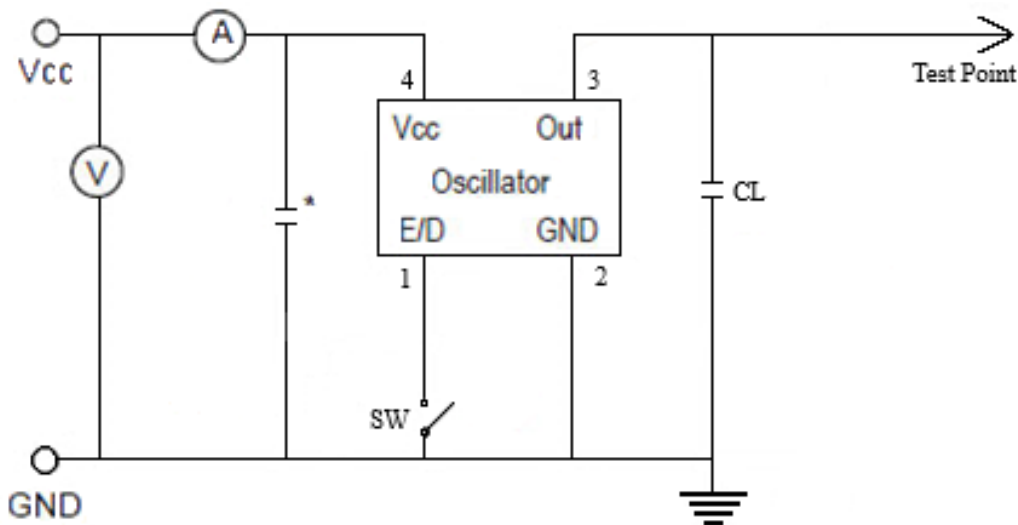
## Dimensions in mm

**Contacts (pads): Gold (0.3 to 1.0  $\mu$ m) over Nickel (1.27 to 8.89  $\mu$ m)**

For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

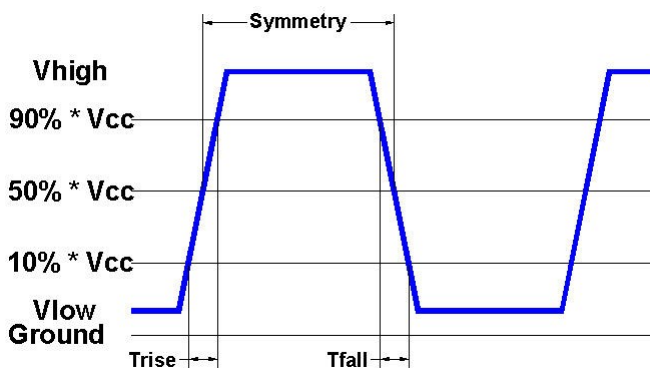
## Electrical Test / Load Circuit



Notes:

CL: Includes the input capacitance of oscilloscope

\*  $0.01\mu F$  external by-pass filter is recommended



## Environmental / ESD Ratings

### Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

### ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

### Thermal Characteristics:

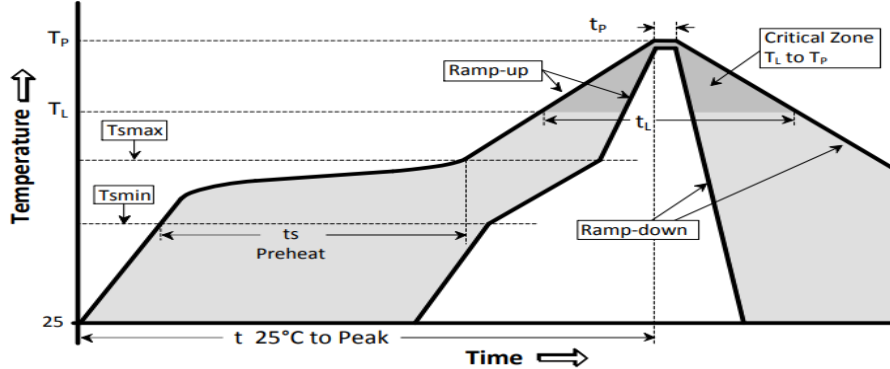
The maximum die or junction temperature is  $150^{\circ}C$

### Absolute Maximum Ratings

Parameter	Unit
$V_{CC}$ Supply Voltage	-0.3V to +4.0V
$V_i$ Input Voltage	-0.3V to $V_{CC} + 0.3V$
$V_o$ Output Voltage	-0.3V to $V_{CC} + 0.3V$

## Reflow Cycle

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"

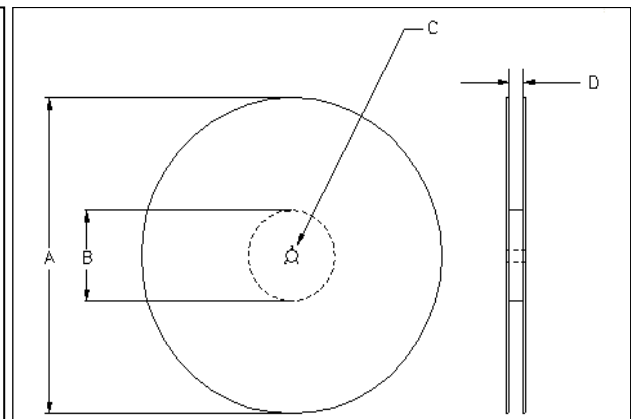
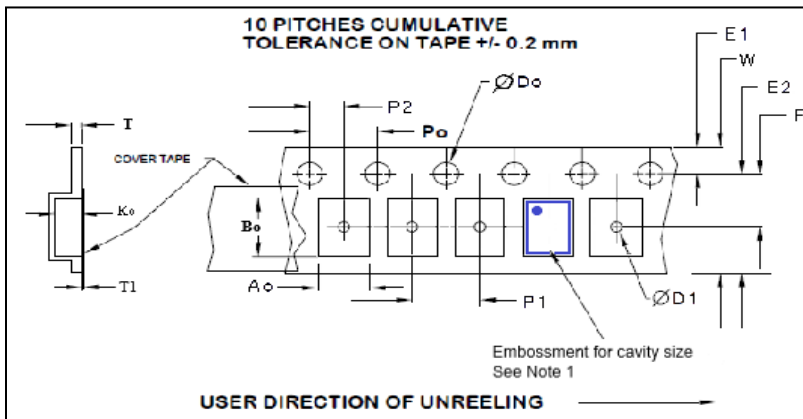


The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	$(T_{Smax} \text{ to } T_p)$	3°C / second max	°C / s
Ramp down Rate	$T_{cool}$	6°C / second max	°C / s
Time 25°C to Peak Temperature	$T_{to-peak}$	8 minutes max	min
<b>Preheat</b>			
Temperature min	$T_{Smin}$	150	°C
Temperature max	$T_{Smax}$	200	°C
Time $T_{Smin}$ to $T_{Smax}$	$t_s$	60 – 180	sec
<b>Soldering above liquidus</b>			
Temperature liquidus	$T_L$	217	°C
Time above liquidus	$t_L$	60 – 150	sec
<b>Peak temperature</b>			
Peak Temperature	$T_p$	260	°C
Time within 5°C of peak temperature	$t_p$	20 – 40	sec

## Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.



Tape Variable Dimensions Table 2

Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko
8mm	6.25	3.5 ±0.05	4.0 ±0.1	8.2	2.7 ± 0.1	3.4 ± 0.1	1.4 ± 0.1

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA-481-B

Tape Constant Dimensions Table 1

Tape Size	Do	D1 min	E1	Po	P2	T max	T1 max
8mm	1.5 +0.1 -0.0	1.0	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.3	0.1

Reel Dimensions (may vary) Table 3

Reel Size	A	B	C	D
Inches	mm	Inches	mm	mm
7	7.0	180	2.50	60
				13.0
				+0.5 -0.2
				Tape size +0.4 +2.0 -0.0



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