

5.2kVDC Isolated 6W Gate Drive SM DC-DC Converters



FEATURES

- No opto feedback
- Patents Protected
- Optimised bipolar output voltages for IGBT/ SiC & Mosfet gate drives
- Configurable dual outputs for all gate drive applications: +15V/-5V, +15V/-10V & +20V/-5V outputs
- Reinforced insulation to UL60950 recognised
- ANSI/AAMI ES60601-1 1MOPP/2MOOPs recognised
- Characterised CMTI >100kV/µS
- Characterised partial discharge performance
- 5.2kVDC isolation test voltage 'Hi Pot Test'
- Ultra low isolation capacitance 15pF
- Continuous barrier withstand voltage 3kVDC
- 5V, 12V & 24V input voltages
- 105°C operating temperature

PRODUCT OVERVIEW

Offering configurable dual output voltages of +15V/-10V, +20V/-5V and +15V/-5V, the MGJ6 series of DC-DC converters is ideal for powering 'high side' and 'low side' gate drive circuits for IGBTs, Silicon and Silicon Carbide Mosfets in bridge circuits. A choice of asymmetric output voltages allows optimum drive levels for best system efficiency and EMI. The MGJ6 series is Characterised for high isolation and dv/dt requirements commonly seen in bridge circuits used in motor drives and inverters. A disable/frequency synchronisation pin simplifies EMC filter design. The MGJ6 protection features include short circuit protection and overload protection.









SELECTION GUIDE								
				Output 1			Output 2	
Order Code ¹	Input Voltage Range	Typical Application	Rated Output Voltage	Rated Output Current	Output Power	Rated Output Voltage	Rated Output Current	Output Power
	V	See page 8	V	mA	W	V	mA	W
MGJ6T12150505MC	9 - 18	IGBT	+15	240	3.6	-10	240	2.4
MGJ6T24150505MC	18 - 36	IGBT	+15	240	3.6	-10	240	2.4
MGJ6T12150505MC	9 - 18	SiC	+20	240	4.8	-5	240	1.2
MGJ6T24150505MC	18 - 36	SiC	+20	240	4.8	-5	240	1.2
MGJ6T12150505MC	9 - 18	MOSFET	+15	300	4.5	-5	300	1.5
MGJ6T24150505MC	18 - 36	MOSFET	+15	300	4.5	-5	300	1.5
	NRND							
MGJ6T05150505MC	4.5 - 9	IGBT	+15	240	3.6	-10	240	2.4
MGJ6T05150505MC	4.5 - 9	SiC	+20	240	4.8	-5	240	1.2
MGJ6T05150505MC	4.5 - 9	MOSFET	+15	300	4.5	-5	300	1.5

SELECTION GUIDE ((Continued	l)								
	Output 1				Output 2					
Order Code ¹	Input Voltage Range	Typical Application	Load Regulation (Typ) ³	Load Regulation (Max) ³	Ripple & Noise (Typ) ²	Ripple & Noise (Max) ²	Load Regulation (Typ) ³	Load Regulation (Max) ³	Ripple & Noise (Typ) ²	Ripple & Noise (Max) ²
	V	See page 8	9	6	mVp-p		%		mVp-p	
MGJ6T12150505MC	9 - 18	IGBT	5	10	148	200	5	10	116	150
MGJ6T24150505MC	18 - 36	IGBT	5	10	148	200	5	10	110	150
MGJ6T12150505MC	9 - 18	SiC	5	10	206	275	5	10	58	75
MGJ6T24150505MC	18 - 36	SiC	5	10	203	275	5	10	55	75
MGJ6T12150505MC	9 - 18	MOSFET	5	10	148	200	5	10	58	75
MGJ6T24150505MC	18 - 36	MOSFET	5	10	148	200	5	10	55	75
			NF	RND						
MGJ6T05150505MC	4.5 - 9	IGBT	5	10	120	200	5	10	118	150
MGJ6T05150505MC	4.5 - 9	SiC	5	10	169	275	5	10	59	75
MGJ6T05150505MC	4.5 - 9	MOSFET	5	10	120	200	5	10	59	75

- 1. Components are supplied in tape and reel packaging, please refer to package specification section. Orderable part numbers are MGJ6TXX150505MC-R7 (23 pieces per reel), or MGJ6TXX150505MC-R13 (92 pieces per reel).
- See ripple & noise test method.
 Between 50% and 100% rated output current.
- All specifications typical at T_A=25°C, nominal input voltage and rated output current unless otherwise specified.

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SELECTION GUIDE (Continued)						
	put	ent	8:	Efficiency (Min) Efficiency (Typ)		MT	TF ¹
Order Code	Nominal Input Voltage	Input Current at Rated Load	Efficienc (Min)			MIL 217	Telecordia
	V	mA	0	%	pF	kH	Irs
MGJ6T12150505MC	12	600	77	81	15	633	7061
MGJ6T24150505MC	24	300	79	83	15	666	6774
	NRND						
MGJ6T05150505MC	5	1500	72	78	15	552	6821

INPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
	5V input types	4.5	5	9	
Voltage range	12V input types	9	12	18	V
	24V input types	18	24	36	
	Turn on threshold MGJ6T05		4.1		
	Turn off threshold MGJ6T05		3.0		
Under veltege leek out	Turn on threshold MGJ6T12		8.1		v
Under voltage lock out	Turn off threshold MGJ6T12		7.5		v
	Turn on threshold MGJ6T24		16.7		
	Turn off threshold MGJ6T24		16.3		
	5V input types		40		^
Input ripple current	12V input types		40		mA
	24V input types		24		p-p

OUTPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Minimum load	Below 10% load, 5V and 15V outputs are clamped to 6V and 16V respectively	10			%
Voltage set point accuracy	All output types		±4		%
Line regulation	Low line to high line			2	%
Transient reapense	Peak deviation (50-100% & 100-50% load swing)		0.4		%V _{out}
Transient response	Settling time		0.1		ms

ISOLATION CHARACTERIS	STICS						
Parameter		Conditions		Min.	Тур.	Max.	Units
Isolation test voltage	lealation took walte me			5200			VDC
isolation test voltage		Qualification tested for 1 minute		5200			VDC
Resistance	Resistance		Viso = 1kVDC				GΩ
Continuous barrier withstand	voltage	Non-safety barrier application				3000	V
Safety standard	UL60950-1	Reinforced	Croopage and clearance 7mm			250	Vrms
Salety Standard	ANSI/AAMI ES60601-1	1 MOPP/2 MOOP	Creepage and clearance 7mm			250	VIIIIS

GENERAL CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Switching frequency			100		kHz

^{1.} Calculated using MIL-HDBK-217 FN2 and Telecordia SR-332 calculation model at TA=25°C with nominal input voltage at full load.



TEMPERATURE CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Operation	See derating graphs	-40		105	
Storage		-50		125	°C
Product temperature rise above ambient	100% Load, Nom V _{IN} , Still Air		25		

ABSOLUTE MAXIMUM RATINGS	
Short-circuit protection	Continuous
Input voltage, MGJ6 5V input types	12V
Input voltage, MGJ6 12V input types	20V
Input voltage, MGJ6 24V input types	40V



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TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions MGJ6 series of DC-DC converters are all 100% production tested at 5.2kVDC for 1 second and qualification tested at 5.2kVDC for 1 minute.

The MGJ6 series is recognised by Underwriters Laboratory, please see safety approval section for more information. When the insulation in the MGJ6 series is not used as a safety barrier, i.e. provides functional isolation only, continuous or switched voltages across the barrier up to 3kV are sustainable. This is established by measuring the partial discharge Inception voltage in accordance with IEC 60270. Please contact Murata for further information.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

SAFETY APPROVAL

ANSI/AAMI ES60601-1

The MGJ6 series is recognised to ANSI/AAMI ES60601-1 and provides 1 MOPP (Means Of Patient Protection) and 2 MOOP (Means Of Operator Protection) based upon a working voltage of 250 Vrms max, between Primary and Secondary.

UL 60950

The MGJ6 series has been recognised by Underwriters Laboratory (UL) to UL 60950 for reinforced insulation to a working voltage of 250Vrms with a maximum measured product operating temperature of 105°C.

Creepage and clearance 7mm.

FUSING

The MGJ6 Series of converters are not internally fused so to meet the requirements of UL an anti-surge input line fuse should always be used with ratings as defined below.

Input Voltage, 5V 4A Input Voltage, 12V 2A Input Voltage, 24V 1A

All fuses should be UL recognised, 125V rated.

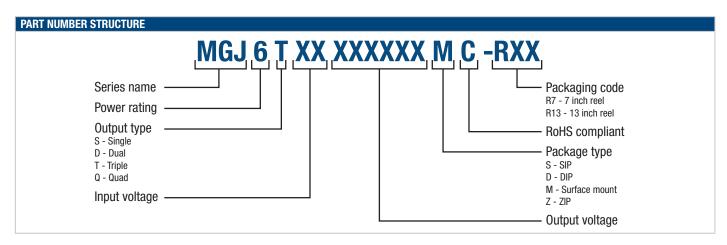
ROHS COMPLIANCE, MSL, PSL AND REFLOW SOLDERING INFORMATION



This series is compatible with RoHS soldering systems with a peak reflow solder temperature of 245°C and Time Above Liquidus for 90 seconds, as per J-STD-020. Please refer to application notes for further information. The pin termination finish on this product series is Gold with Nickel Pre-plate. The series is backward compatible with Sn/Pb soldering systems. The series has a Moisture Sensitivity Level (MSL) 2. Samples of the product series were tested in accordance with the conditioning described for MSL level 2 in IPC/J-STD-020. The product series passed electrical tests, co-planarity and visual inspection criteria.



ENVIRONMENTAL VALID	ATION TESTING						
The following tests have been	The following tests have been conducted on this product series, please contact Murata if further information about the tests is required.						
Test	Standard	Condition					
Temperature cycling	MIL-STD-883 Method 1010, Condition B	10 cycles between two chambers set to achieve -55°C and +125°C. The dwell time shall not be less than 10min and the load shall reach the specified temperature in 15min.					
HAST (Unbiased)	JEDEC JESD22-A118	96Hrs +2/-0Hrs at 130°C ± 2°C, 85% ± 5% R.H.					
High Temperature Storage life	JEDEC JESD22-A103, Condition A	125°C +10/-0°C for ≥1000 hours.					
Vibration	BS EN 61373 with respect to BS EN 60068-2-64, Test Fh Category 1 Class B	5 – 150Hz. Level at each axis – Vertical, Traverse and Longitudinal: 5.72m/s² rms. 5 hours in each axis. Crest factor: 3 Sigma. Device is secured via surface mount pins.					
Shock	BS EN 61373, Category 1 Class B	Test is 30ms duration, 3 shocks in each sense of 3 mutually perpendicular axis (18 shocks total). Level at each axis: Vertical, Traverse and Longitudinal: 50m/s². Device is secured via surface mount pins.					
Solderability	EIA/IPC/JEDEC J-STD-002, Test S and S1	The parts are conditioned in a steam ager for 8 hours ± 15 min. at a temperature of $93\pm3^{\circ}$ C. SnPb (Test S): The parts are placed onto a stencil with Sn60Pb40 solder paste on and then placed into the reflow oven at 215 $\pm5^{\circ}$ C for 50–70 seconds. Pb-free (Test S1): The parts are placed onto a stencil with Sn96.5Ag3.0Cu0.5 solder paste on and then placed into the reflow oven at 245 $\pm5^{\circ}$ C for 30–60 seconds.					
Solvent cleaning	Resistance to cleaning agents.	Solvent – Novec 71IPA & Topklean EL-20A. Pulsed ultrasonic immersion 45°C - 65°C.					
Solvent Resistance	MIL-STD-883 Method 2015	The parts and the bristle portion of the brush are immersed in Isopropanol for a minimum of 1 minute. The parts are brushed 3 times, after the third time the parts are blown dry and inspected.					
Moisture sensitivity level (MSL 2)	Based on IPC/JEDEC J-STD-020	Bake samples at 125 +5/-0°C for 24hours minimum before conditioning in the temperature/humidity chamber for 168 hours at 85°C/60%RH and Pb Free JEDEC Max profile conditioning with electrical testing, co-planarity inspection before and after.					





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CHARACTERISATION TEST METHODS Ripple & Noise Characterisation Method Ripple and noise measurements are performed with the following test configuration. 1µF X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC-DC converter 10µF tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC-DC converter with an ESR of less C2 than 100mΩ at 100 kHz C3 100nF multilayer ceramic capacitor, general purpose R1 450 Ω resistor, carbon film, ±1% tolerance R2 50Ω BNC termination 3T of the coax cable through a ferrite toroid T1 RLOAD Resistive load to the maximum power rating of the DC-DC converter. Connections should be made via twisted wires Measured values are multiplied by 10 to obtain the specified values. Differential Mode Noise Test Schematic DC/DC Converter OSCILLOSCOPE R2 _____



APPLICATION NOTES

Disable/Frequency synchronisation

Please refer to application notes for further information.

		Min	Тур	Max	Units
	Pull Down Current		0.5		mA
Disable/Sync1	Input High	2		60	V
	Input Low	-0.6		8.0	V
Synchronisation	Frequency Range	90	100	110	kHz
Synchronisation	Duty Cycle	25		75	%

The Disable/Synchronization pin has three modes:

- 1. When a DC logic low voltage is applied to this pin the MGJ6 is disabled and enters a low quiescent current sleep mode.
- When this pin is left floating or a DC logic high (CMOS/TTL compatible) voltage is applied the MGJ6 is enabled and operates at the programmed frequency of 100kHz.
- 3. When a square wave of between 90kHz and 110kHz is applied to this pin, the switcher operates at the same frequency as the square wave. The falling edge of the square wave corresponds to the start of the switching cycle. If the signal is slower than 25Hz, it will be interpreted as enabling and disabling the part. If the MGJ6 is disabled, it must be disabled for 7 clock cycles before being re-enabled.

Note: The Dis/Sync pin is a high impedance TTL input and can be triggered by noise from external circuits if not treated carefully.

Please refer to "LAYOUT CONSIDERATIONS" and "SYNCHRONISATION CIRCUIT" for further details.

Click here for general guidance for gate drive applications.

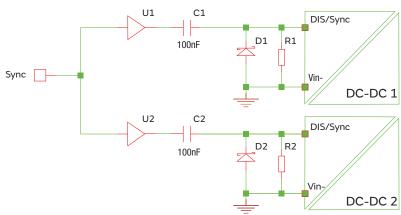
LAYOUT CONSIDERATIONS

Unlike standard isolated DC-DC products the MGJ6 series has been designed specifically for high side gate drive applications where the outputs are being driven to a high voltage at a very high dV/dT. This is possible due to minimum transformer isolation capacitance and considered circuit design regarding common mode transient immunity. It is important that these few simple pcb layout guidelines are implemented so as not to compromise the performance of the DC-DC and that of the overall system.

- 1. The keep clear area shown must not have any copper traces even on internal layers. This is not only to avoid compromising the creepage and clearance distance but also to minimise capacitive isolation between the noisy output circuits and input control circuits. In general it is good practice to maintain the same band of clearance area running directly through both the DC-DC and the gate drive isolators as shown so that input and output are kept separate and do not overlap or mesh together at any point.
- A top layer ground plane copper area connected to -Vin can be used to create an effective screen to the underside of the MGJ6 series and can also be used as a
 guard ring for the gate drive isolator inputs. If the Dīs/Synch pin is being used then it is imperative that it follows a route covered by this screen to avoid differential
 pick up. It should also be kept as short as possible.

Please refer to "PACKAGE SPECIFICATIONS" for recommended layout.

SYNCHRONISATION CIRCUIT



- 1. A suggested synchronisation circuit is shown. C1 and C2 are 100nF capacitors. D1 and D2 are schottky diodes. The capacitive isolation and close connected diode ensures that a transition from high to low is seen at the input pin even in a noisy environment or when there is a slight ground shift between devices.
- 2. If the Dis/Sync pin is not used for synchronisation, then a 22nF capacitor can be added between the Dis/Sync pin and —Vin pin to improve noise immunity. If the functionality of Dis/Sync is not required, the Dis/Sync pin can be connected directly to the +Vin pin to improve noise immunity.
- 3. One very effective method to reduce common mode transient interference is to add a common mode filter to the DC input. It may only be necessary to add one before splitting the supply to each DC-DC.



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APPLICATION NOTES (Continued)

Start-up times

Typical start up times for this series, with no additional output capacitance are:

Part No.	Start-up times
raitivo.	ms
MGJ6T05150505MC	15
MGJ6T12150505MC	15
MGJ6T24150505MC	15

Output capacitance must not exceed:

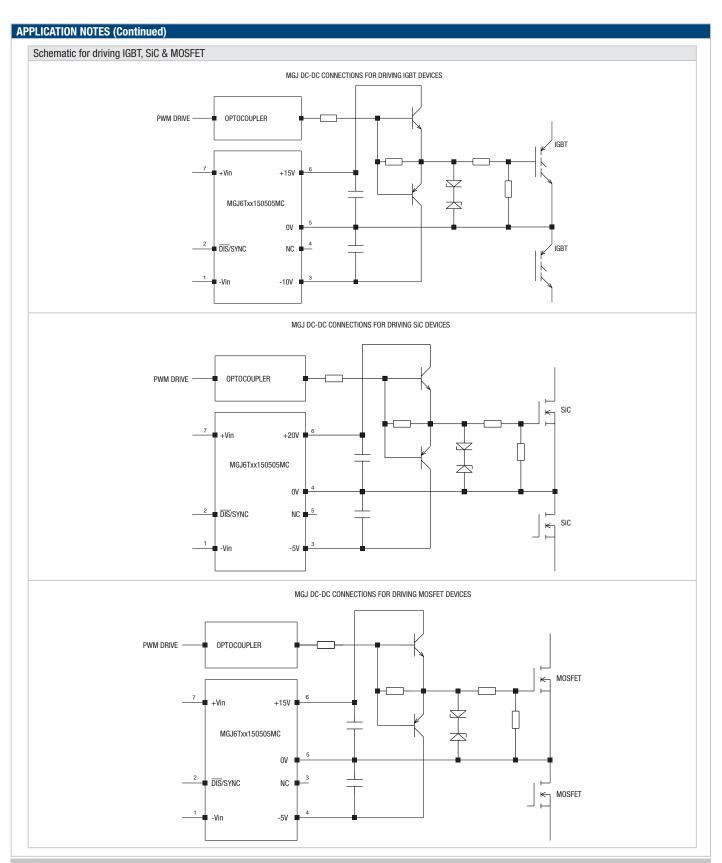
Output Voltage	Maximum output capacitance		
V	μF		
15	220		
5	470		

Output configurations for power switches

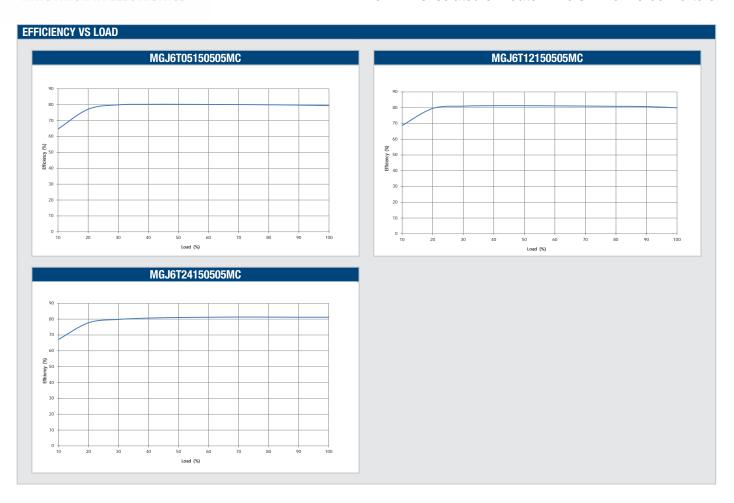
Terminal	IGBT	SIC	MOSFET	
(P6) 15V Output	+15V 0.24A	+20V 0.24A	+15V 0.3A	
(P5) 15V Return 5VA Output	OV	No connection	OV	
(P4) 5VA Return 5VB Output	No connection	OV	-5V 0.3A	
(P3) 5VB Return	-10V 0.24A	-5V 0.24A	No connection	









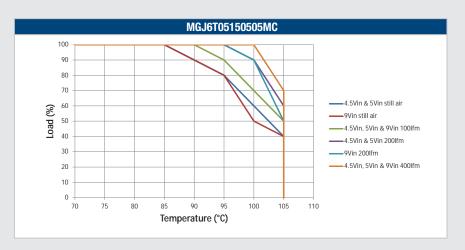


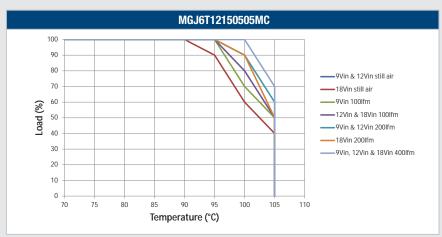


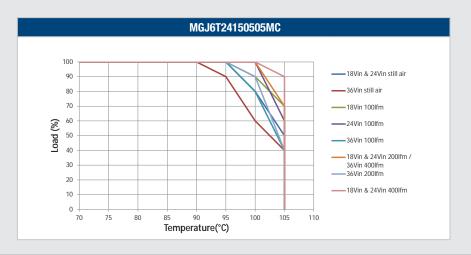


DERATING GRAPHS

Derating curves are based on IPC-9592. With no derating some components may be operating at the manufacturers maximum temperature ratings.





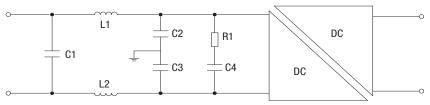




EMC FILTERING AND SPECTRA

FILTERING

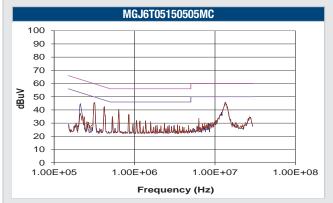
The following filter circuit and filter table shows the input filters typically required to meet EN55022 Quasi-Peak Curve A or B. If a high dv/dt above 80kV/us is expected from output to input it is advised that a common mode filter is used on the input without Y capacitors. This will reduce the common mode current and reduce interference with primary side circuits.

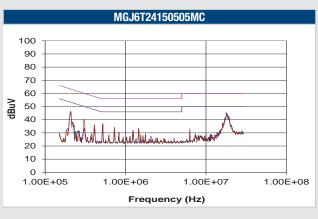


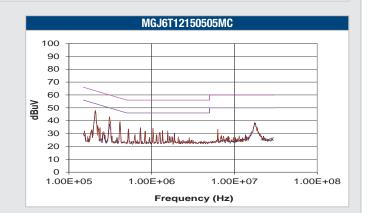
C1, C2 & C3 Polyester or ceramic capacitor

C4 Electrolytic capacitor (note R1 could be omitted if C4 has ESR >= R1)

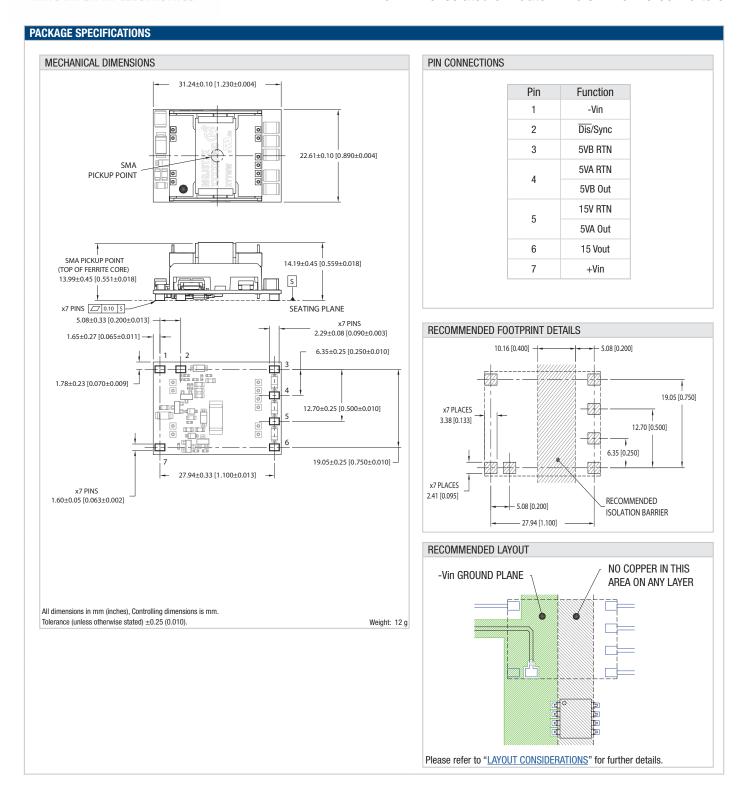
TO MEET CURVE B							
Part Number	C1	L1	L2	C2	C3	R1	C4
MGJ6T05150505MC	3.3µF	47100SC		10nF	10nF	500m Ω	470µF
MGJ6T12150505MC	3.3µF	4710	OSC	10nF	10nF	500m Ω	470µF
MGJ6T24150505MC	3.3µF	4710	OSC	10nF	10nF	500m Ω	470µF







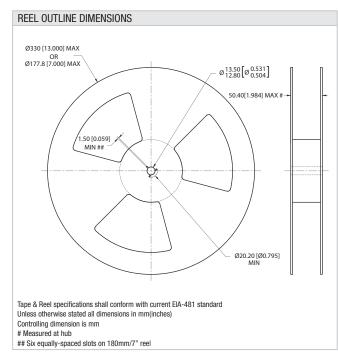


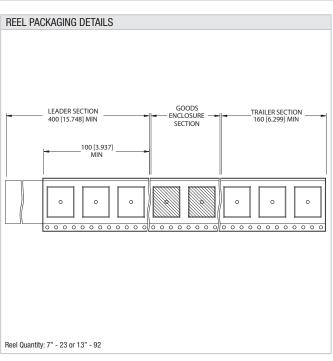


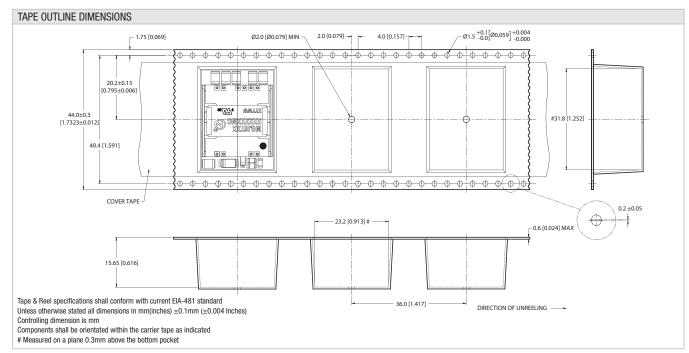




TAPE & REEL SPECIFICATIONS









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- Power plant control equipment
- Medical equipment
- Transportation equipment (automobiles, trains, ships, etc.)
- Traffic signal equipment
- Disaster prevention / crime prevention equipment
- Data Processing equipment

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