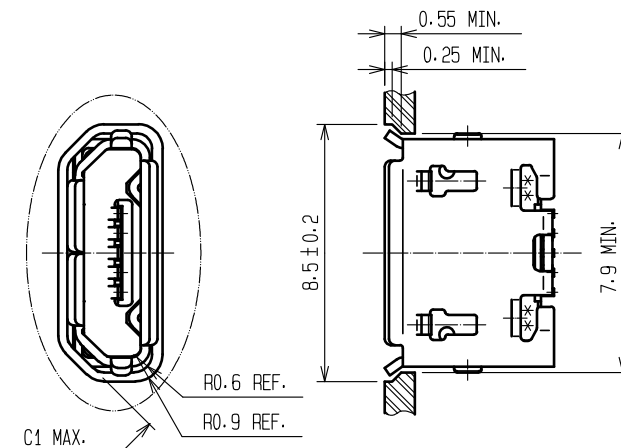
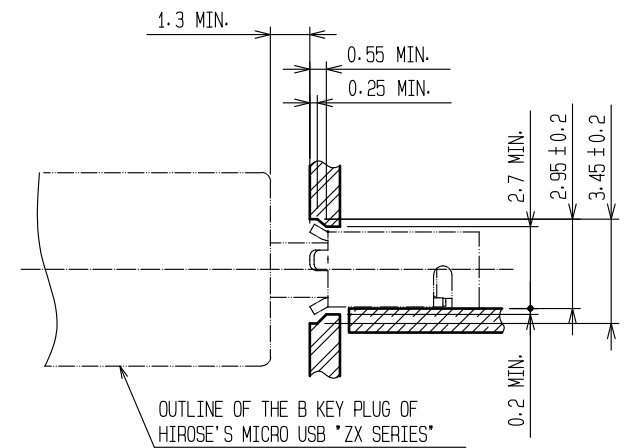


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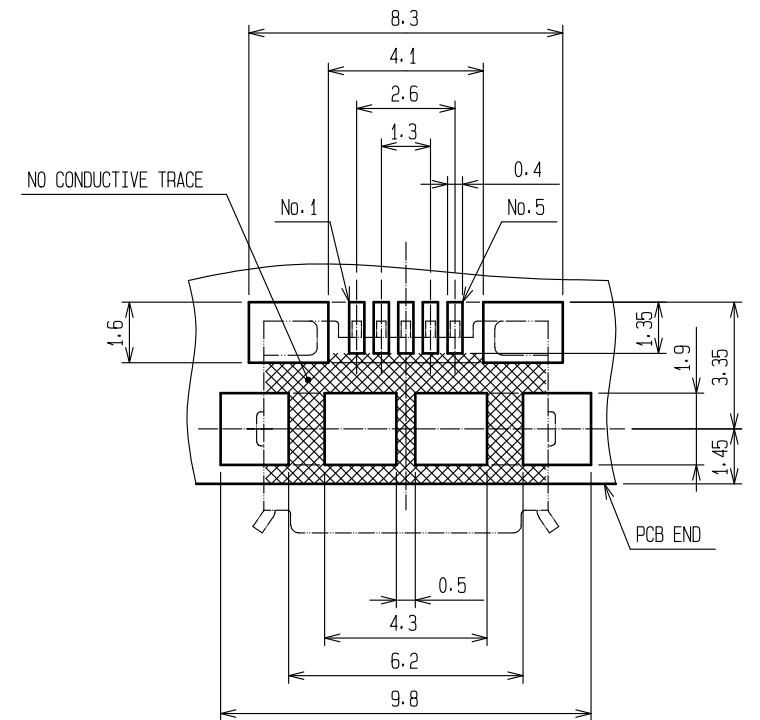
RECOMMENDED PANEL CUTOUT(4:1)



MATED WITH CORRESPONDING PLUG(4:1)



PCB LAYOUT



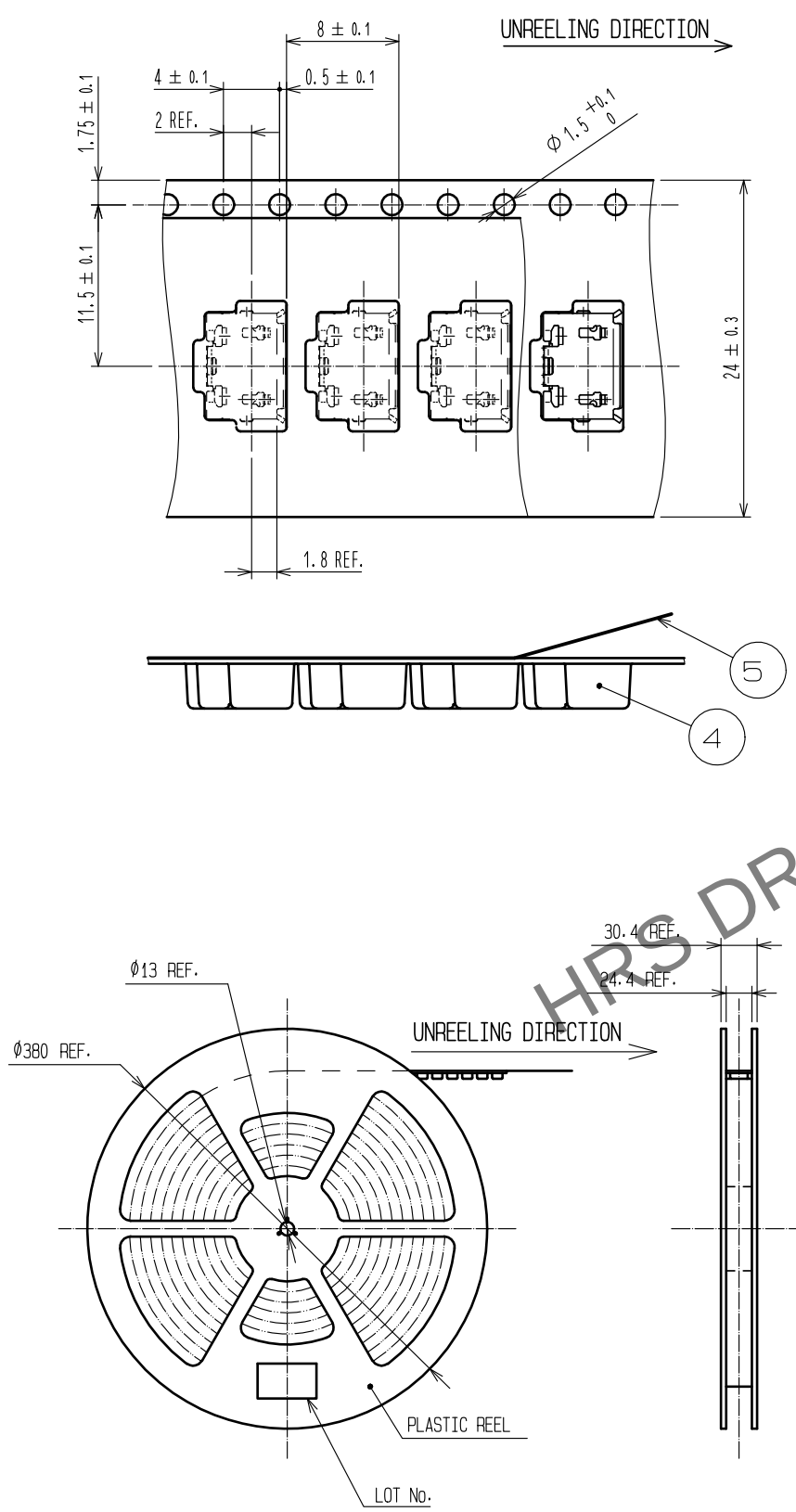
GENERAL TOLERANCE IS $\pm 0.05\text{mm}$.

- NOTES
- CO-PLANARITY IS WITHIN 0.08 mm .
 - PLATING
CONTACT AREA : GOLD $0.75\mu\text{m}$ MIN.
LEAD AREA : GOLD $0.05\mu\text{m}$ MIN.
UNDER PLATING: NICKEL $2\mu\text{m}$ MIN.
 - PER REEL : 3500 pcs.
 - INTERFACE DIMENSIONS CONFORM TO USB2.0 SPECIFICATION.
 - AS FOR PART No.3, THE PERFORMANCE REMAINS GOOD EVEN IF THERE ARE RUBBING WOUNDS BY THE ASSEMBLY PROCESS.
AND, THE PERFORMANCE REMAINS GOOD THOUGH THERE MIGHT BE A DIFFERENCE IN THE PLATING LUSTER BETWEEN EACH PLATING SUBCONTRACT COMPANIES.
 - AS FOR PART No.1, A PART OF SHAPE MAY BECOME DIFFERENT DEPENDING ON THE PRODUCTION METHOD.
 - CODE CONFIGURATION IS SHOWN IN FIG.1.

2	COPPER ALLLOY	2	5	PE	
1	LCP	BLACK, UL94V-0	4	PS	
NO.	MATERIAL	FINISH, REMARKS	NO.	MATERIAL	OVER PLATING : TIN 1 μm min. LUBRICANT
UNITS mm		SCALE 5 : 1	COUNT	DESIGNED	CHECKED
HIROSE ELECTRIC CO., LTD.		APPROVED : NM. NISHIMATSU	15.10.27	DRAWING NO.	EDC-126332-33-00
		CHECKED : KN. ICHIKAWA	15.10.27	PART NO.	ZX62-B-5PA(33)
		DESIGNED : TS. ITO	15.10.27	CODE NO.	CL242-0033-8-33
		DRAWN : AK. AKIYAMA	15.10.27		

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3> DRAWING FOR PACKING



7> FIG.1 LOT CODE CONFIGURATION FOR 4 DIGITS

YEAR		MONTH		DAY						LINE No.	
YEAR	CODE	MONTH	CODE	DAY	CODE	DAY	CODE	DAY	CODE	LINE No.	CODE
2007	7	Jan.	A	1	1	11	B	21	M	1	A
2008	8	Feb.	B	2	2	12	C	22	N	2	B
2009	9	Mar.	C	3	3	13	D	23	P	3	C
2010	0	Apr.	D	4	4	14	E	24	Q	4	D
2011	1	May.	E	5	5	15	F	25	R	5	E
		Jun.	F	6	6	16	G	26	S	6	F
		Jul.	G	7	7	17	H	27	T	7	G
		Aug.	H	8	8	18	J	28	U	8	H
		Sep.	I	9	9	19	K	29	V	9	I
		Oct.	J	10	A	20	L	30	W	10	J
		Nov.	K					31	X	11	K
		Dec.	L								

HRS	DRAWING NO.	EDC-126332-33-00	
	PART NO.	ZX62-B-5PA(33)	
	CODE NO.	CL242-0033-8-33	2 2