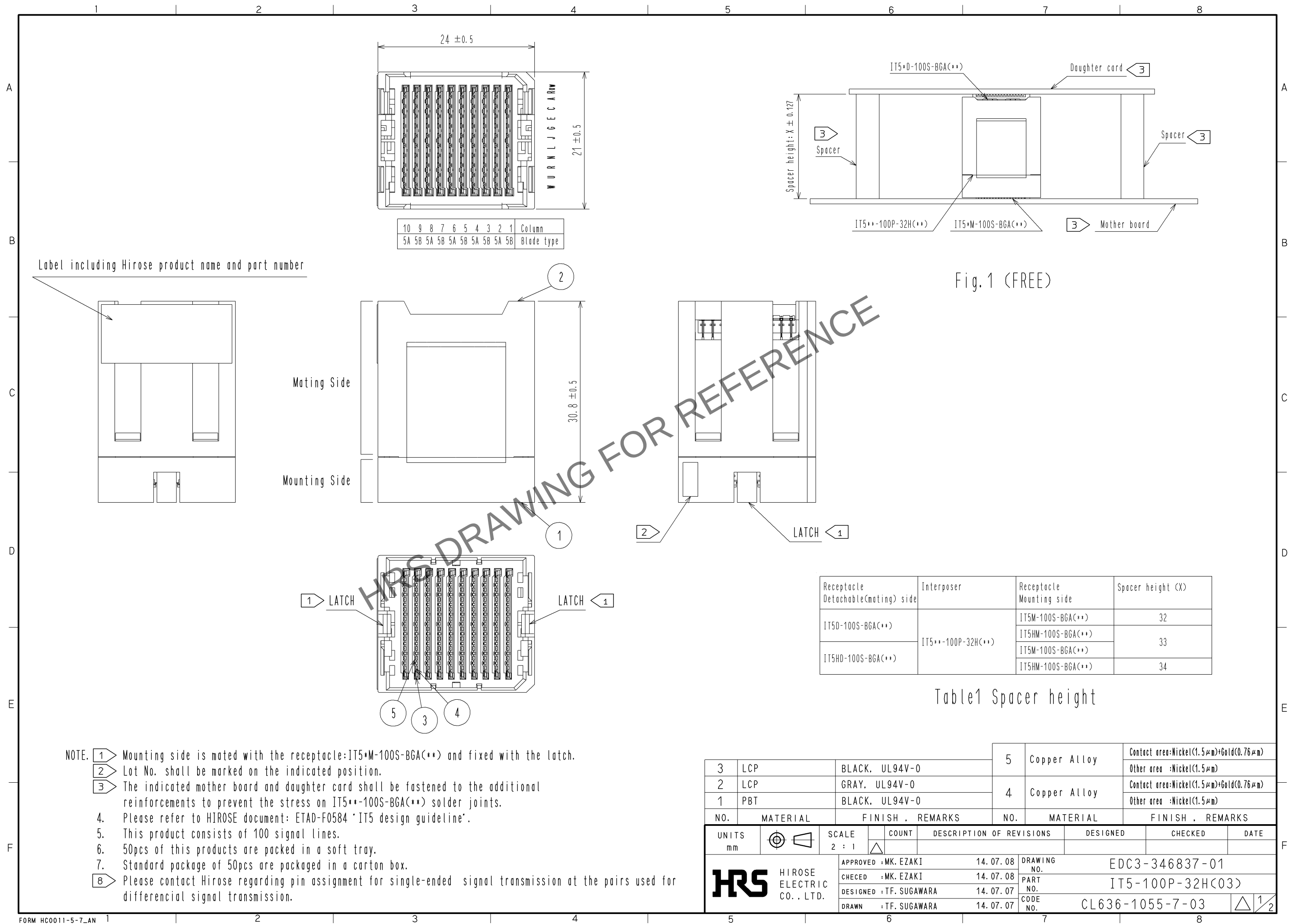




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A
B
C
D
E
F

A
B
C
D
E
F

Wafer type		Wafer #	
		Column	Row
IT5 wafer B (100 ohm)	10	20	G
IT5 wafer B (100 ohm)	9	19	G
IT5 wafer A (100 ohm)	8	18	G
IT5 wafer A (100 ohm)	7	17	G
IT5 wafer B (100 ohm)	6	16	G
IT5 wafer B (100 ohm)	5	15	G
IT5 wafer A (100 ohm)	4	14	G
IT5 wafer A (100 ohm)	3	13	G
IT5 wafer B (100 ohm)	2	12	G
IT5 wafer B (100 ohm)	1	11	G
IT5 wafer A (100 ohm)	10	10	G
IT5 wafer A (100 ohm)	9	9	G
IT5 wafer B (100 ohm)	8	8	G
IT5 wafer B (100 ohm)	7	7	G
IT5 wafer A (100 ohm)	6	6	G
IT5 wafer A (100 ohm)	5	5	G
IT5 wafer B (100 ohm)	4	4	G
IT5 wafer B (100 ohm)	3	3	G
IT5 wafer A (100 ohm)	2	2	G
IT5 wafer A (100 ohm)	1	1	G
IT5 wafer B (100 ohm)	10	20	X
IT5 wafer B (100 ohm)	9	19	A
IT5 wafer A (100 ohm)	8	18	B
IT5 wafer A (100 ohm)	7	17	C
IT5 wafer B (100 ohm)	6	16	D
IT5 wafer B (100 ohm)	5	15	E
IT5 wafer A (100 ohm)	4	14	F
IT5 wafer A (100 ohm)	3	13	G
IT5 wafer B (100 ohm)	2	12	H
IT5 wafer B (100 ohm)	1	11	I
IT5 wafer A (100 ohm)	10	10	J
IT5 wafer A (100 ohm)	9	9	K
IT5 wafer B (100 ohm)	8	8	L
IT5 wafer B (100 ohm)	7	7	M
IT5 wafer A (100 ohm)	6	6	N
IT5 wafer A (100 ohm)	5	5	O
IT5 wafer B (100 ohm)	4	4	P
IT5 wafer B (100 ohm)	3	3	Q
IT5 wafer A (100 ohm)	2	2	R
IT5 wafer A (100 ohm)	1	1	S
IT5 wafer B (100 ohm)	10	20	T
IT5 wafer B (100 ohm)	9	19	U
IT5 wafer A (100 ohm)	8	18	V
IT5 wafer A (100 ohm)	7	17	W
IT5 wafer B (100 ohm)	6	16	X
IT5 wafer B (100 ohm)	5	15	Y

IT5•M-100S-BGA(**) Pin assignment footprint for mounting side

Wafer type		Wafer #	
		Column	Row
IT5 wafer B (100 ohm)	10	20	G
IT5 wafer B (100 ohm)	9	19	G
IT5 wafer A (100 ohm)	8	18	G
IT5 wafer A (100 ohm)	7	17	G
IT5 wafer B (100 ohm)	6	16	G
IT5 wafer B (100 ohm)	5	15	G
IT5 wafer A (100 ohm)	4	14	G
IT5 wafer A (100 ohm)	3	13	G
IT5 wafer B (100 ohm)	2	12	G
IT5 wafer B (100 ohm)	1	11	G
IT5 wafer A (100 ohm)	10	10	G
IT5 wafer A (100 ohm)	9	9	G
IT5 wafer B (100 ohm)	8	8	G
IT5 wafer B (100 ohm)	7	7	G
IT5 wafer A (100 ohm)	6	6	G
IT5 wafer A (100 ohm)	5	5	G
IT5 wafer B (100 ohm)	4	4	G
IT5 wafer B (100 ohm)	3	3	G
IT5 wafer A (100 ohm)	2	2	G
IT5 wafer A (100 ohm)	1	1	G
IT5 wafer B (100 ohm)	10	20	Y
IT5 wafer B (100 ohm)	9	19	W
IT5 wafer A (100 ohm)	8	18	V
IT5 wafer A (100 ohm)	7	17	U
IT5 wafer B (100 ohm)	6	16	T
IT5 wafer B (100 ohm)	5	15	R
IT5 wafer A (100 ohm)	4	14	Q
IT5 wafer A (100 ohm)	3	13	N
IT5 wafer B (100 ohm)	2	12	M
IT5 wafer B (100 ohm)	1	11	L
IT5 wafer A (100 ohm)	10	10	K
IT5 wafer A (100 ohm)	9	9	J
IT5 wafer B (100 ohm)	8	8	H
IT5 wafer B (100 ohm)	7	7	G
IT5 wafer A (100 ohm)	6	6	F
IT5 wafer A (100 ohm)	5	5	E
IT5 wafer B (100 ohm)	4	4	D
IT5 wafer B (100 ohm)	3	3	C
IT5 wafer A (100 ohm)	2	2	B
IT5 wafer A (100 ohm)	1	1	A
IT5 wafer B (100 ohm)	10	20	X

IT5•D-100S-BGA(**) Pin assignment footprint for mating side

Symbols

G: GND, Power

P: Positive of differential pair

N: Negative of differential pair

8 Twisted pair used for differential

8 Non twisted pair used for differential

HRS

DRAWING NO.	EDC3-346837-01
PART NO.	IT5-100P-32H(C03)
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