

#### Anti-parallel silicon RF Schottky diode pair



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## **Product description**

These Infineon RF Schottky diodes are silicon low barrier N-type devices with an integrated guard ring on-chip for over-voltage protection. Their low barrier height, low forward voltage and low junction capacitance make BAT15-099 a suitable choice for mixer and detector functions in applications which frequencies are as high as 12 GHz.



#### **Feature list**

- Low inductance  $L_S = 2 \text{ nH (typical)}$
- Low capacitance C = 0.29 pF (typical) at 1 MHz
- Industry standard SOT143 package (2.9 mm x 2.4 mm x 1 mm)
- Pb-free (RoHS compliant) and halogen-free

#### **Product validation**

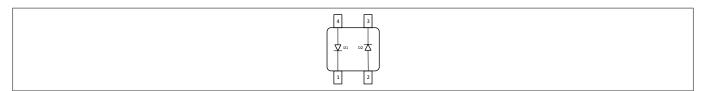
Qualified for industrial applications according to the relevant tests of JEDEC47/20/22.

## **Potential applications**

For mixers and detectors in:

- Wearables
- **Smart metering**
- Telematic systems
- Set top boxes

## **Device information**



#### Table 1 **Part information**

Product name / Ordering code	Package	Pin configuration	Marking	Pieces / Reel
BAT15-099 / BAT15099E6327HTSA1	SOT143	Anti-parallel pair	S5s	3 k
BAT15-099 / BAT15099E6433HTMA1				10 k

Attention: ESD (Electrostatic discharge) sensitive device, observe handling precautions!

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# 1 Absolute maximum ratings

Table 2 Absolute maximum ratings at  $T_A = 25$  °C, unless otherwise specified

Parameter	Symbol	Values		Unit	Note or test condition
		Min.	Max.		
Diode reverse voltage	$V_{R}$	_	4	V	
Forward current	I <sub>F</sub>	_	110	mA	
Total power dissipation	$P_{TOT}$	_	100	mW	$T_{\rm S} \le 48  {\rm ^{\circ}C^{1}}$
Junction temperature	TJ	_	150	°C	
Operating temperature	$T_{OP}$	-55	150		
Storage temperature	$T_{STG}$	-55	150		

Attention: Stresses above the maximum values listed here may cause permanent damage to the device.

Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Exceeding only one of these values may cause irreversible damage to the component.

 $T_{\rm S}$  is the soldering point temperature.

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**Electrical performance in test fixture** 

#### **Electrical performance in test fixture** 2

#### **Electrical characteristics** 2.1

Table 3 Electrical characteristics at  $T_A$  = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Breakdown voltage	$V_{BR}$	4	-	_	V	/ <sub>R</sub> = 100 μA
Reverse current	I <sub>R</sub>	_	-	5	μΑ	V <sub>R</sub> = 1 V
Forward voltage	$V_{F}$	0.16	0.25	0.32	V	/ <sub>F</sub> = 1 mA
		0.25	0.35	0.41		/ <sub>F</sub> = 10 mA
Forward voltage matching	$\Delta V_{F}$	_	_	20	mV	/ <sub>F</sub> = 10 mA <sup>1)</sup>
Differential forward resistance	$R_{F}$	-	5.8	-	Ω	$I_{\rm F} = 10  \text{mA}  /  50  \text{mA}^{2}$
Capacitance	С	_	0.29	0.35	pF	$V_{R} = 0 \text{ V}, f = 1 \text{ MHz}$
Inductance	L <sub>S</sub>	_	2	_	nH	

3 Datasheet

 $<sup>\</sup>Delta V_{\rm F}$  is the difference between lowest and highest  $V_{\rm F}$  in a multiple diode component.  $R_F = \frac{V_F^{(50~{\rm mA}) - V_F^{(10~{\rm mA})}}}{50~{\rm mA} - 10~{\rm mA}}$ 1



## **Electrical performance in test fixture**

## 2.2 Characteristic curves

At  $T_A$  = 25 °C, unless otherwise specified

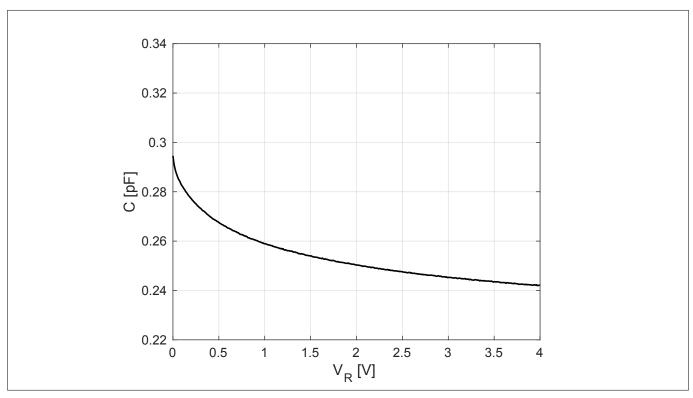


Figure 1 Diode capacitance C vs. reverse voltage  $V_R$  at frequency f = 1 MHz

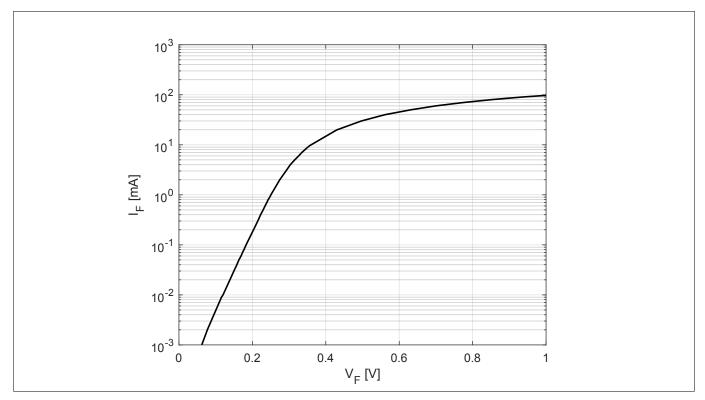


Figure 2 Forward current  $I_F$  vs. forward voltage  $V_F$ 

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## **Electrical performance in test fixture**

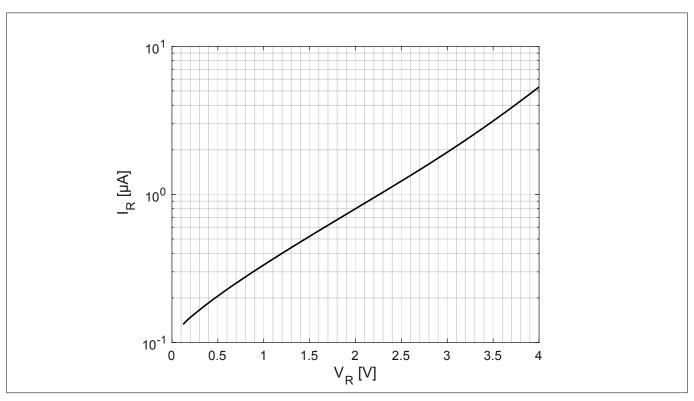


Figure 3 Reverse current  $I_R$  vs. reverse voltage  $V_R$ 

Note: The curves shown in this chapter have been generated using typical devices but shall not be understood as a guarantee that all devices have identical characteristic curves.



**Thermal characteristics** 

# **3** Thermal characteristics

Table 4 Thermal resistance

Parameter	Sym	Values			Unit	Note or test condition
	bol	Min.	Тур.	Max.		
Thermal resistance	R <sub>thJS</sub>	_	1020	_	K/W	T <sub>S</sub> = 48 °C 1)
(junction - soldering point)						

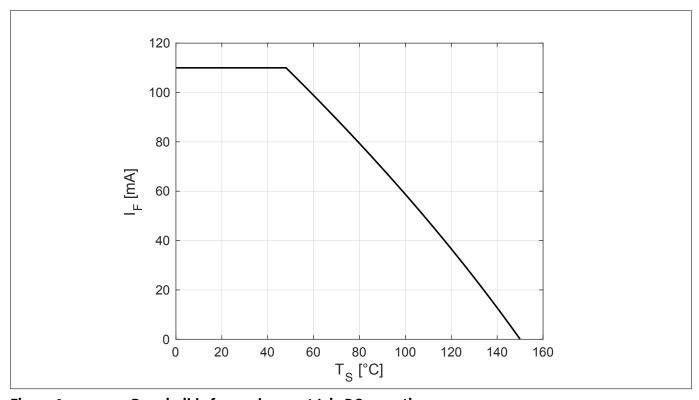


Figure 4 Permissible forward current I<sub>F</sub> in DC operation

 $<sup>^{1}~~</sup>$  For  $R_{\mathsf{thJS}}$  in other conditions refer to the curves in this chapter.

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## **Thermal characteristics**

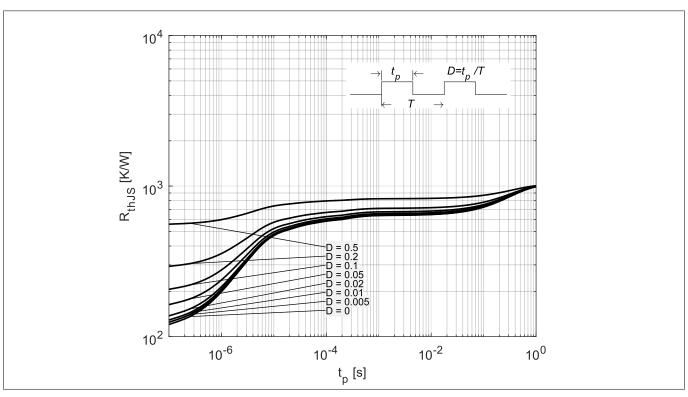


Figure 5 Thermal resistance  $R_{\text{thJS}}$  in pulse operation

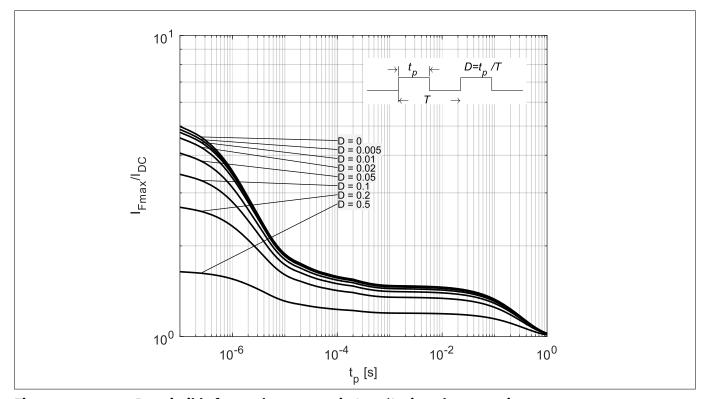


Figure 6 Permissible forward current ratio  $I_{Fmax}/I_{DC}$  in pulse operation



**Package information SOT143** 

# 4 Package information SOT143

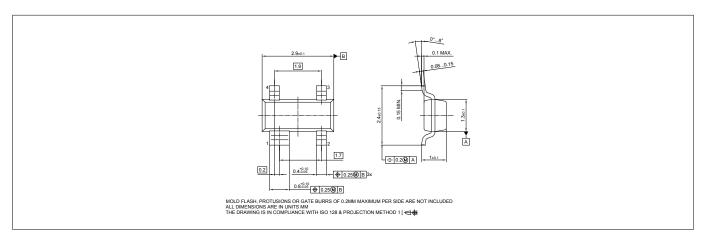


Figure 7 Package outline

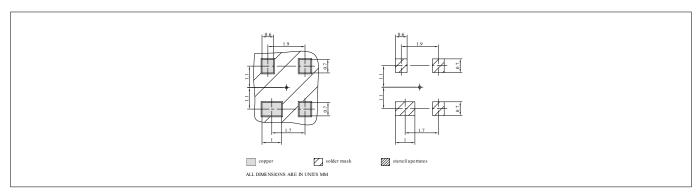


Figure 8 Foot print

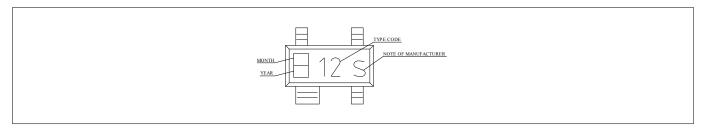


Figure 9 Marking layout example

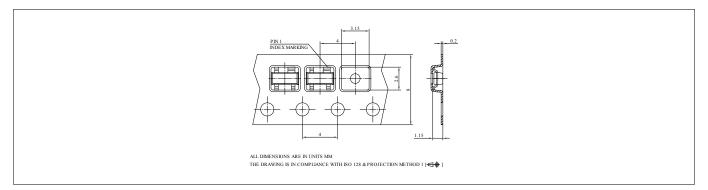


Figure 10 Tape dimensions

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**Revision history** 

# **Revision history**

Document version	Date of release	Description of changes						
1.0	2018-09-07	<ul> <li>Change from series datasheet to individual one</li> <li>Initial release of datasheet</li> <li>Typical values and curves updated to the values of the production (No product or process change behind)</li> <li>Typical values added</li> <li>Typical curves removed</li> </ul>						

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