

1664080

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RJ45 PCB connector, number of positions: 8, 100 Mbps, THT wave, degree of protection: IP20, Trav

Your advantages

- · Simplified Ethernet design process thanks to integrated filtering
- · Reduced PCB complexity and reduced EMC risks
- · Automated processing thanks to reflow solderability
- Ideal version for any application thanks to wide range of products
- Equipped for demanding areas of application thanks to industrial design

Commercial data

Item number	1664080
Packing unit	50 pc
Minimum order quantity	50 pc
Sales key	AB12
Product key	ABNAFA
GTIN	4067923203686
Weight per piece (including packing)	8.7 g
Weight per piece (excluding packing)	6.1 g
Customs tariff number	85366930
Country of origin	CN



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Technical data

Product properties

Product type	Data connector (device side)
Number of positions	8
Connection profile	RJ45
Housing shield springs	Yes
Number of slots	1
Shielded	yes
Insulation characteristics	
Overvoltage category	I
Degree of pollution	2

Electrical properties

Rated surge voltage	1.5 kV
Rated current	0.5 A
Insulation resistance	> 500 MΩ
Transmission speed	100 Mbps
Transmission speed	10/100 Mbps

Signaling

Status display present	no

Dimensions

Width	15.9 mm
Height	17.13 mm
Length	21.35 mm
Installed height	13.63 mm
Orientation to PCB	90.00 °
Data pin length	3.50 mm

Material specifications

Material Metal surface contact area (top layer)	Au (0.762 μm/30 μ")
Material Metal surface contact area (middle layer)	Ni (1,270 μm min / 50 μ" min)
Material Metal surface soldering area (middle layer)	Ni (1,016 μm min / 40 μ" min)
Material Metal surface soldering area (top layer)	Sn (3,048 µm min / 120 µ" min)
Material	Copper alloy (Housing (shielding))
	Ni (0.762 μm min / 30 μ" min) (Housing shielding (top layer))
Material Contact	Phosphor bronze
Material	LCP (Housing (female/insert))

Cable/line

Halogen-free	yes
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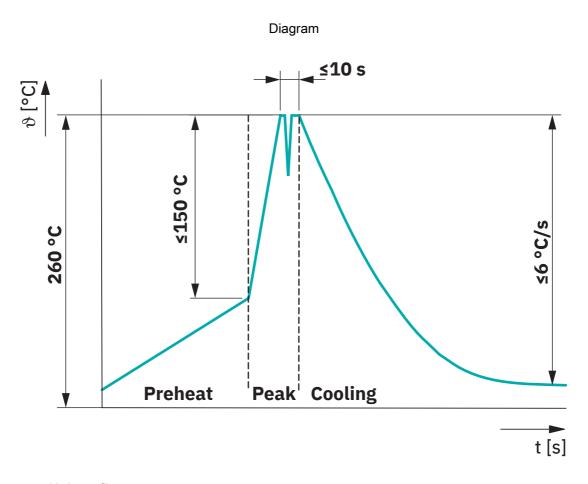
UL 94 V0
> 750
< 20 N
< 20 N
IEC 60603-7
IDOS
IP20
IP20
-40 °C 85 °C
-40 °C 85 °C
UL 94 V0
THT wave
260 °C



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Drawings



Classification wave soldering profile

Circuit diagram **ELECTRONICAL SPECIFICATIONS PCB SIDE JACK SIDE** 1. ACCORDING TO IEEE 802.3 **CAP 1000pF 2KV** 75Ω -(J8 (P1~P2):(J1~J2) = 1:1±5% (P3~P6):(J3~J6) = 1:1±5% 2. TURN RATIO @100KHz: SHIELD O **← J7** 3. PRIMARY INDUCTANCE: 350μH MIN @100KHz 0,1V 8mA DC BIAS 75Ω 75Ω 75Ω **√ J4** 4. DC RESISTANCE: 1,2 OHMS MAX **P6** 0 5. INSERTION LOSS: 1-100MHz -1,2dB MAX **P7** 0 -16dB MIN -12dB MIN -10dB MIN 1-30MHz 30-60MHz 60-80MHz 6. RETURN LOSS: **P3** C **P2** O **√** J2 7. CROSS TALK: -30dB MIN 1-100MHz 8. COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN

Circuit diagram

9. ISOLATION: PHY SIDE TO LINE SIDE: 1500VAC or 2250VDC



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Classifications

ETIM 9.0

ECLASS

	ECLASS-13.0	27460201
	ECLASS-15.0	27460201
ΕT	ТМ	

EC002637



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Environmental product compliance

EU RoHS

otions
substances above the limits
above 0.1 wt%
: 6

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