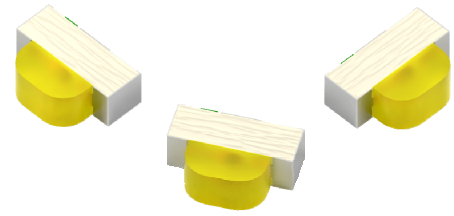


APA3010VWF/A

3.0 x 1.0 mm Right Angle SMD Chip LED Lamp



DESCRIPTIONS

- The source color devices are made with InGaN Light Emitting Diode
- Electrostatic discharge and power surge could damage the LEDs
- It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs
- All devices, equipments and machineries must be electrically grounded

FEATURES

- 3.0 x 2.0 x 1.0 mm right angle SMD LED, 1.0 mm thickness
- Low power consumption
- Wide viewing angle
- Ideal for back light and indicator
- Package: 2000 pcs / reel
- Moisture sensitivity level: 3
- Tinned pads for improved solderability
- Halogen-free
- RoHS compliant

APPLICATIONS

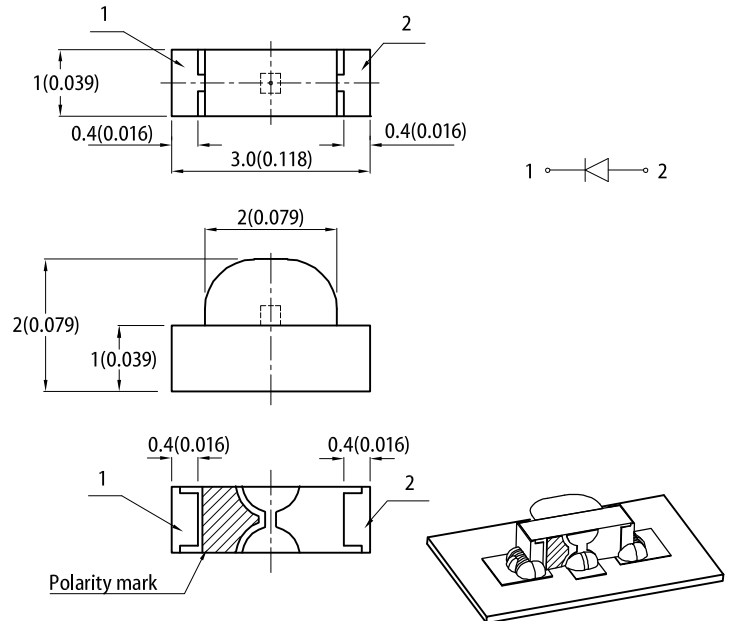
- Backlight
- Status indicator
- Home and smart appliances
- Wearable and portable devices
- Healthcare applications

ATTENTION

Observe precautions for handling electrostatic discharge sensitive devices

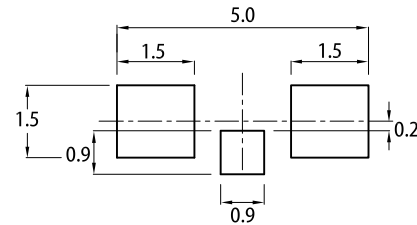


PACKAGE DIMENSIONS



RECOMMENDED SOLDERING PATTERN

(units : mm; tolerance : ± 0.1)



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.15(0.006")$ unless otherwise noted.
3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.
4. The device has a single mounting surface. The device must be mounted according to the specifications.
5. For right angle SMD LEDs, the solder stencil should be at least 5mil in thickness, to prevent poor solder wetting due to insufficient solder paste.

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	Iv (mcd) @ 20mA ^[2]		Viewing Angle ^[1]
			Min.	Typ.	2θ1/2
APA3010VWF/A	White (InGaN)	Yellow Fluorescent	280	400	160°

Notes:
 1. $\theta 1/2$ is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
 2. Luminous intensity / luminous flux: $\pm 15\%$.
 3. Luminous intensity value is traceable to CIE127-2007 standards.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit
			Typ.	Max.	
Chromaticity Coordinates x I _F = 20mA	x ^[1]	White	0.31	-	
Chromaticity Coordinates y I _F = 20mA	y ^[1]	White	0.31	-	
Forward Voltage I _F = 20mA	V _F ^[2]	White	3.3	4.0	V
Reverse Current (V _R = 5V)	I _R	White	-	50	μA
Temperature Coefficient of x I _F = 20mA, -10°C ≤ T ≤ 85°C	TC _x	White	-0.17	-	10 ⁻³ /°C
Temperature Coefficient of y I _F = 20mA, -10°C ≤ T ≤ 85°C	TC _y	White	-0.18	-	10 ⁻³ /°C
Temperature Coefficient of V _F I _F = 20mA, -10°C ≤ T ≤ 85°C	TC _V	White	-3.0	-	mV/°C

Notes:

1. Measurement tolerance of the chromaticity coordinates is ±0.01.

2. Forward voltage: ±0.1V.

3. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Symbol	Value	Unit
Power Dissipation	P _D	120	mW
Reverse Voltage	V _R	5	V
Junction Temperature	T _j	100	°C
Operating Temperature	T _{op}	-40 to +85	°C
Storage Temperature	T _{stg}	-40 to +85	°C
DC Forward Current	I _F	30	mA
Peak Forward Current	I _{FP} ^[1]	100	mA
Electrostatic Discharge Threshold (HBM)	-	250	V
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[2]	680	°C/W
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[2]	540	°C/W

Notes:

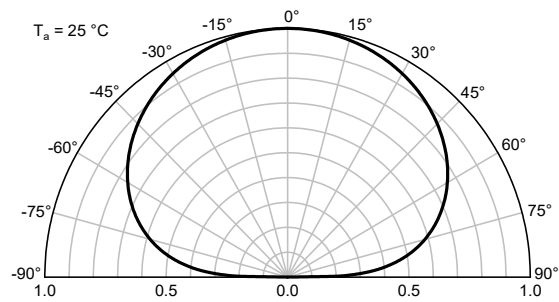
1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. R_{th JA}, R_{th JS} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad).

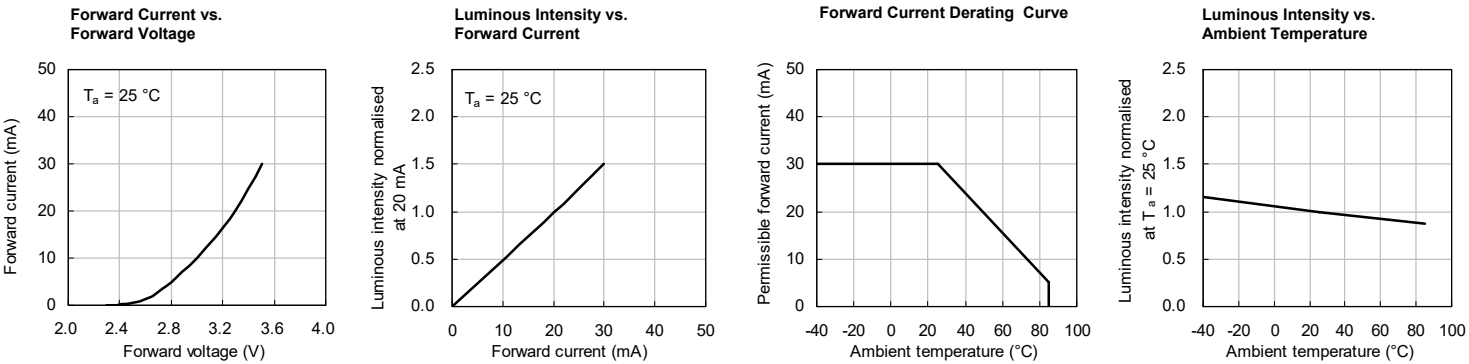
3. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA

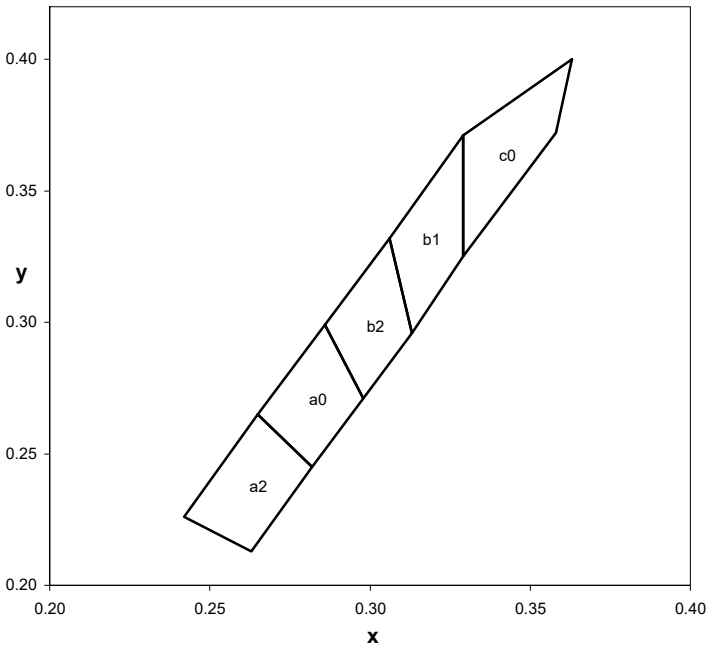
SPATIAL DISTRIBUTION



WHITE



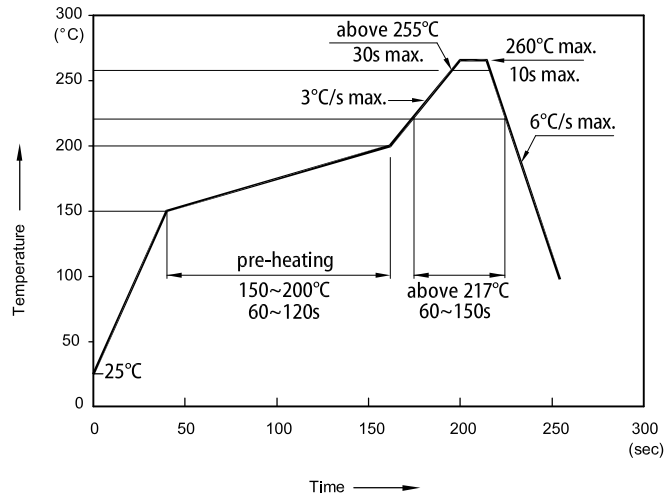
CIE CHROMATICITY DIAGRAM



		x y			x y
a2	0.263	0.213	a0	0.282	0.245
	0.282	0.245		0.298	0.271
	0.265	0.265		0.286	0.299
	0.242	0.226		0.265	0.265
b2	0.298	0.271	b1	0.313	0.296
	0.313	0.296		0.329	0.325
	0.306	0.332		0.329	0.371
	0.286	0.299		0.306	0.332
c0	0.329	0.325			
	0.358	0.372			
	0.363	0.400			
	0.329	0.371			

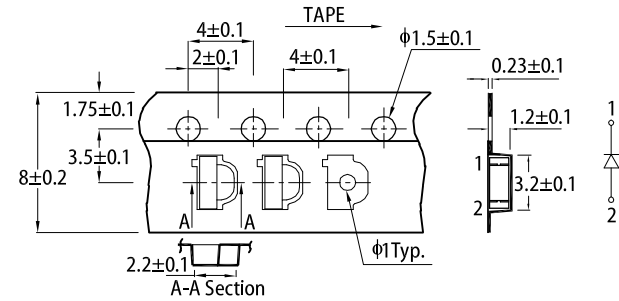
Notes:
Shipment may contain more than one chromaticity regions.
Orders for single chromaticity region are generally not accepted.
Measurement tolerance of the chromaticity coordinates is ± 0.01 .

REFLOW SOLDERING PROFILE for LEAD-FREE SMD PROCESS

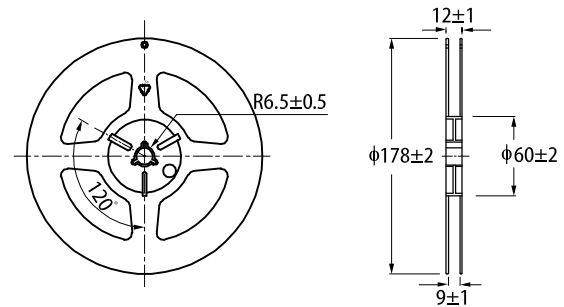


Notes:
 1. Don't cause stress to the LEDs while it is exposed to high temperature.
 2. The maximum number of reflow soldering passes is 2 times.
 3. Reflow soldering is recommended. Other soldering methods are not recommended as they might cause damage to the product.

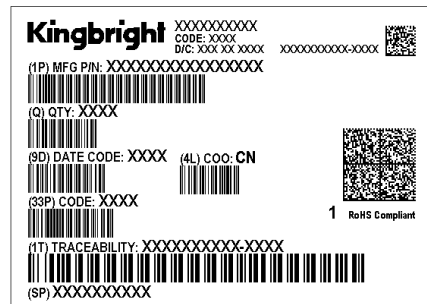
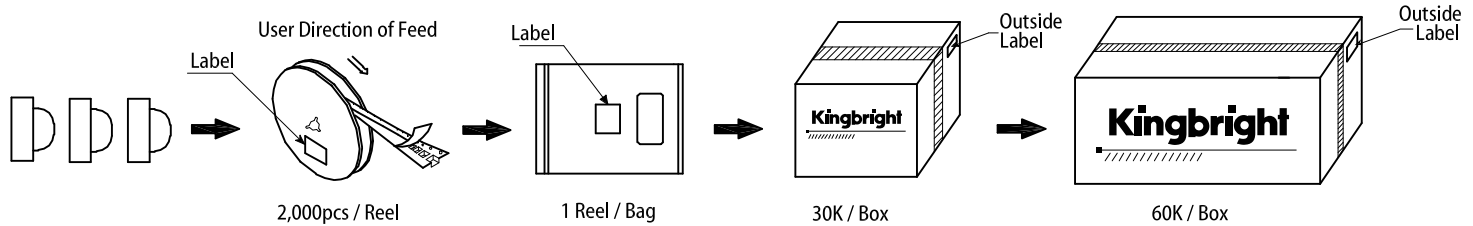
TAPE SPECIFICATIONS (units : mm)



REEL DIMENSION (units : mm)



PACKING & LABEL SPECIFICATIONS



PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
- When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
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