

**Microchip****Filter specification****TFS 192F****1/5****Measurement condition**

|                          |              |        |
|--------------------------|--------------|--------|
| Ambient temperature:     | 23           | °C     |
| Input power level:       | 0            | dBm    |
| Terminating impedance: * |              |        |
| Input:                   | 130 $\Omega$ | -25 pF |
| Output:                  | 53 $\Omega$  | -43 pF |

**Characteristics**

## Remark:

The reference level for the relative attenuation  $a_{rel}$  of the TFS192F is the minimum of the pass band attenuation. This value is defined as the insertion loss  $a_e$ . The nominal frequency  $f_N$  is fixed at 192,0 MHz without any tolerance. The values of relative attenuation  $a_{rel}$  are guaranteed for the whole operating temperature range. The frequency shift of the filter in the operating temperature range is included in the production tolerance scheme.

| D a t a   |           | typ. value |         | tolerance / limit   |       |         |
|---|-----------|------------|---------|---------------------|-------|---------|
| <b>Insertion loss</b><br>(reference level)                | $a_e$     | 12,7       | dB      | max.                | 13,5  | dB      |
| <b>Nominal frequency</b>                                  | $f_N$     |            |         |                     | 192,0 | MHz     |
| <b>Passband</b>   | PB        |            |         | $f_N \pm$           | 15,25 | MHz     |
| <b>Pass band ripple</b>                                   | p-p       | 0,4        | dB      | max.                | 1,0   | dB      |
| <b>Amplitude ripple over any 200kHz of the PB</b>         | p-p       | 0,15       | dB      | max.                | 0,4   | dB      |
| <b>Bandwidth</b>  | BW        |            |         |                     |       |         |
| 1 dB  |           | 36         | MHz     | min.                | 30,5  | MHz     |
| 3 dB  |           | 37,9       | MHz     | max.                | 44,0  | MHz     |
| 40 dB   |           | 48,8       | MHz     | max.                | 66,0  | MHz     |
| <b>Relative attenuation</b>                               | $a_{rel}$ |            |         |                     |       |         |
| $f_N$ ... $f_N \pm$ 15,25 MHz                             |           | 0,4        | dB      | max.                | 1     | dB      |
| $f_N \pm$ 22,0 MHz ... $f_N \pm$ 33,0 MHz                 |           | 16         | dB      | min.                | 3     | dB      |
| $f_N \pm$ 33,0 MHz ... $f_N \pm$ 62,0 MHz                 |           | 42         | dB      | min.                | 40    | dB      |
| $f_N \pm$ 62,0 MHz ... $f_N \pm$ 98,0 MHz                 |           | 51         | dB      | min.                | 45    | dB      |
| <b>Absolute group delay within PB</b>                     |           | 0,49       | $\mu$ s | max.                | 1     | $\mu$ s |
| <b>Group delay ripple within PB</b>                       | p-p       | 35         | ns      | max.                | 100   | ns      |
| <b>Group delay ripple over any 200kHz of the PB</b>       | p-p       | 20         | ns      | max.                | 30    | ns      |
| <b>Return loss ( 50<math>\Omega</math> test circuit )</b> | in PB     | 12         | dB      | min.                | 9     | dB      |
| <b>Input power level</b>                                  |           |            |         | max.                | 18    | dBm     |
| <b>Operating temperature range</b>                        | OTR       |            |         | - 40 °C ... + 85 °C |       |         |
| <b>Storage temperature range</b>                          |           |            |         | - 40 °C ... + 85 °C |       |         |
| <b>Temperature coefficient of frequency</b>               | $TC_f$ ** | -94        | ppm/K   | -                   |       |         |

\*) The terminating impedances depend on parasitics and q-values of matching elements and the board used, and are to be understood as reference values only. Should there be additional questions do not hesitate to ask for an application note or contact our design team.

\*\*)  $\Delta f(\text{Hz}) = TC_f(\text{ppm/K}) \times (T - T_0) \times f_{cat}(\text{MHz})$ .

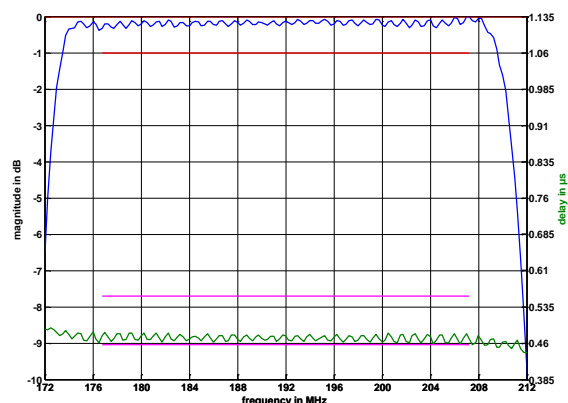
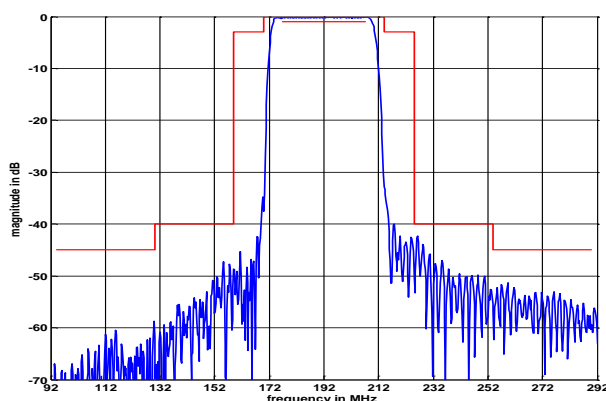
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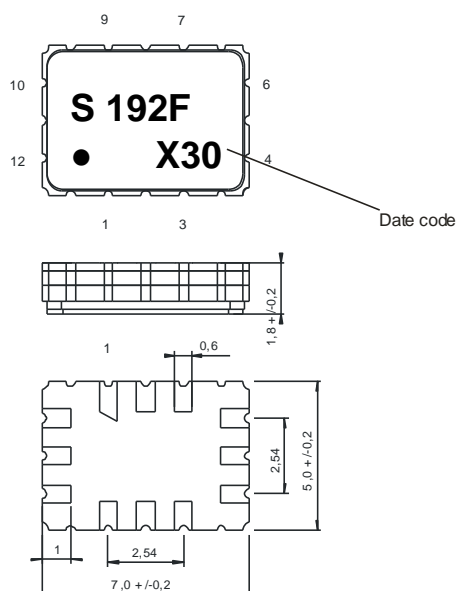
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## Filter characteristic



## Construction and pin connection

(All dimensions in mm)



|    |                          |
|----|--------------------------|
| 1  | Ground                   |
| 2  | Ground                   |
| 3  | Ground                   |
| 4  | Output                   |
| 5  | Ground                   |
| 6  | Output RF Return         |
| 7  | Ground                   |
| 8  | Ground                   |
| 9  | Ground                   |
| 10 | Input                    |
| 11 | Ground                   |
| 12 | Input RF Return / Ground |

Date code: Year + week

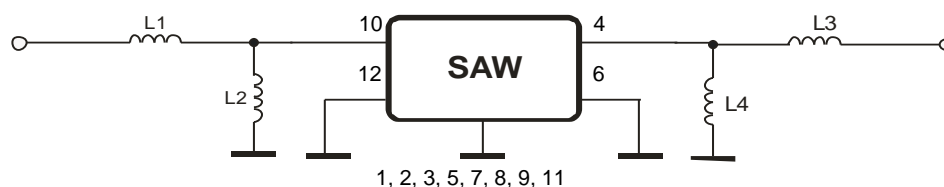
X 2009

A 2010

B 2011

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## 50 Ohm Test circuit



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**Stability characteristics, reliability**

After the following tests the filter shall meet the whole specification:

1. Shock: 500g, 1 ms, half sine wave, 3 shocks each plane;  
DIN IEC 68 T2 - 27
2. Vibration: 10 Hz to 500 Hz, 0,35 mm or 5 g respectively, 1 octave per min, 10 cycles per plan, 3 plans;  
DIN IEC 68 T2 - 6
3. Change of temperature: -55 °C to 125°C / 30 min. each / 10 cycles  
DIN IEC 68 part 2 – 14 Test N
4. Resistance to solder heat (reflow): reflow possible: three times max.;  
for temperature conditions refer to the attached "Air reflow temperature conditions" on page 4;
5. ESD MIL-STD-883E using coupling network of ISO 10605 and EN 6100-4-2  
HBM:250V;

This filter is RoHS compliant (2002/95/EG, 2005/618/EG)

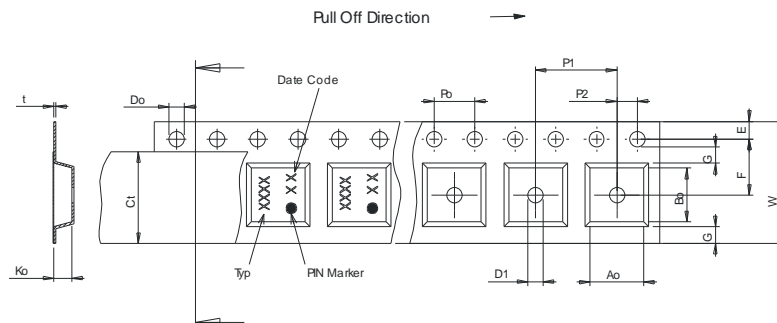
**Packing**

Tape & Reel: IEC 286 – 3, with exception of value for N and minimum bending radius;  
tape type II, embossed carrier tape with top cover tape on the upper side;

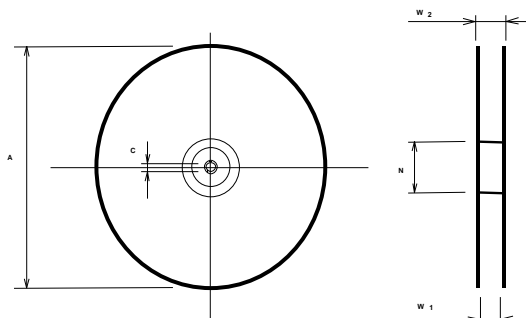
|   |             |
|---|-------------|
| max. pieces of filters per reel:                    | 3000        |
| reel of empty components at start:                  | min. 300 mm |
| reel of empty components at start including leader: | min. 500 mm |
| trailer:  | min. 300 mm |

**Tape (all dimensions in mm)**

|         |                   |
|---------|-------------------|
| W       | : 16,00 +0,3/-0,1 |
| Po      | : 4,00 ± 0,1      |
| Do      | : 1,50 +0,1/-0    |
| E       | : 1,75 ± 0,1      |
| F       | : 7,50 ± 0,1      |
| G(min)  | : 0,75            |
| P2      | : 2,00 ± 0,1      |
| P1      | : 8,00 ± 0,1      |
| D1(min) | : 1,50            |
| Ao      | : 5,40 ± 0,1      |
| Bo      | : 7,60 ± 0,1      |
| Ct      | : 13,5 ± 0,1      |

**Reel (all dimensions in mm)**

|         |                  |
|---------|------------------|
| A       | : 330            |
| W1      | : 16,4 +2/-0     |
| W2(max) | : 22,4           |
| N(min)  | : 50             |
| C       | : 13,0 +0,5/-0,2 |



The minimum bending radius is 45 mm.

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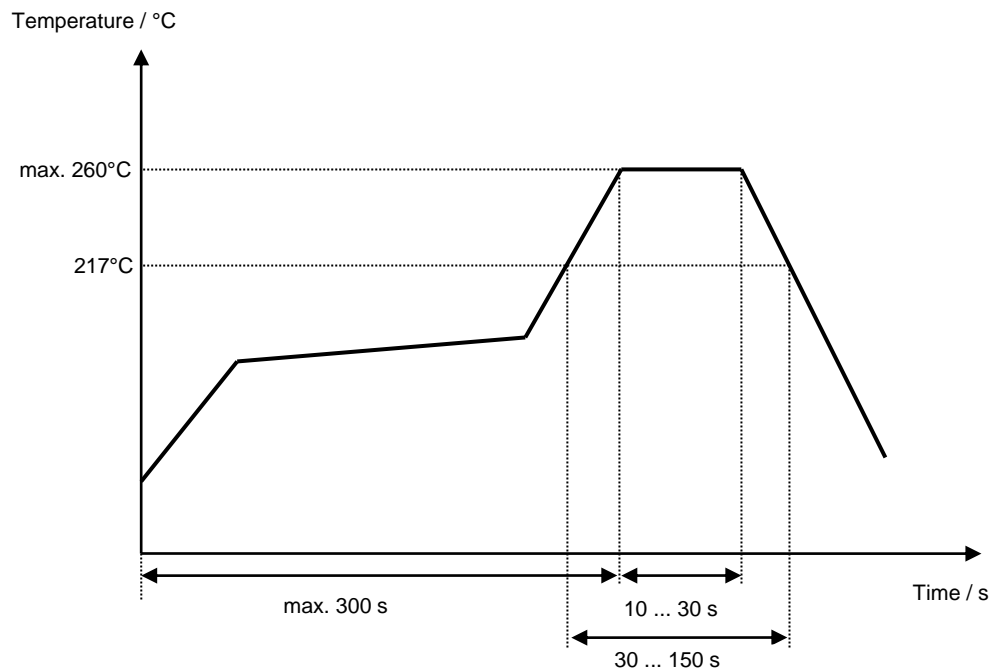
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**Air reflow temperature conditions**

| <b>Conditions</b>                          | <b>Exposure</b>             |
|--|-----------------------------|
| Average ramp-up rate (30°C to 217°C)       | less than 3°C/second        |
| > 100°C                                    | between 300 and 600 seconds |
| > 150°C                                    | between 240 and 500 seconds |
| > 217°C                                    | between 30 and 150 seconds  |
| Peak temperature                           | max. 260°C                  |
| Time within 5°C of actual peak temperature | between 10 and 30 seconds   |
| Cool-down rate (Peak to 50°C)              | less than 6°C/second        |
| Time from 30°C to Peak temperature         | no greater than 300 seconds |

**Chip-mount air reflow profile**

**Microchip****Filter specification****TFS 192F****5/5****History**

| <b>Version</b> | <b>Reason of Changes</b>  | <b>Name</b> | <b>Date</b> |
|----------------|---|-------------|-------------|
| 1.0            | - Generation of development specification   | Strehl      | 02.02.2009  |
| 1.1            | - Changed return loss   | Chilla      | 16.02.2009  |
| 1.2            | - Created filter specification<br>- Added terminating impedance<br>- Added typical values<br>- Added filter characteristic<br>- Added filter test circuit | Chilla      | 20.07.2009  |

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