

dsPIC33FJ256MC710 100-pin to 100-pin TQFP Plug-In Module (PIM) Information Sheet

OVERVIEW

The dsPIC33FJ256MC710 PIM is designed to demonstrate the capabilities of the dsPIC33FJ256MC710 family of motor control devices using development boards such as the Explorer 16 Development Board, which support 100-pin PIM interfaces.

The dsPIC33FJ256MC710 is a high-performance 16-bit Digital Signal Controller in a 100-pin TQFP package.

Table 1 shows the mapping between the 100-pin PIM interface board functions and the device pins.

TABLE 1: 100-PIN TO 100-PIN PIM

Device Pin #	dsPIC33FJ256MC710 Functional Description	PIM Pin #
1	COFS/RG15	1
2	VDD	2
3	AN29/RE5	3
4	AN30/RE6	4
5	AN31/RE7	5
6	AN16/T2CK/T7CK/RC1	6
7	AN17/T3CK/T6CK/RC2	7
8	AN18/T4CK/T9CK/RC3	8
9	AN19/T5CK/T8CK/RC4	9
10	SCK2/CN8/RG6	10
11	SDI2/CN9/RG7	11
12	SDO2/CN10/RG8	12
13	MCLR	13
14	SS2/CN11/RG9	14
15	VSS	15
16	VDD	16
17	TMS/RA0	17
18	AN20/INT1/RA12	18
19	AN21/INT2/RA13	19
20	AN5/CN7/RB5	20
21	AN4/CN6/RB4	21
22	AN3/CN5/RB3	22
23	AN2/SS1/CN4/RB2	23
24	PGC3/EMUC3/AN1/CN3/RB1	24
25	PGD3/EMUD3/AN0/CN2/RB0	25
26	PGC1/EMUC1/AN6/OCFA/RB6	26
27	PGD1/EMUD1/AN7/RB7	27
28	VREF-/RA9	28
29	VREF+/RA10	29
30	AVDD	30
31	AVSS	31
32	AN8/RB8	32
33	AN9/RB9	33

Device Pin #	dsPIC33FJ256MC710 Functional Description	PIM Pin #
35	AN11/RB11	35
36	VSS	36
37	VDD	37
38	TCK/RA1	38
39	U2RTS/RF13	39
40	U2CTS/RF12	40
41	AN12/RB12	41
42	AN13/RB13	42
43	AN14/RB14	43
44	AN15/OCFB/CN12/RB15	44
45	VSS	45
46	VDD	46
47	IC7/U1CTS/CN20/RD14	47
48	IC8/U1RTS/CN21/RD15	48
49	U2RX/CN17/RF4	49
50	U2TX/CN18/RF5	50
51	U1TX/RF3	51
52	U1RX/RF2	52
53	SDO1/RF8	53
54	SDI1/RF7	54
55	SCK1/INT0/RF6	55
56	SDA1/RG3	56
57	SCL1/RG2	57
58	SCL2/RA2	58
59	SDA2/RA3	59
60	TDI/RA4	60
61	TDO/RA5	61
62	VDD	62
63	OSC1/CLKIN/RC12	63
64	OSC2/CLKO/RC15	64
65	VSS	65
66	INT3/RA14	66
67	INT4/RA15	67

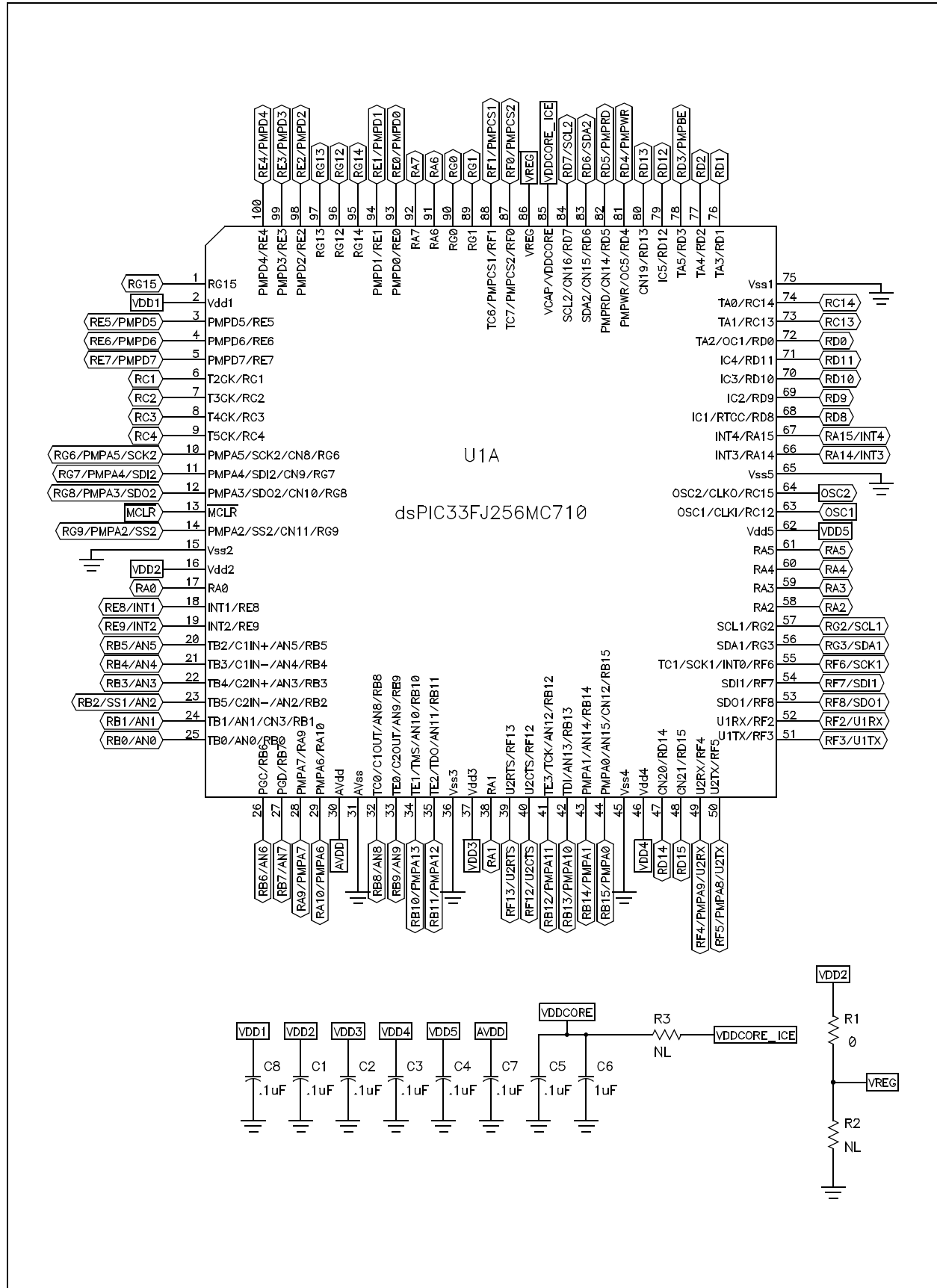
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TABLE 1: 100-PIN TO 100-PIN PIM (CONTINUED)

Device Pin #	dsPIC33FJ256MC710 Functional Description	PIM Pin #	Device Pin #	dsPIC33FJ256MC710 Functional Description	PIM Pin #
34	AN10/RB10	34	68	IC1/RD8	68
69	IC2/RD9	69	85	VDDCORE	85
70	IC3/RD10	70	86	VDD	86
71	IC4/RD11	71	87	C1RX/RF0	87
72	OC1/RD0	72	88	C1TX/RF1	88
73	PGD2/EMUD2/SOSCI/CN1/RC13	73	89	C2TX/RG1	89
74	PGC2/EMUC2/SOSCO/T1CK/CN0/RC14	74	90	C2RX/RG0	90
75	Vss	75	91	AN22/CN22/RA6	91
76	OC2/RD1	76	92	AN23/CN23/RA7	92
77	OC3/RD2	77	93	AN24/RE0	93
78	OC4/RD3	78	94	AN25/RE1	94
79	IC5/RD12	79	95	C5CK/RG14	95
80	IC6/CN19/RD13	80	96	CSDI/RG12	96
81	OC5/CN13/RD4	81	97	CSDO/RG13	97
82	OC6/CN14/RD5	82	98	AN26/RE2	98
83	OC7/CN15/RD6	83	99	AN27/RE3	99
84	OC8/CN16/RD7	84	100	AN28/RE4	100

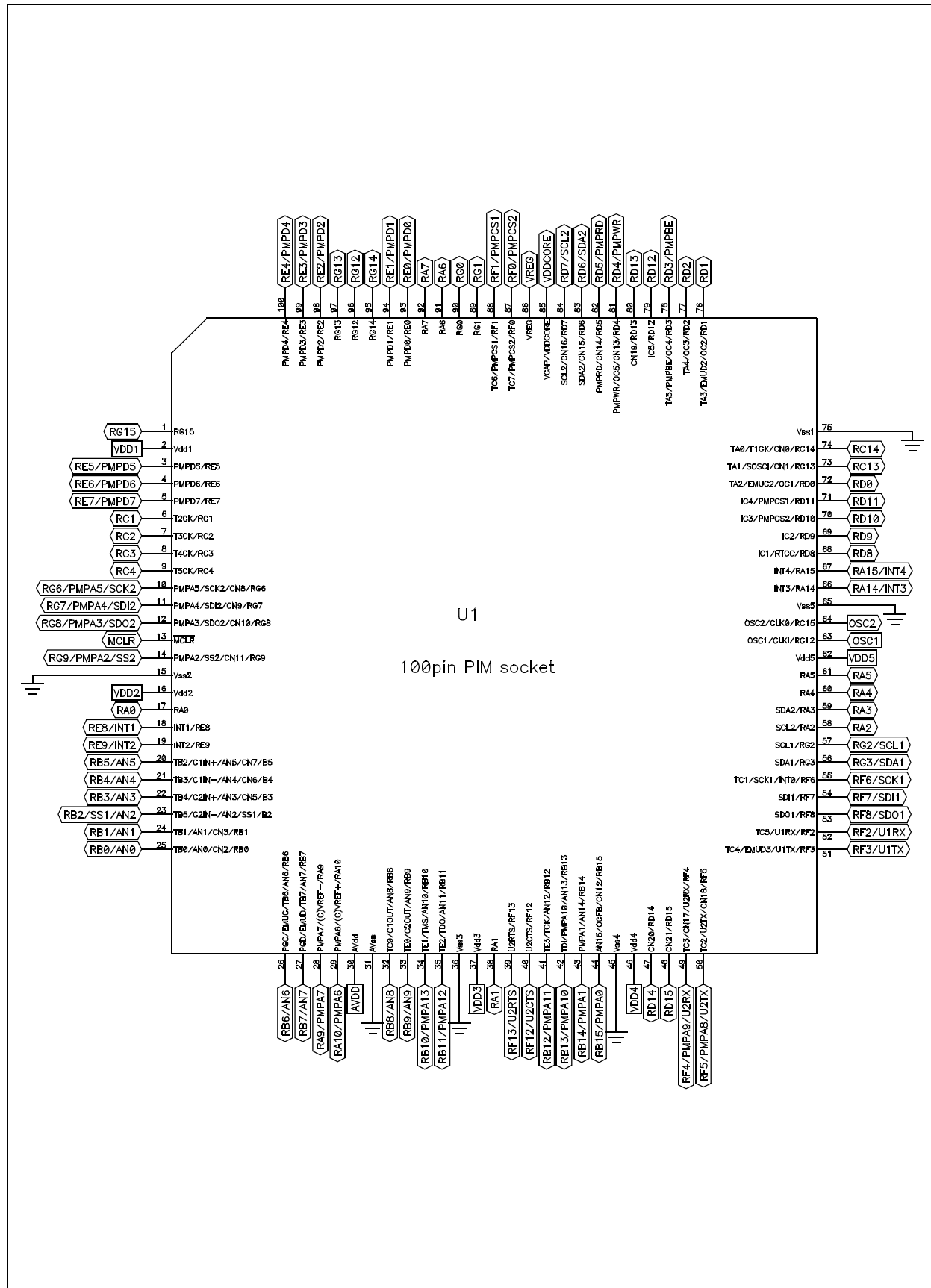
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FIGURE 1: 100-PIN DEVICE SCHEMATIC



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FIGURE 2: 100-PIN PIM SOCKET SCHEMATIC



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