



1200V N-CHANNEL SILICON CARBIDE POWER MOSFET

Product Summary

BV _{DSS}	Rds(on) Max	I _D T _C = +25°C
1200V	$37m\Omega$ @ $V_{GS} = 18V$	75A

Description and Applications

This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

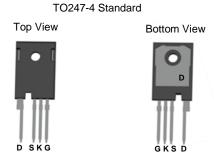
- EV high-power DC-DC converters
- EV charging systems
- AC-DC traction inverters
- Solar inverters
- · Automotive motor drivers

Features and Benefits

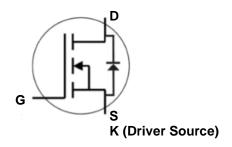
- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: TO247-4
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 6.6 grams (Approximate)



Pin Configuration



Internal Schematic

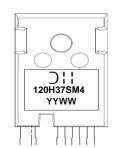
Ordering Information (Note 4)

Orderable Part Number	Deckers		Packing		
Orderable Part Number	Package	Qty.	Carrier		
DMWSH120H37SM4	TO247-4 Standard	30 Pieces	Tube		

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



D: I = Manufacturer's Marking
120H37SM4 = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 24 = 2024)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		VDSS	1200	V
Gate-Source Voltage		Vgs	-10/+22	V
Gate-Source Voltage (Recommended Operating Values)		Vgs	-5/+18	V
Gate-Source Transient Voltage, tp < 1µs, t ≤ 10 Hours over Lifetime		Vgs	-11/+25	V
Continuous Drain Current (Notes 5, 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	75 53	А
Continuous Diode Forward Current (Note 5)		Is	66	Α
Pulsed Source Current (Pulse Width tp Limited by T _{J Max}) (Note 5)		Ism	168	Α
Pulsed Drain Current (Pulse Width t _P Limited by T _{J Max}) (Note 5)		I _{DM}	168	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Dawer Dissination (Note 5)	Tc = +25°C	D-	315	W	
Total Power Dissipation (Note 5)	Tc = +100°C	PD	157		
Thermal Resistance, Junction to Ambient (Note 7)		R _{0JA}	29.5	9000	
Thermal Resistance, Junction to Case (Note 5)		Rejc	0.47	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C	

Notes:

- 5. Device mounted on an infinite heatsink.
- 5. Device mounted on an infinite reasons.6. Drain current limited by maximum junction temperature.7. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

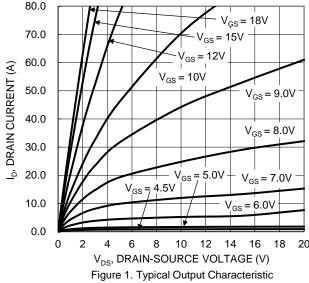
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	1200	_	_	V	Vgs = 0, ID = 1mA	
Zero Gate Voltage Drain Current	IDSS	_	_	10	μΑ	V _{DS} = 1200V, V _{GS} = 0	
Gate-Source Leakage	Igss	_	_	±250	nA	Vgs = -10/+22V, Vps = 0	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	Vgs(TH)	2.04	2.5	4.4	V	V _{DS} = V _{GS} , I _D = 15mA	
Static Drain-Source On-Resistance	RDS(ON)	_	28.5	37	mΩ	V _G S = 18V, I _D = 30A	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	35	_	mΩ	$V_{GS} = 15V, I_D = 30A$	
Diode Forward Voltage	VsD	_	4.0	_	V	V _G S = -5V, I _S = 30A	
Transconductance	gfs	_	12	_	S	VDS = 10V, ID = 30A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	2320	_			
Output Capacitance	Coss	_	112	_			
Reverse Transfer Capacitance	Crss	_	6.4	_	pF	$V_{GS} = 0$, $V_{DS} = 800V$ $V_{AC} = 25mV$, $f = 1MHz$	
Effective Output Capacitance (Energy Related)	C _{O(ER)}	_	145	_			
Effective Output Capacitance (Time Related)	Co(TR)	_	216	_			
Coss Stored Energy	Eoss	_	45	_	μJ	1	
Turn-On Switching Energy (Body Diode Forward)	Eon	_	259	_	1	$V_{GS} = -5V/+18V$, $V_{DS} = 800V$ $R_g = 5\Omega$, $I_D = 30A$, $L = 157\mu H$	
Turn-Off Switching Energy (Body Diode Forward)	E _{OFF}	_	145	_	μJ		
Gate Resistance	Rg	_	1.3	_	Ω	V _{AC} = 100mV, f = 1MHz	
Total Gate Charge	Qg	_	135	_		V _{GS} = -5V/+18V, V _{DS} = 800V I _D = 30A	
Gate-Source Charge	Q _{gs}	_	46.4	_	nC		
Gate-Drain Charge	Q _{gd}	_	36.7	_			
Turn-On Delay Time	td(ON)	_	15.0	_		$V_{GS} = -5V/+18V$, $V_{DD} = 800V$ $R_g = 5\Omega$, $I_D = 30A$ Inductive Load	
Turn-On Rise Time	tr	_	24.6	_			
Turn-Off Delay Time	t _{D(OFF)}	_	34.5	_	ns		
Turn-Off Fall Time	tF	_	9.6	_		maddivo Load	
Body Diode Reverse-Recovery Time	trr	_	24.6	_	ns	5// // 200//	
Body Diode Reverse-Recovery Charge	Q _{RR}	_	181	_	nC	$V_{GS} = -5V$, $V_{DS} = 800V$ - $I_{D} = 30A$, $di/dt = 1000A/\mu s$	
Body Diode Reverse-Recovery Current	I _{RRM}	_	13.5	_	Α	110 = 30A, αι/αι = 1000A/μS	

Notes:

^{8.} Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to production testing.







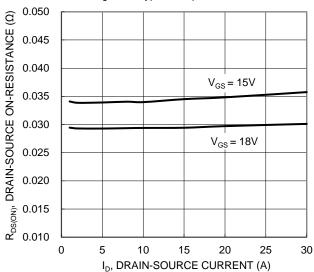


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

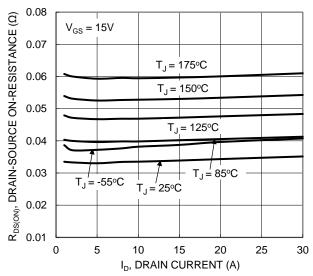
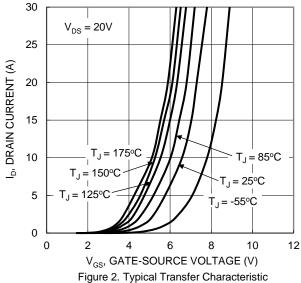
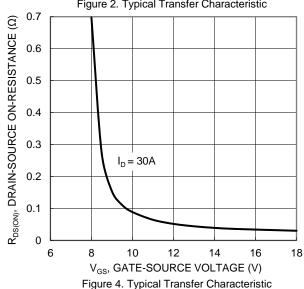


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





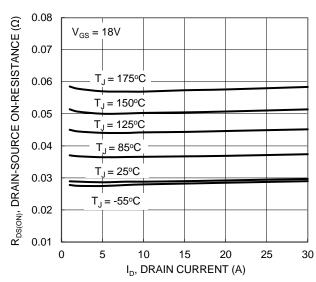


Figure 6. Typical On-Resistance vs. Drain Current and Junction Temperature





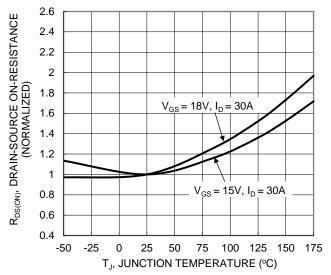


Figure 7. On-Resistance Variation with Junction Temperature

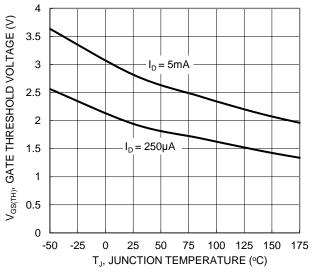


Figure 9. Gate Threshold Variation vs. Junction Temperature

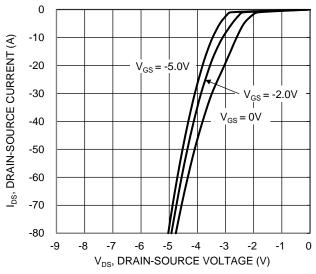


Figure 11. Body Diode Characteristic at 25°C

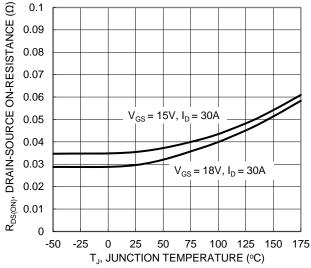


Figure 8. On-Resistance Variation with Junction Temperature

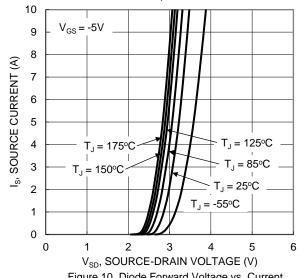


Figure 10. Diode Forward Voltage vs. Current

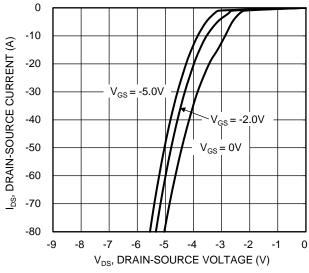
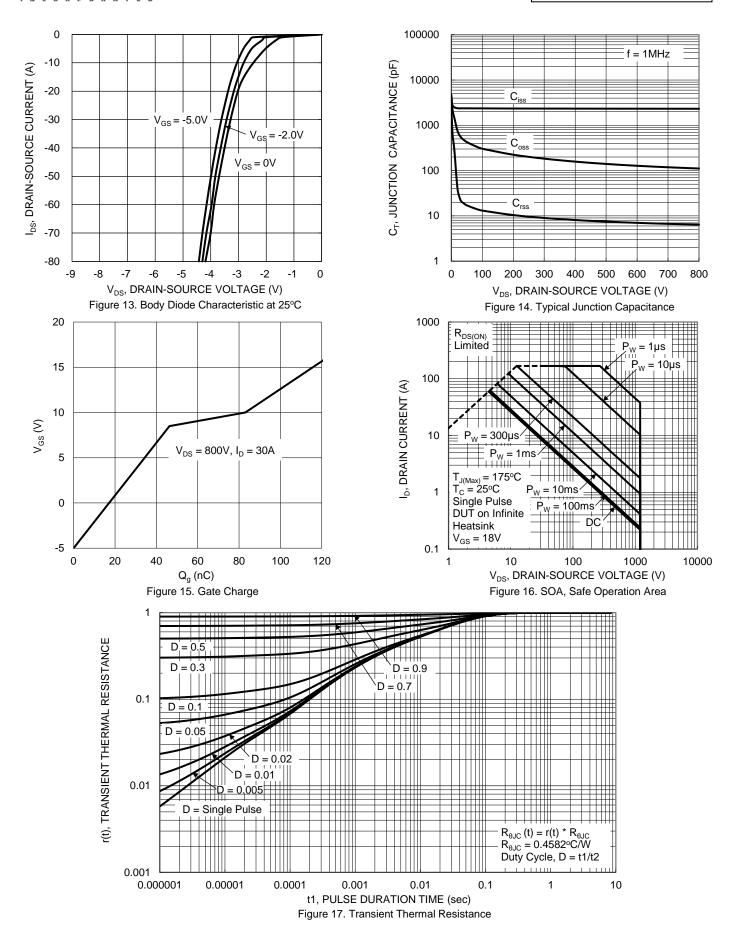


Figure 12. Body Diode Characteristic at -55°C







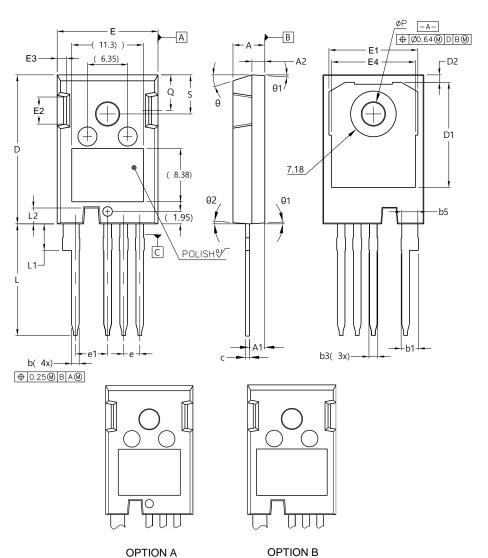


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

(TOP VIEW)

TO247-4 Standard



TO247-4 Standard				
Dim	Min	Max		
Α	4.83	5.21		
A1	2.29	2.54		
A2	1.91	2.16		
b	1.07	1.33		
b1	2.39	2.94		
b3	1.07	1.60		
b5	2.39	2.69		
С	0.55	0.68		
D	23.30	23.60		
D1	16.25	17.65		
D2	0.95	1.25		
Е	15.75	16.30		
E1	13.10	14.15		
E2	3.68	5.10		
E3	1.00	1.90		
E4	12.38	13.43		
е	2.54 BSC			
e1	5.08 BSC			
L	17.31	17.82		
L1	3.97	4.37		
L2	2.35	2.65		
ØP	3.51	3.65		
Q	5.49	6.00		
S	6.04	6.30		
θ	17.5°- 20° REF			
θ1	3.5°- 5° REF			
θ2	θ2 4°- 5° REF			
All Dimensions in mm				

(TOP VIEW)



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