

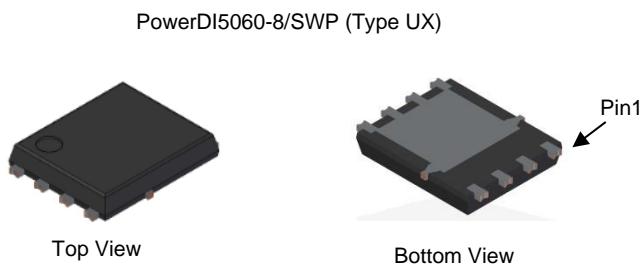
## Product Summary

BV <sub>DSS</sub>	R <sub>D(on)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
100V	4.9mΩ @ V <sub>GS</sub> = 10V	107A
	6.7mΩ @ V <sub>GS</sub> = 4.5V	92A

## Description

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Motor control
- DC-DC converters
- Power management



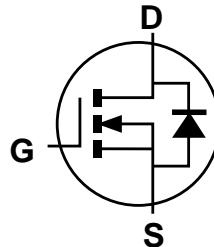
## Features

- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R<sub>D(on)</sub> – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH10H4M5LPSWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

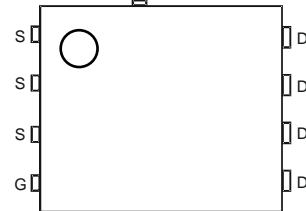
<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

- Package: PowerDI®5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish — Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.097 grams (Approximate)



Internal Schematic



Top View  
Pin Configuration

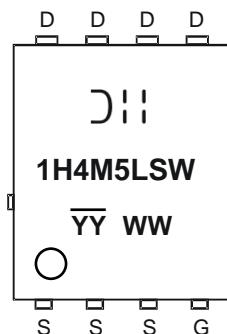
## Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMTH10H4M5LPSWQ-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



DII = Manufacturer's Marking  
1H4M5LSW = Product Type Marking Code  
YYWW = Date Code Marking  
YY = Last Two Digits of Year (ex: 25 = 2025)  
WW = Week Code (01 to 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	100	V
Gate-Source Voltage			$V_{GSS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} = 10\text{V}$ (Note 5)	Steady State	$T_A = +25^\circ\text{C}$	$I_D$	20	A
		$T_A = +100^\circ\text{C}$		14	
Continuous Drain Current, $V_{GS} = 10\text{V}$ (Note 6)	Steady State	$T_C = +25^\circ\text{C}$	$I_D$	107	A
		$T_C = +100^\circ\text{C}$		76	
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, $T_C = +25^\circ\text{C}$ , Package Limited)			$I_{DM}$	428	A
Maximum Continuous Body Diode Forward Current (Note 6)			$I_S$	107	A
Pulsed Body Diode Forward Current (10 $\mu\text{s}$ Pulse, $T_C = +25^\circ\text{C}$ , Package Limited)			$I_{SM}$	428	A
Avalanche Current (Note 7) $L=0.3\text{mH}$			$I_{AS}$	40	A
Avalanche Energy (Note 7) $L=0.3\text{mH}$			$E_{AS}$	240	$\text{mJ}$

**Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^\circ\text{C}$	$P_D$	4.7	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	32	$^\circ\text{C}/\text{W}$
Total Power Dissipation (Note 6)	$T_C = +25^\circ\text{C}$	$P_D$	136	W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	1.1	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range		$T_J, T_{STG}$	-55 to +175	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 7)						
Drain-Source Breakdown Voltage	$BV_{DSS}$	100	—	—	V	$V_{GS} = 0\text{V}, I_D = 10\text{mA}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS</b> (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1.3	—	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	2.4	4.9	$\text{m}\Omega$	$V_{GS} = 10\text{V}, I_D = 30\text{A}$
		—	3.9	6.7		$V_{GS} = 4.5\text{V}, I_D = 20\text{A}$
Diode Forward Voltage	$V_{SD}$	—	0.8	1.2	V	$V_{GS} = 0\text{V}, I_S = 30\text{A}$
<b>DYNAMIC CHARACTERISTICS</b> (Note 8)						
Input Capacitance	$C_{iss}$	—	4843	—	$\text{pF}$	$V_{DS} = 50\text{V}, V_{GS} = 0\text{V}$ $f = 1\text{MHz}$
Output Capacitance	$C_{oss}$	—	1302	—		
Reverse Transfer Capacitance	$C_{rss}$	—	25.5	—		
Gate Resistance	$R_g$	—	2.1	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge	$Q_g$	—	80	—	$\text{nC}$	$V_{DD} = 50\text{V}, I_D = 30\text{A},$ $V_{GS} = 10\text{V}$
Gate-Source Charge	$Q_{gs}$	—	14	—		
Gate-Drain Charge	$Q_{gd}$	—	18	—		
Turn-On Delay Time	$t_{D(ON)}$	—	9	—	$\text{ns}$	$V_{DD} = 50\text{V}, V_{GS} = 10\text{V},$ $I_D = 30\text{A}, R_g = 4.7\Omega, R_L = 1.1\Omega$
Turn-On Rise Time	$t_R$	—	26	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	76	—		
Turn-Off Fall Time	$t_F$	—	50	—		
Reverse-Recovery Time	$t_{RR}$	—	63	—	$\text{ns}$	$I_F = 22.5\text{A}, \text{di}/\text{dt} = 100\text{A}/\mu\text{s}$
Reverse-Recovery Charge	$Q_{RR}$	—	133	—	$\text{nC}$	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

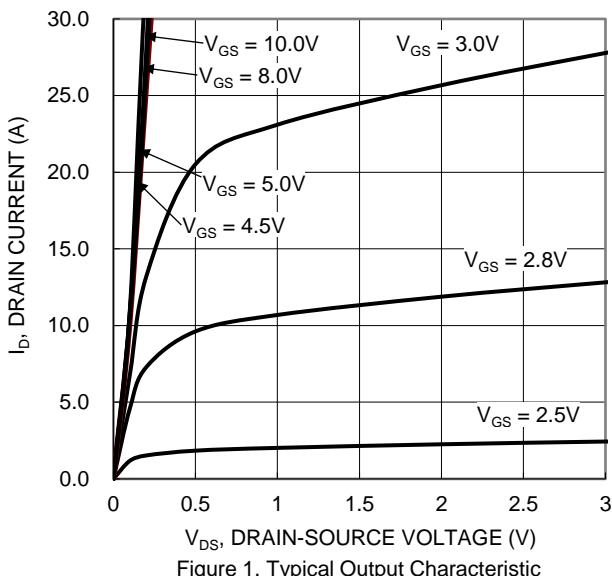


Figure 1. Typical Output Characteristic

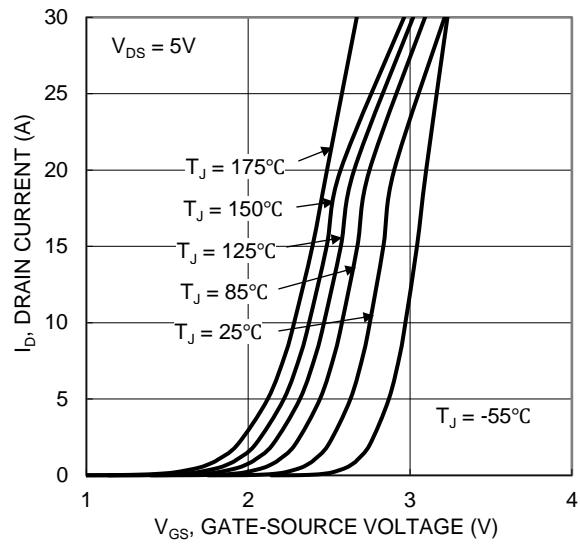


Figure 2. Typical Transfer Characteristic

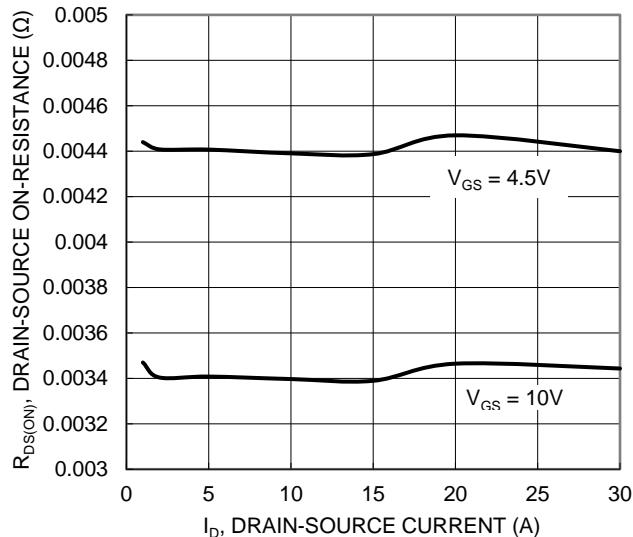


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

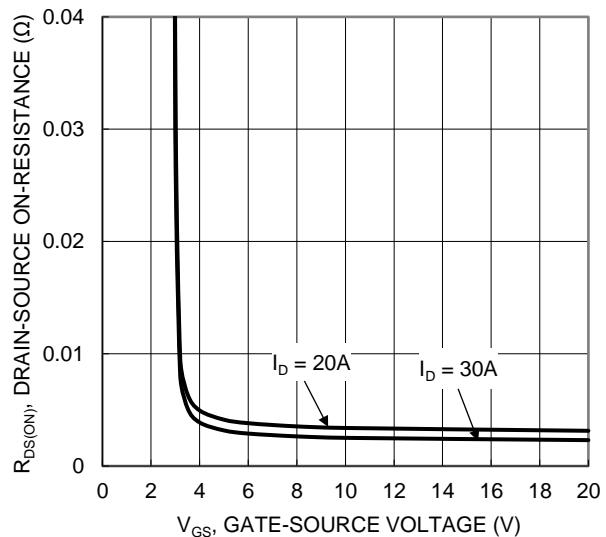


Figure 4. Typical Transfer Characteristic

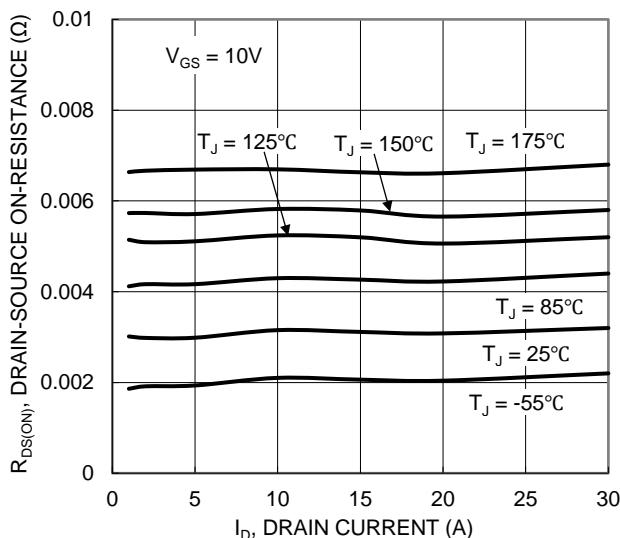


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

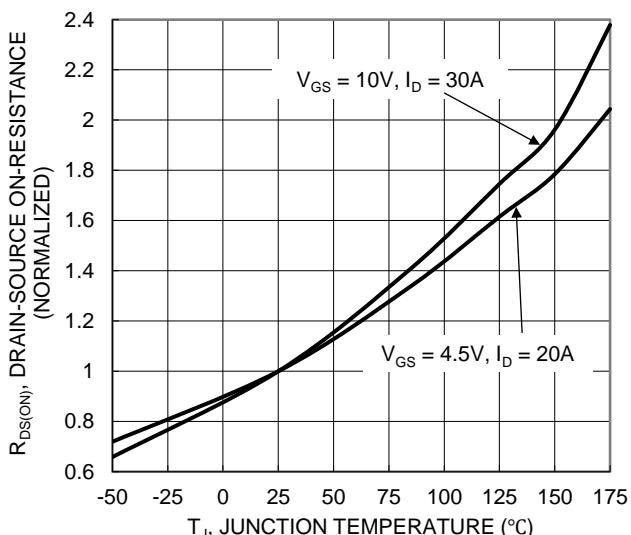


Figure 6. On-Resistance Variation with Junction Temperature

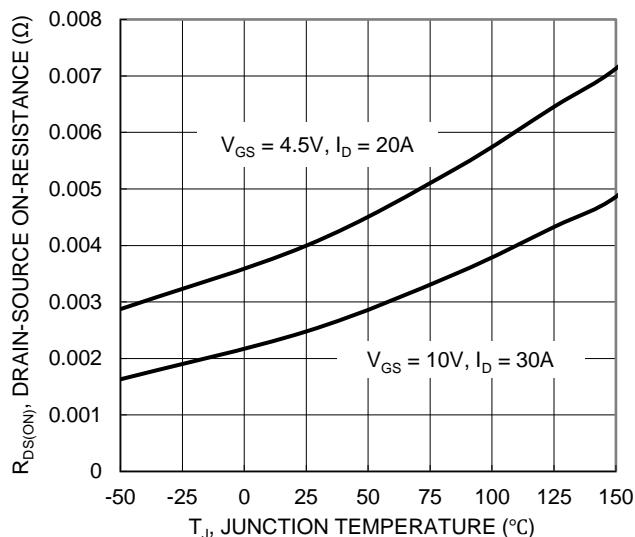


Figure 7. On-Resistance Variation with Junction Temperature

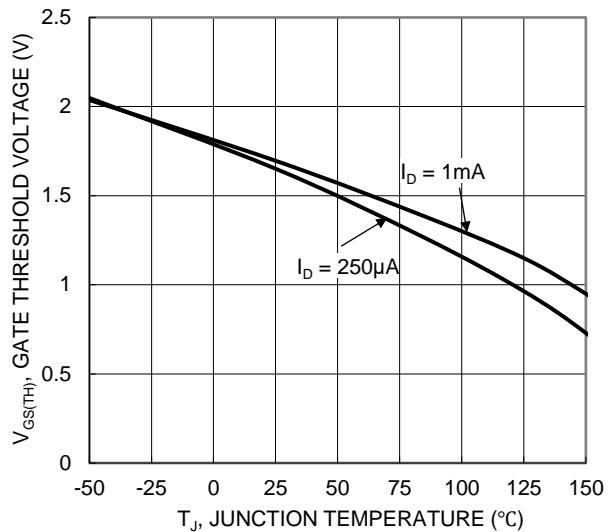


Figure 8. Gate Threshold Variation vs. Junction Temperature

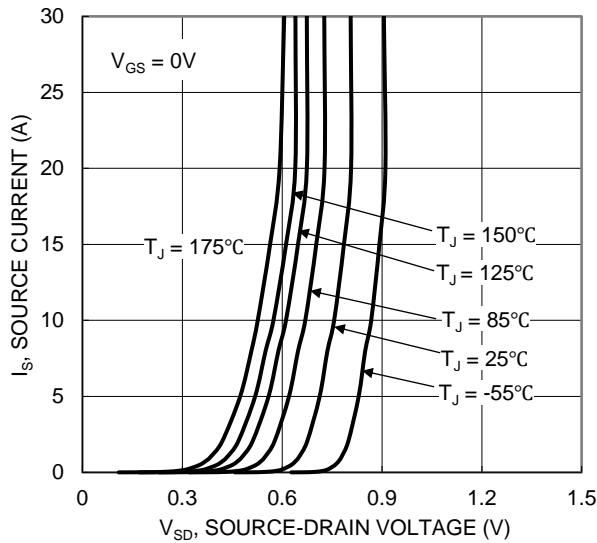


Figure 9. Diode Forward Voltage vs. Current

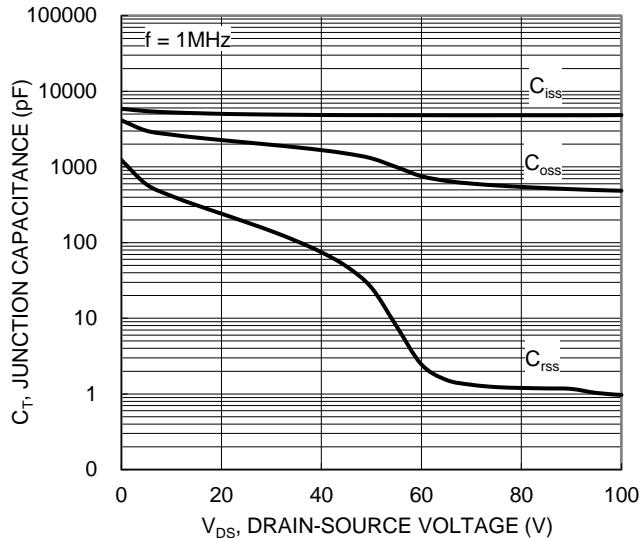


Figure 10. Typical Junction Capacitance

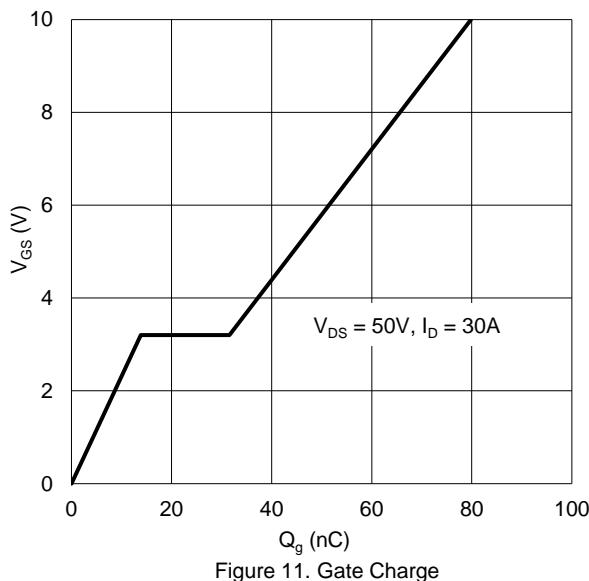


Figure 11. Gate Charge

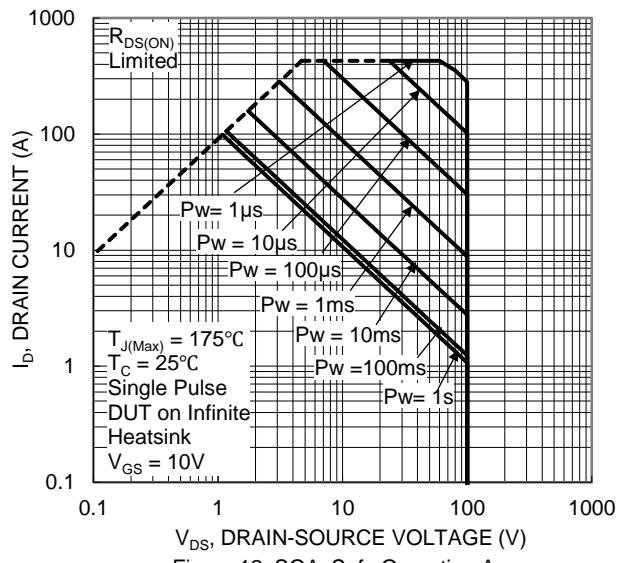


Figure 12. SOA, Safe Operation Area

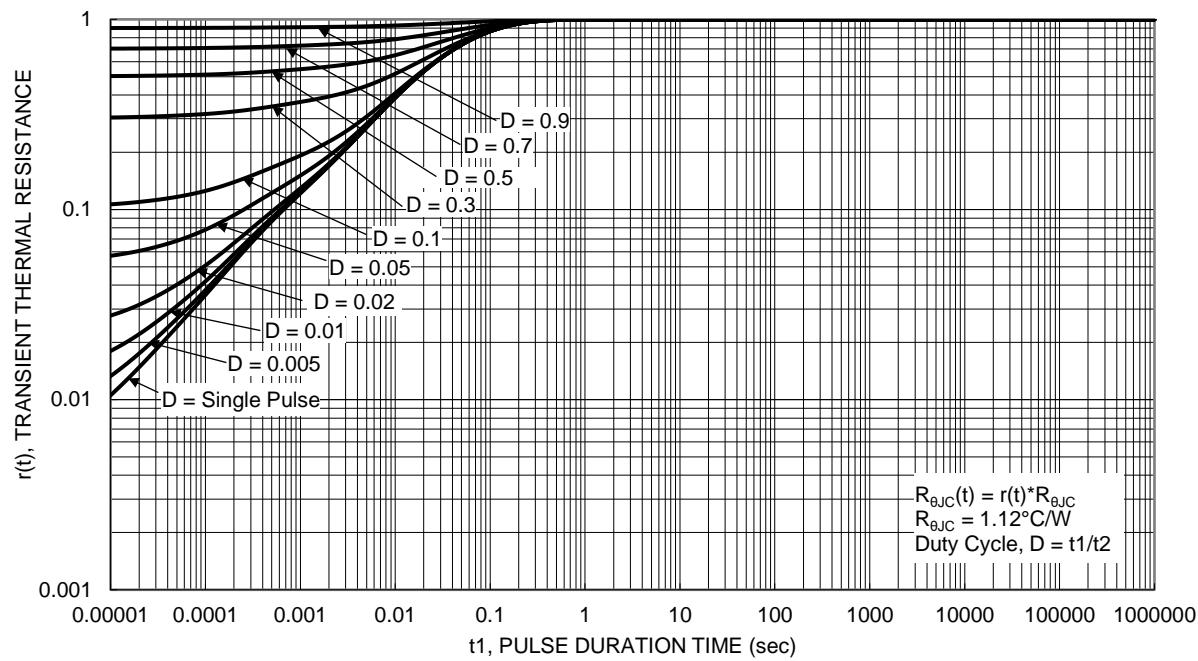
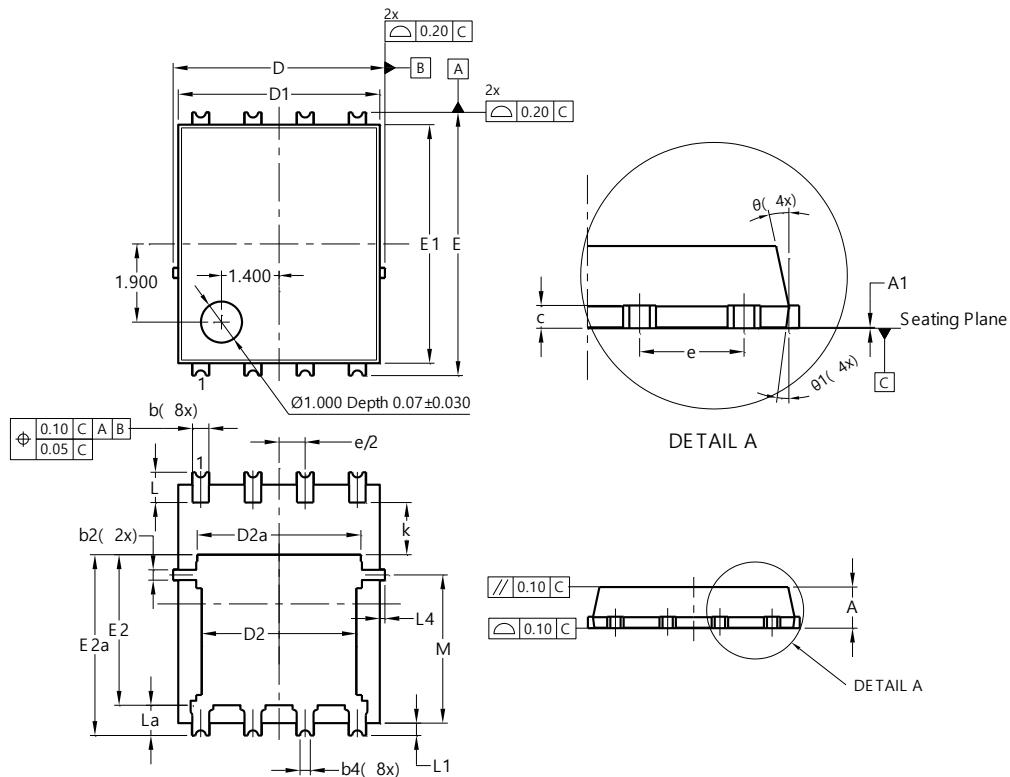


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

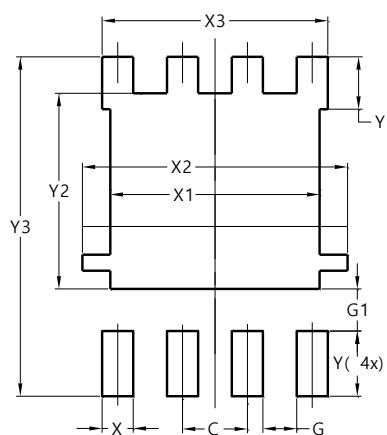
PowerDI5060-8/SWP (Type UX)



## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)
<b>C</b>	1.270
<b>G</b>	0.660
<b>G1</b>	0.820
<b>X</b>	0.610
<b>X1</b>	4.100
<b>X2</b>	5.190
<b>X3</b>	4.420
<b>Y</b>	1.270
<b>Y1</b>	1.020
<b>Y2</b>	3.810
<b>Y3</b>	6.610

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