



DMN62D2U

#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
	$2.0\Omega$ @ V <sub>GS</sub> = $5.0V$	390mA
60V	2.5Ω @ V <sub>GS</sub> = 2.5V	368mA
	4.0Ω @ V <sub>GS</sub> = 1.8V	309mA

### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An automotive-compliant part is available under separate datasheet (<u>DMN62D2UQ</u>)

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Motor controls
- Power-management functions
- Backlighting

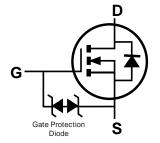
### **Mechanical Data**

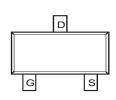
- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe.
   Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.008 grams (Approximate)





SOT23





Top View

**Equivalent Circuit** 

Top View

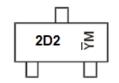
### **Ordering Information** (Note 4)

Orderable Part Number	Paakaga	Packing		
Orderable Part Number	Package	Qty.	Carrier	
DMN62D2U-7	SOT23	3,000	Tape & Reel	
DMN62D2U-13	SOT23	10,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



2D2 = Product Type Marking Code

YM = Date Code Marking
Y = Year (ex: M = 2025)
M = Month (ex: 9 = September)

Date Code Key

Year	2022	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	J	-	М	N	Р	R	S	Т	U	V	W	X
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 5) Vgs = 5.0V	lD	390 314	mA		
Maximum Continuous Body Diode Forward Current (Note 5)			Is	390	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6) (Note 5)	)	I <sub>DM</sub>	1.2	Α

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		PD	0.5	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	238	°C/W
Total Power Dissipation (Note 5)		PD	0.6	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	197	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

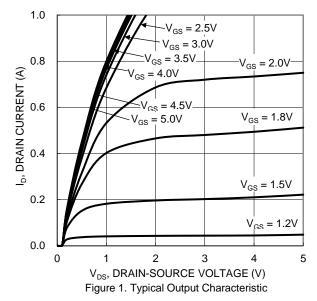
## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	V <sub>G</sub> S = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_		±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.5	_	1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
			1.0	2.0		$V_{GS} = 5.0V, I_{D} = 0.05A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	1.7	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
			2.1	4.0		$V_{GS} = 1.8V, I_{D} = 0.05A$
Diode Forward Voltage	VsD	_	0.7	1.4	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	41	_	pF	.,
Output Capacitance	Coss	_	5.4	_	pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	4.2	_	pF	1 – 1.01/11/2
Gate Resistance	Rg	-	52	_	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge	Qg		0.8		nC	\\ 45\\ \\ 40\\
Gate-Source Charge	Qgs		0.2		nC	Vgs = 4.5V, Vps = 10V
Gate-Drain Charge	Qgd	_	0.1	_	nC	1D = 23011A
Turn-On Delay Time	tD(ON)	_	1.5	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	9.7	_	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V
Turn-Off Delay Time	tD(OFF)	_	22.6	_	ns	$R_G = 25\Omega$ , $I_D = 200mA$
Turn-Off Fall Time	t <sub>F</sub>	_	19.5	_	ns	

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





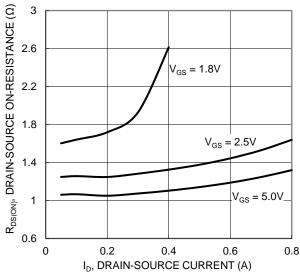


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

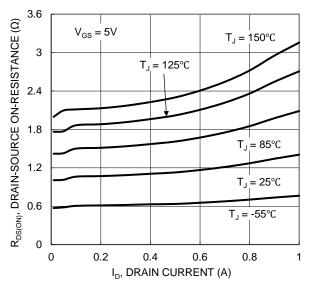


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

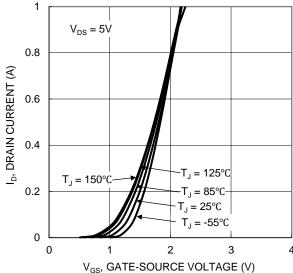


Figure 2. Typical Transfer Characteristic

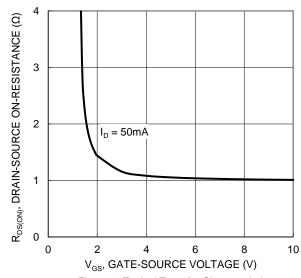


Figure 4. Typical Transfer Characteristic

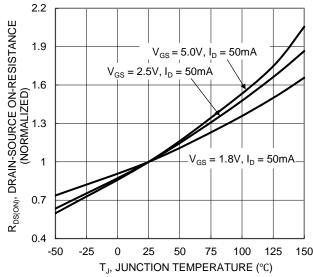


Figure 6. On-Resistance Variation with Junction Temperature



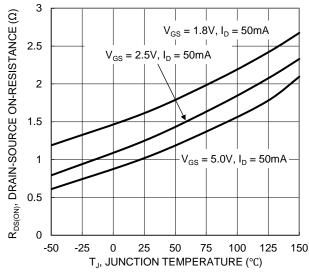
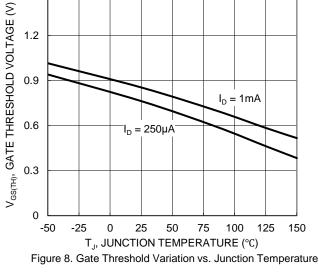


Figure 7. On-Resistance Variation with Junction Temperature



1.5

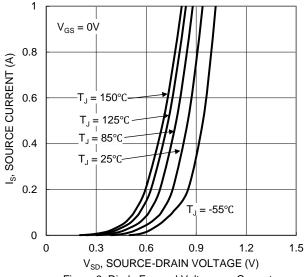
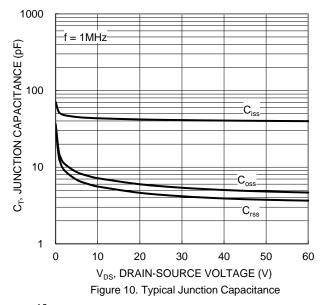
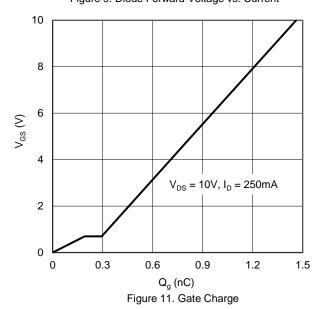


Figure 9. Diode Forward Voltage vs. Current





10 R<sub>DS(ON)</sub> Limited ID, DRAIN CURRENT (A) 0.1 Pw = 1msPw = 10ms  $T_{J(Max)} = 150$ °C  $T_A = 25$ °C Pw = 100ms 0.01 Single Pulse Pw = 10sDUT on 1\*MRI Board  $V_{GS} = 10V$ 0.001 10 100 0.1 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



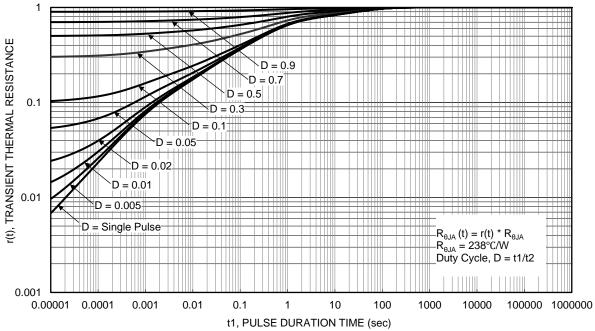


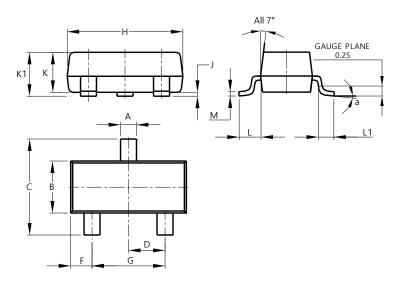
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23

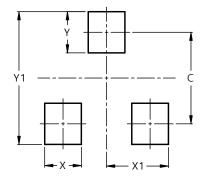


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
H	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.890	1.00	0.975				
<b>K</b> 1	0.903	1.10	1.025				
١	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	All Dimensions in mm						

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)				
С	2.0				
Х	0.8				
X1	1.35				
Υ	0.9				
V1	2.0				



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