

MOSFET – N-Channel, SUPERFET®

600 V, 7 A, 600 mΩ

FCPF7N60, FCP7N60

Description

SUPERFET MOSFET is onsemi's first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SUPERFET MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.

Features

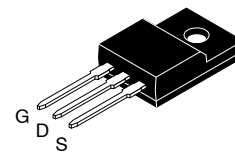
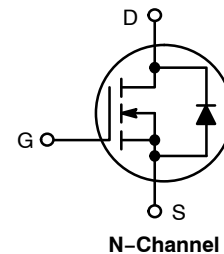
- 650 V @ $T_J = 150^{\circ}\text{C}$
- $R_{DS(on)} = 530\text{ m}\Omega$ (Typ.)
- Ultra Low Gate Charge (Typ. $Q_g = 23\text{ nC}$)
- Low Effective Output Capacitance (Typ. $C_{oss(eff.)} = 60\text{ pF}$)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

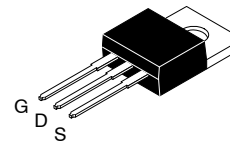
- LCD/LED/PDP TV
- Solar Inverter
- AC-DC Power Supply

V_{DS}	$R_{DS(on)}\text{ MAX}$	$I_D\text{ MAX}$
600 V	600 mΩ @ 10 V	7 A*

*Drain current limited by maximum junction temperature.

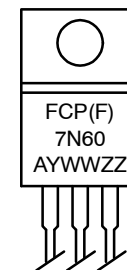


TO-220 Fullpack, 3-Lead
/ TO-220F-3SG
CASE 221AT



TO-220-3LD
CASE 340AT

MARKING DIAGRAM



FCP(F)7N60	= Specific Device Code
A	= Assembly Location
YWW	= Date Code (Year & Week)
ZZ	= Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FCPF7N60	TO-220-3 FullPak	1000 Units / Tube
FCP7N60	TO-220-3	1000 Units / Tube

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MOSFET MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		FCP7N60	FCPF7N60	Unit
V_{DSS}	Drain–Source Voltage		600		V
I_D	Drain Current	– Continuous ($T_C = 25^\circ\text{C}$)	7	7*	A
		– Continuous ($T_C = 100^\circ\text{C}$)	4.4	4.4*	
I_{DM}	Drain Current	– Pulsed (Note 1)	21	21*	A
V_{GSS}	Gate–Source Voltage		± 30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		230		mJ
I_{AR}	Avalanche Current (Note 1)		7		A
E_{AR}	Repetitive Avalanche Energy (Note 1)		8.3		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	83	31	W
		– Derate Above 25°C	0.67	0.25	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range		–55 to +150		$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300		$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*Drain current limited by maximum junction temperature.

1. Repetitive rating: pulse–width limited by maximum junction temperature.
2. $I_{AS} = 3.5\text{ A}$, $V_{DD} = 50\text{ V}$, $R_G = 25\ \Omega$, starting $T_J = 25^\circ\text{C}$.
3. $I_{SD} \leq 7\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, starting $T_J = 25^\circ\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	FCP7N60	FCPF7N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction–to–Case, Max.	1.5	4.0	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction–to–Ambient, Max.	62.5	62.5	

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain–Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA, T _J = 25°C	600	–	–	V
		V _{GS} = 0 V, I _D = 250 μA, T _J = 150°C	–	650	–	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	–	0.6	–	V/°C
BV _{DS}	Drain–Source Avalanche Breakdown Voltage	V _{GS} = 0 V, I _D = 7 A	–	700	–	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V	–	–	1	μA
		V _{DS} = 480 V, T _C = 125°C	–	–	10	
I _{GSSF}	Gate–Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	–	–	100	nA
I _{GSSR}	Gate–Body Leakage Current, Reverse	V _{GS} = –30 V, V _{DS} = 0 V	–	–	–100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	3.0	–	5.0	V
R _{DS(on)}	Static Drain–Source On–Resistance	V _{GS} = 10 V, I _D = 3.5 A	–	0.53	0.6	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 3.5 A	–	6	–	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	–	710	920	pF
C _{oss}	Output Capacitance		–	380	500	pF
C _{rss}	Reverse Transfer Capacitance		–	34	–	pF
C _{oss}	Output Capacitance	V _{DS} = 480 V, V _{GS} = 0 V, f = 1 MHz	–	22	29	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	–	60	–	pF

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn–On Delay Time	V _{DD} = 300 V, I _D = 7 A, V _{GS} = 10 V, R _G = 25 Ω (Note 4)	–	35	80	ns
t _r	Turn–On Rise Time		–	55	120	ns
t _{d(off)}	Turn–Off Delay Time		–	75	160	ns
t _f	Turn–Off Fall Time		–	32	75	ns
Q _g	Total Gate Charge	V _{DS} = 480 V, I _D = 7 A, V _{GS} = 10 V (Note 4)	–	23	30	nC
Q _{gs}	Gate–Source Charge		–	4.2	5.5	nC
Q _{gd}	Gate–Drain Charge		–	11.5	–	nC

DRAIN–SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I _S	Maximum Continuous Drain–Source Diode Forward Current		–	–	7	A
I _{SM}	Maximum Pulsed Drain–Source Diode Forward Current		–	–	21	A
V _{SD}	Drain–Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 7 A	–	–	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 7 A, dI _F /dt = 100 A/μs	–	360	–	ns
Q _{rr}	Reverse Recovery Charge		–	4.5	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

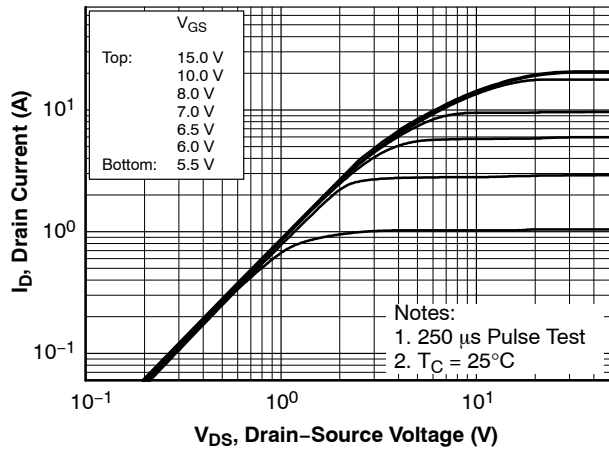


Figure 1. On-Region Characteristics

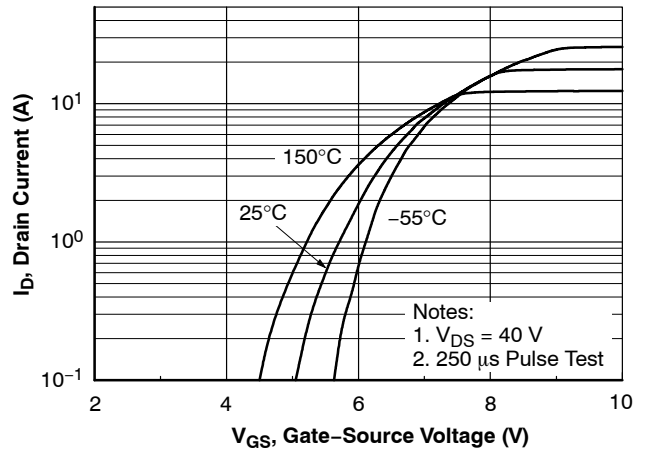


Figure 2. Transfer Characteristics

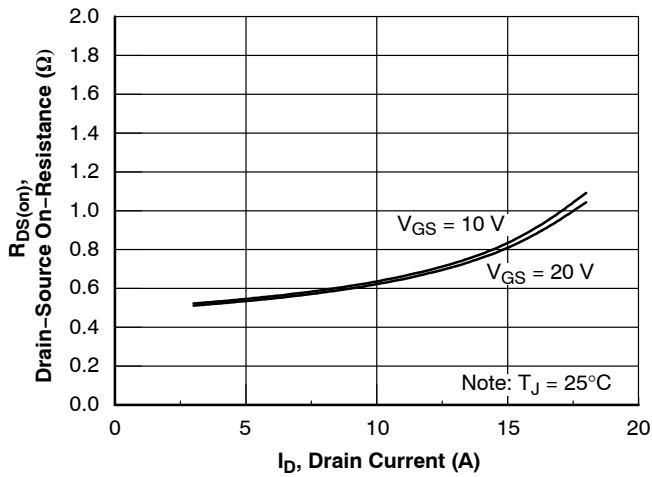


Figure 3. On-Resistance Variation vs. Drain Current and Gate voltage

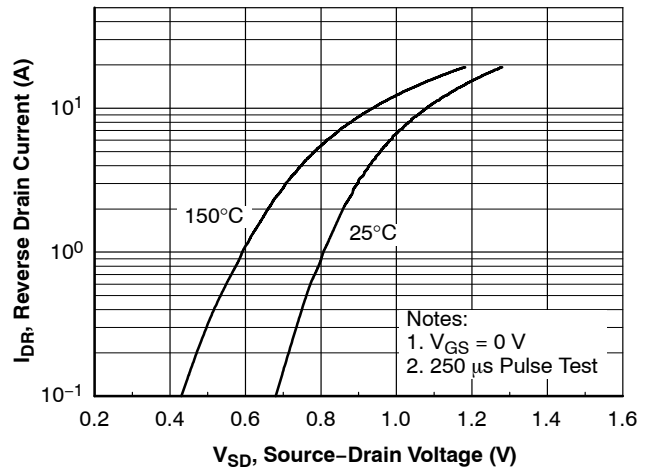


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

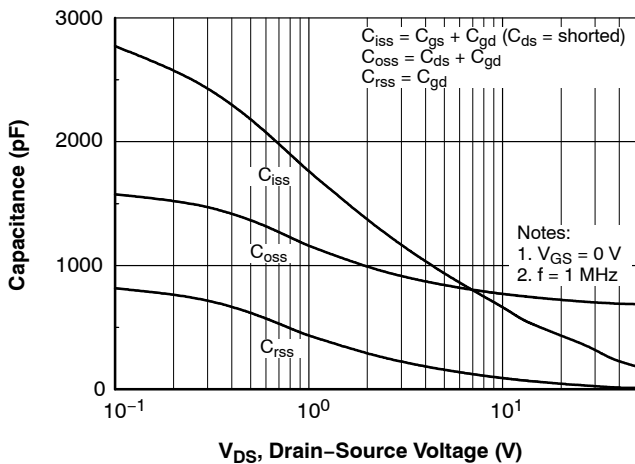


Figure 5. Capacitance Characteristics

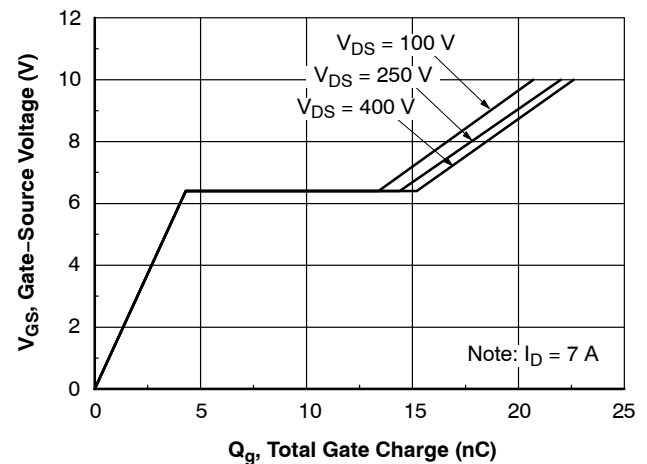


Figure 6. Gate Charge Characteristics

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

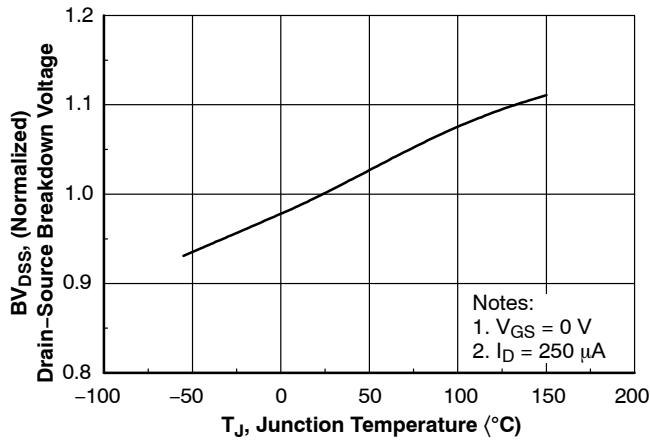


Figure 7. Breakdown Voltage Variation vs. Temperature

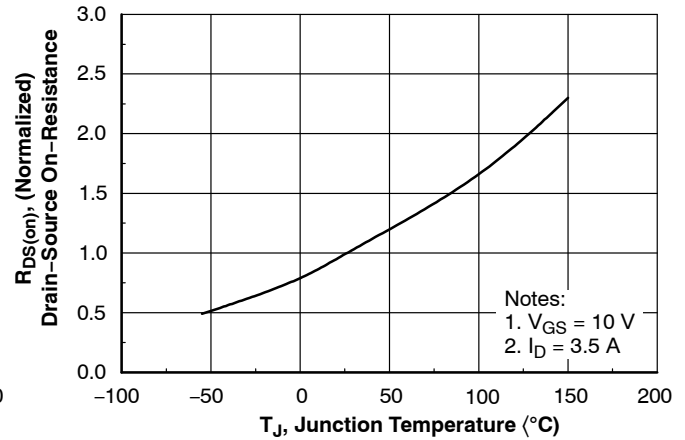


Figure 8. On-Resistance Variation vs. Temperature

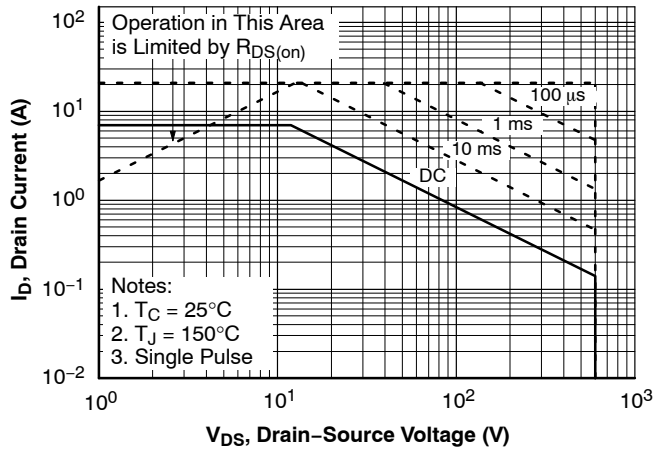


Figure 9. Maximum Safe Operating Area for FCP7N60

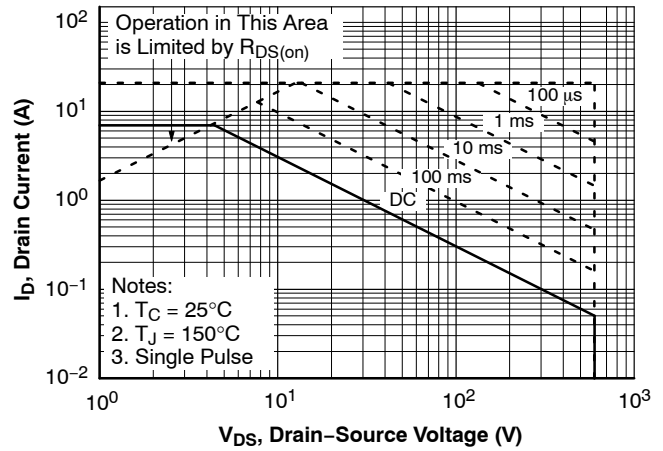


Figure 10. Maximum Safe Operating Area for FCP7N60

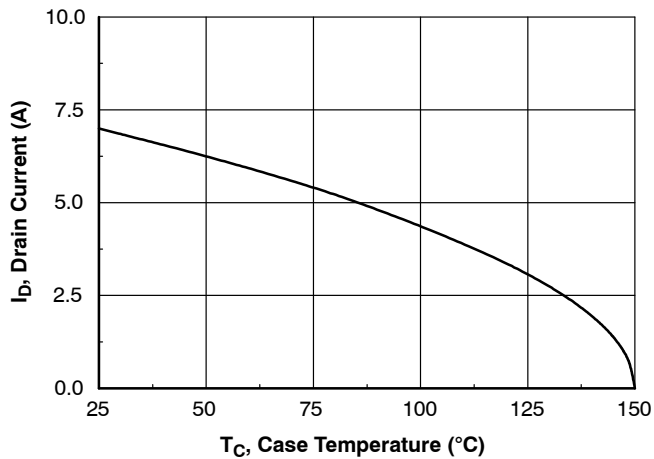


Figure 11. Maximum Drain Current vs. Case Temperature

+

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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

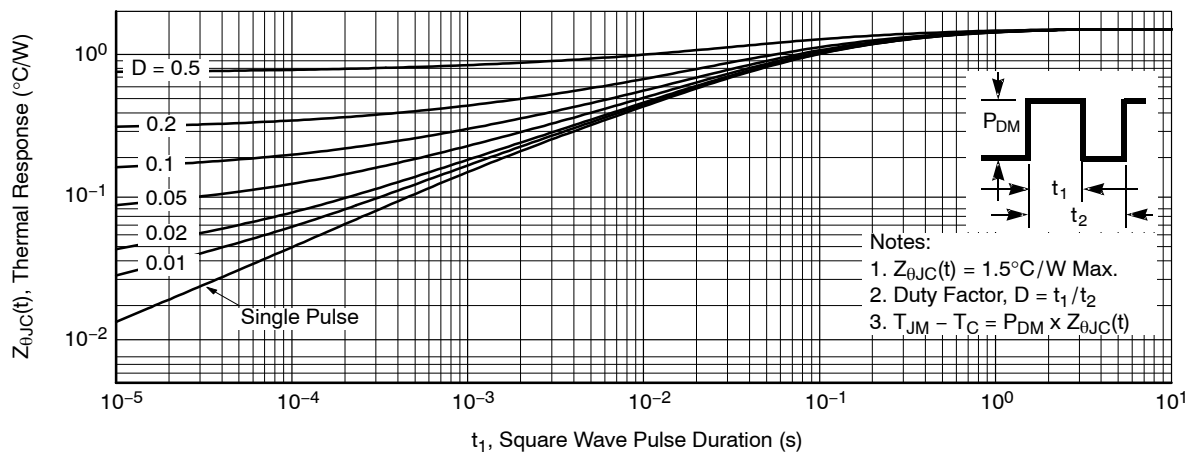


Figure 12. Transient Thermal Response Curve for FCP7N60

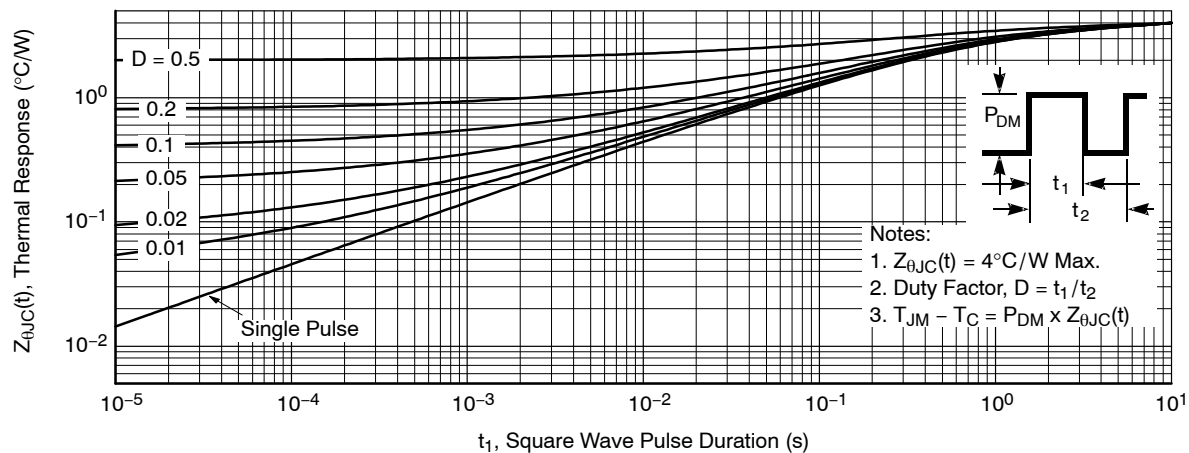


Figure 13. Transient Thermal Response Curve for FCPF7N60

FCPF7N60, FCP7N60

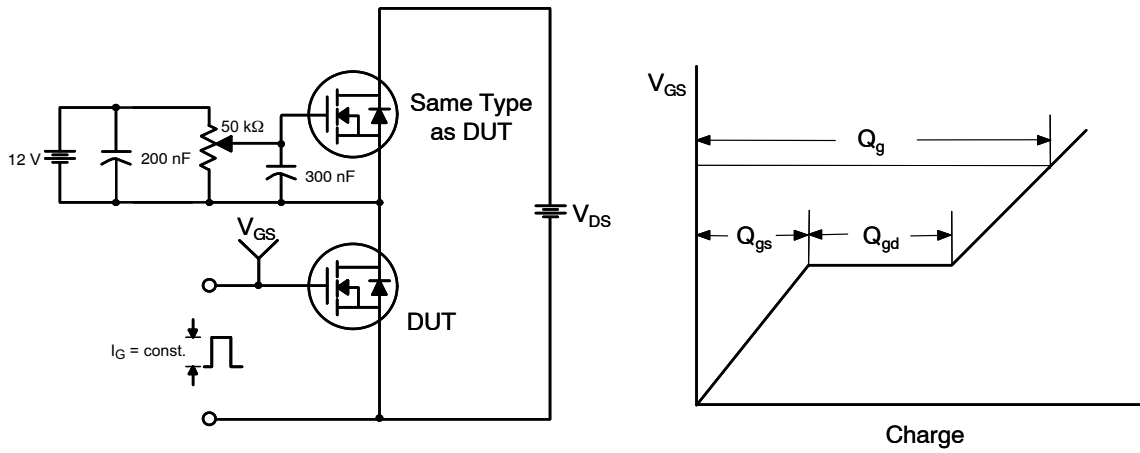


Figure 14. Gate Charge Test Circuit & Waveform

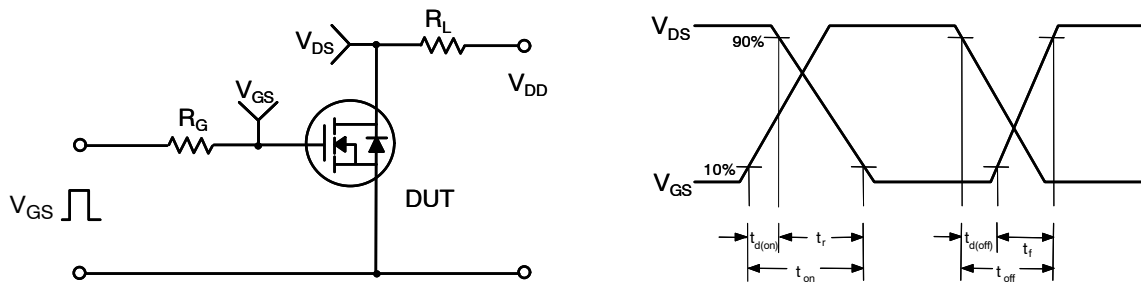


Figure 15. Resistive Switching Test Circuit & Waveforms

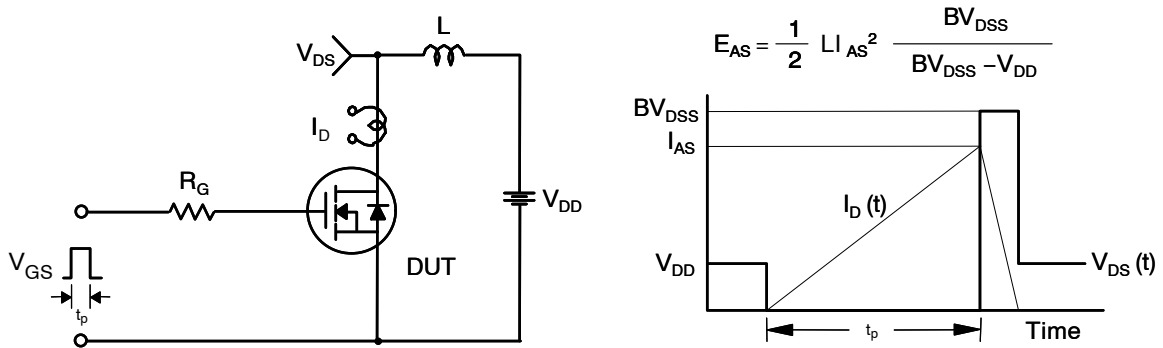


Figure 16. Unclamped Inductive Switching Test Circuit & Waveforms

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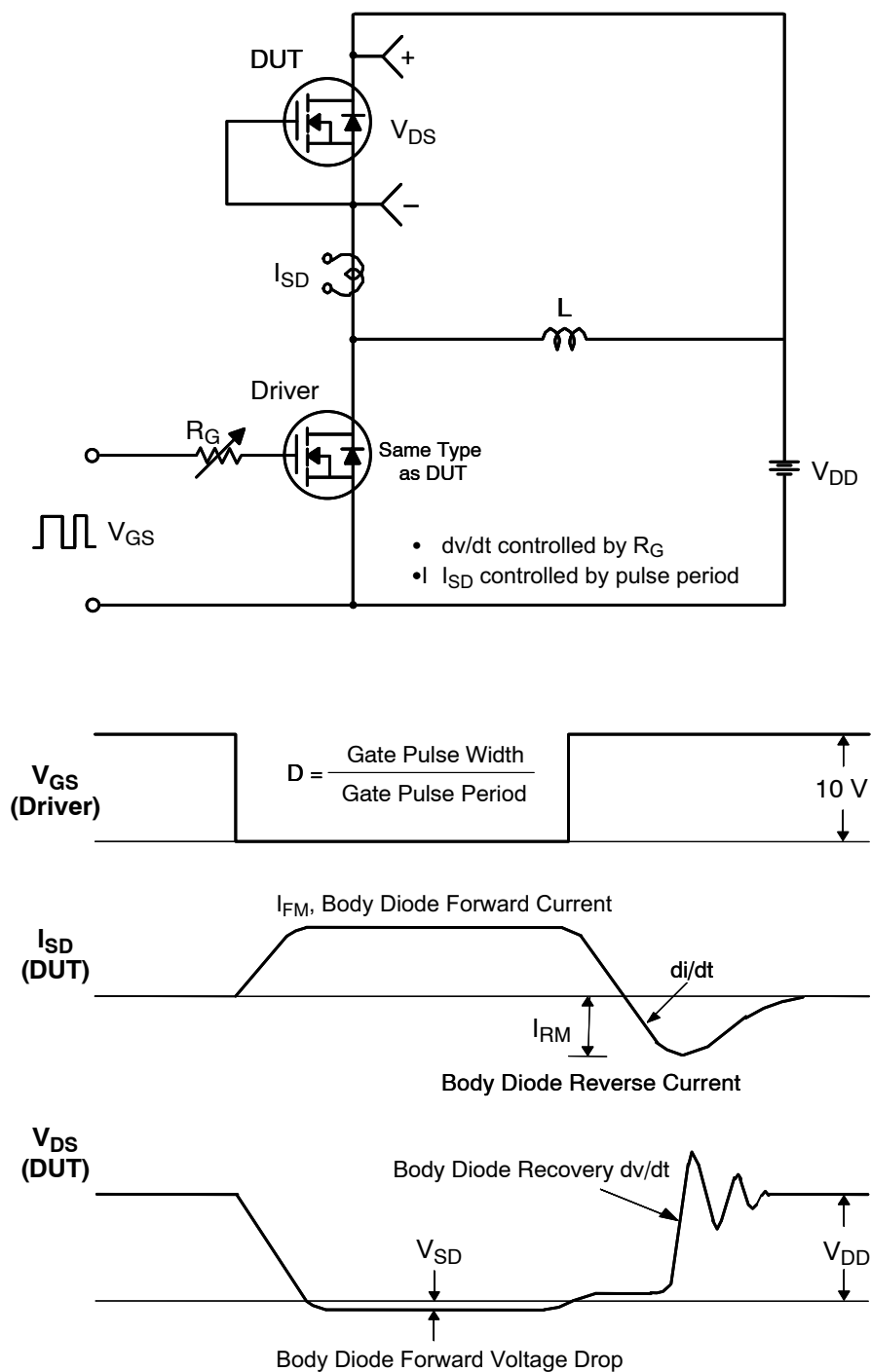


Figure 17. Peak Diode Recovery dv/dt Test Circuit & Waveforms

TO-220 Fullpack, 3-Lead / TO-220F-3SG
CASE 221AT
ISSUE B

DATE 19 JAN 2021



Scale 1:1



OPTION1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
Ø P	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

NOTES:

A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009

B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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DESCRIPTION:	TO-220 FULLPACK, 3-LEAD / TO-220F-3SG	PAGE 1 OF 1

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TO-220-3LD
CASE 340AT
ISSUE B

DATE 08 AUG 2022

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.00	--	4.70
A1	SEE NOTE "F"		
A2	2.10	--	2.85
b	0.55	--	1.00
b2	1.10	--	1.62
b4	1.42	--	1.62
c	0.36	--	0.60
D	13.90	--	16.30
D1	8.13	--	9.40
D2	11.50	--	14.30
D3	15.42	--	16.51
E	9.65	--	10.67
E1	7.59	--	8.65
e	2.40	--	2.67
H1	6.06	--	6.69
L	12.70	--	14.04
L1	2.70	--	4.10
P	3.50	--	4.00
Q	2.50	--	3.40
z	2.13 REF		
z1	2.06 REF		
θ	3°	--	5°

IF PRESENT, SEE NOTE "D"

GENERIC
MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



NOTES:

- A) REFERENCE JEDEC, TO-220, VARIATION AB
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [].
- D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
- E) DOES NOT COMPLY JEDEC STANDARD VALUE.
- F) "A1" DIMENSIONS AS BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1.10 - 1.45
- G) PRESENCE IS SUPPLIER DEPENDENT
- H) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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