

Bipolar Transistor

(-50 V, (-)7 A, Low VCE(sat), (PNP) NPN
Single PCP

2SA2016, 2SC5569

Features

- Adoption of FBET and MBIT Processes
- Low Collector-to-Emitter Saturation Voltage
- Ultrasmall Package Facilitates Miniaturization in End Products
- High Allowable Power Dissipation
- Large Current Capacity
- High-speed Switching

Applications

- Relay Drivers, Lamp Drivers, Motor Drivers, Flash

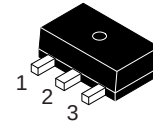
Specifications

(): 2SA2016

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

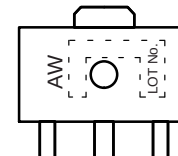
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-50) 100	V
Collector-to-Emitter Voltage	V _{CES}		(-50) 100	V
Collector-to-Emitter Voltage	V _{CEO}		(-)50	V
Emitter-to-Base Voltage	V _{EBO}		(-)6	V
Collector Current	I _C		(-)7	A
Collector Current (Pulse)	I _{CP}		(-)10	A
Base Current	I _B		(-)1.2	A
Collector Dissipation	P _C	When mounted on ceramic substrate (250 mm ² x 0.8 mm)	1.3	W
		T _c = 25°C	3.5	W
Junction Temperature	T _j		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

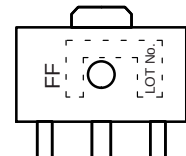


SOT-89 / PCP-1
CASE 419AU

MARKING DIAGRAM

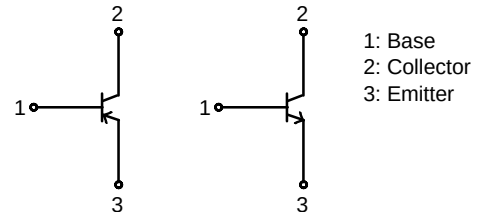


2SA2016



2SC5569

ELECTRICAL CONNECTION



1: Base
2: Collector
3: Emitter

2SA2016

2SC5569

ORDERING INFORMATION

Device	Package Type (JEITA, JEDEC)	Shipping [†]
2SA2016-TD-E	PCP (Pb-Free)	1000 / Tape & Reel
2SC5569-TD-E		

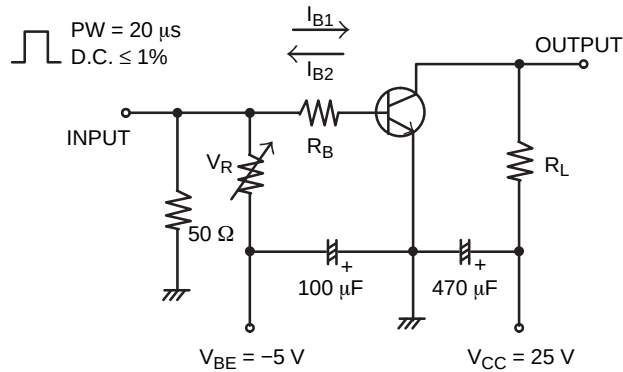
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](http://www.onsemi.com/BRD8011/D).

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Ratings			Unit
			Min	Typ	Max	
Collector Cutoff Current	I_{CBO}	$V_{CB} = (-)40\text{ V}, I_E = 0\text{ A}$	–	–	$(-)0.1$	μA
Emitter Cutoff Current	I_{EBO}	$V_{EB} = (-)4\text{ V}, I_C = 0\text{ A}$	–	–	$(-)0.1$	μA
DC Current Gain	h_{FE}	$V_{CE} = (-)2\text{ V}, I_C = (-)500\text{ mA}$	200	–	560	
Gain–Bandwidth Product	f_T	$V_{CE} = (-)10\text{ V}, I_C = (-)500\text{ mA}$	–	(290) 330	–	MHz
Output Capacitance	C_{ob}	$V_{CB} = (-)10\text{ V}, f = 1\text{ MHz}$	–	(50) 28	–	pF
Collector–to–Emitter Saturation Voltage	$V_{CE(sat)1}$	$I_C = (-)3.5\text{ A}, I_B = (-)175\text{ mA}$	–	(–230) 160	(–390) 240	mV
	$V_{CE(sat)2}$	$I_C = (-)2\text{ A}, I_B = (-)40\text{ mA}$	–	(–240) 110	(–400) 170	mV
Base–to–Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C = (-)2\text{ A}, I_B = (-)40\text{ mA}$	–	(–)0.83	(–)1.2	V
Collector–to–Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = (-)10\text{ }\mu\text{A}, I_E = 0\text{ A}$	(–50) 100	–	–	V
Collector–to–Emitter Breakdown Voltage	$V_{(BR)CES}$	$I_C = (-)100\text{ }\mu\text{A}, R_{BE} = 0\text{ }\Omega$	(–50) 100	–	–	V
Collector–to–Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = (-)1\text{ mA}, R_{BE} = \infty$	(–)50	–	–	V
Emitter–to–Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = (-)10\text{ }\mu\text{A}, I_C = 0\text{ A}$	(–)6	–	–	V
Turn–On Time	t_{on}	See specified Test Circuit.	–	(40) 30	–	ns
Storage Time	t_{stg}		–	(225) 420	–	ns
Fall Time	t_f		–	25	–	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit



$I_C = 20I_{B1} = -20I_{B2} = 2.5\text{ A}$
For PNP, the polarity is reversed.

Figure 1. Switching Time Test Circuit

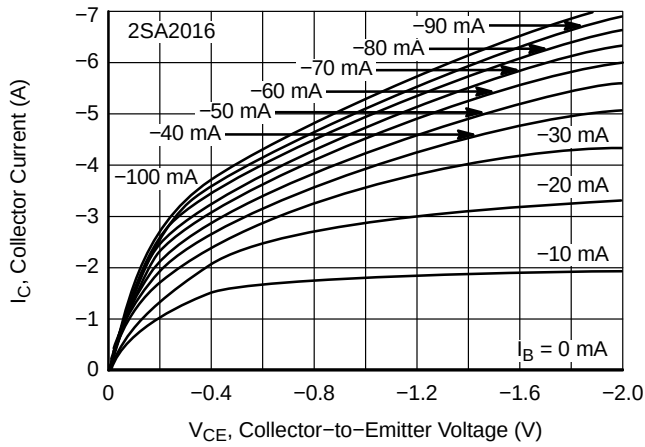


Figure 2. $I_C - V_{CE}$

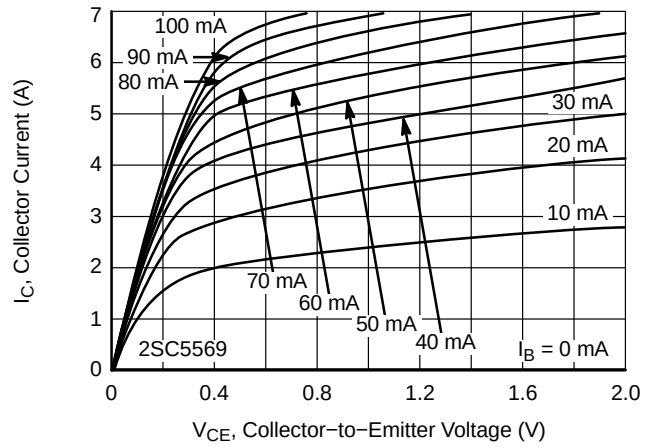


Figure 3. $I_C - V_{CE}$

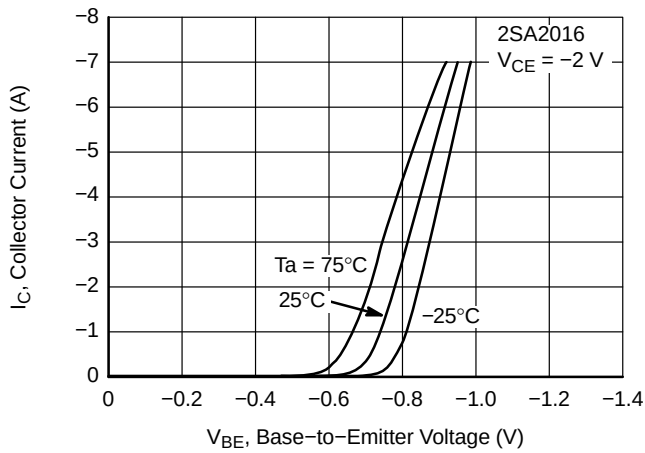


Figure 4. $I_C - V_{BE}$

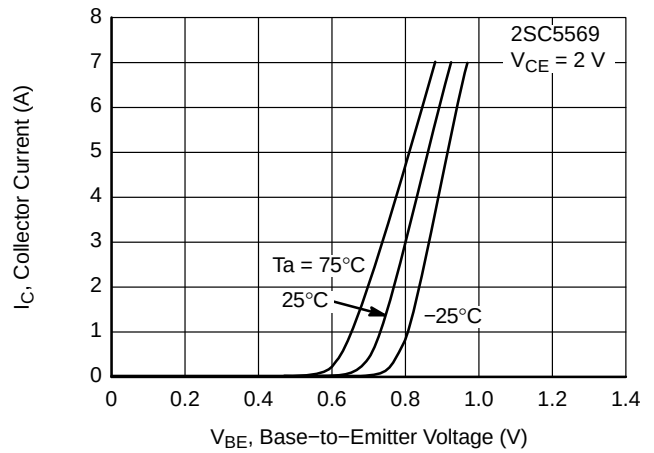


Figure 5. $I_C - V_{BE}$

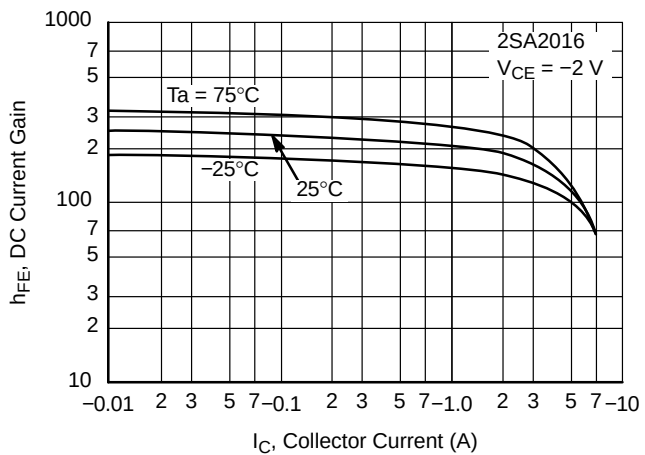


Figure 6. $h_{FE} - I_C$

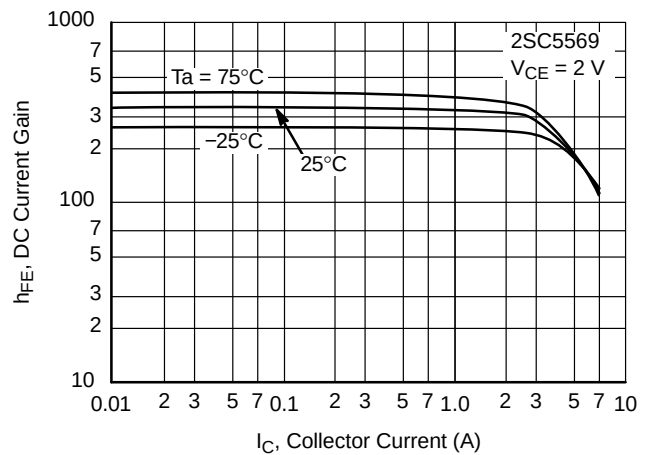


Figure 7. $h_{FE} - I_C$

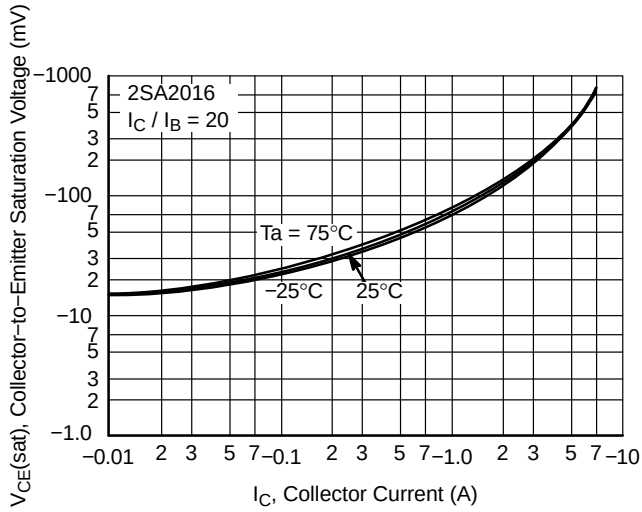


Figure 8. $V_{CE(sat)} - I_C$

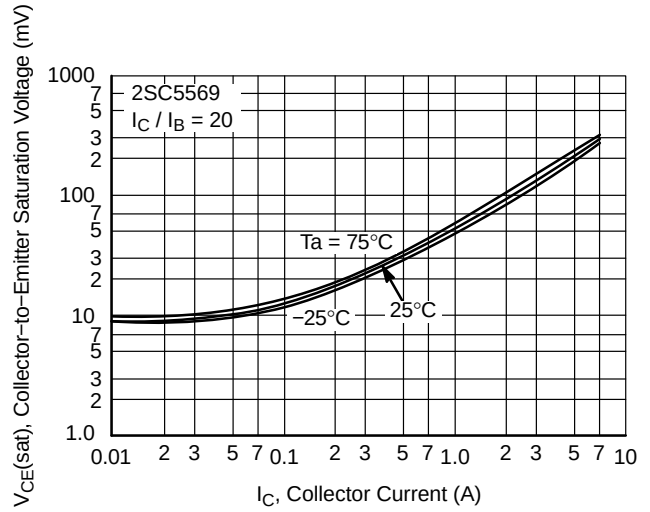


Figure 9. $V_{CE(sat)} - I_C$

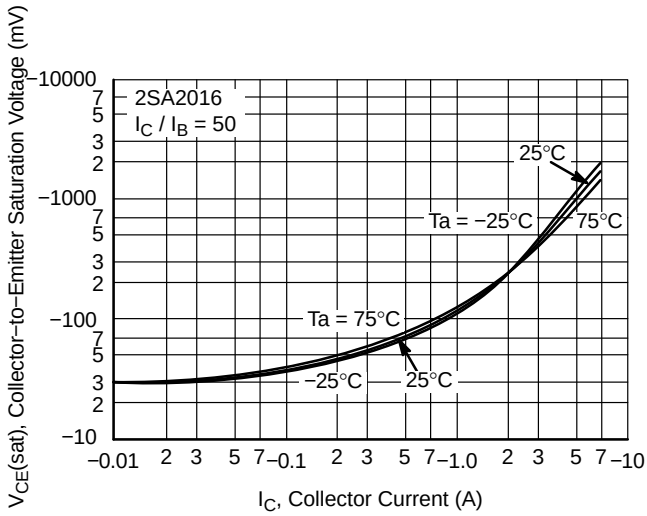


Figure 10. $V_{CE(sat)} - I_C$

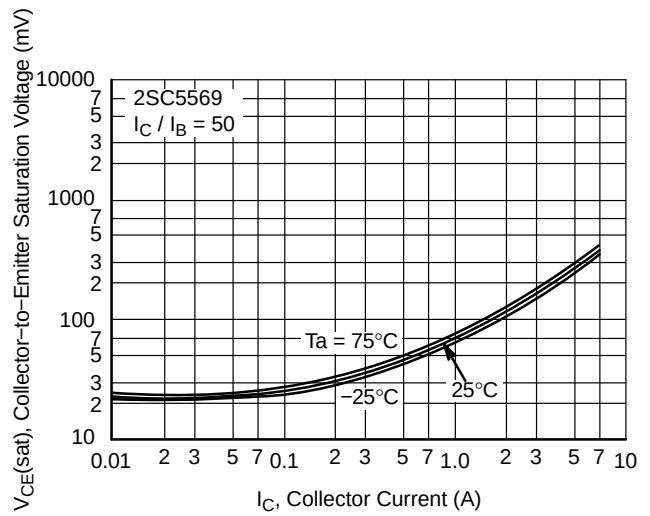


Figure 11. $V_{CE(sat)} - I_C$

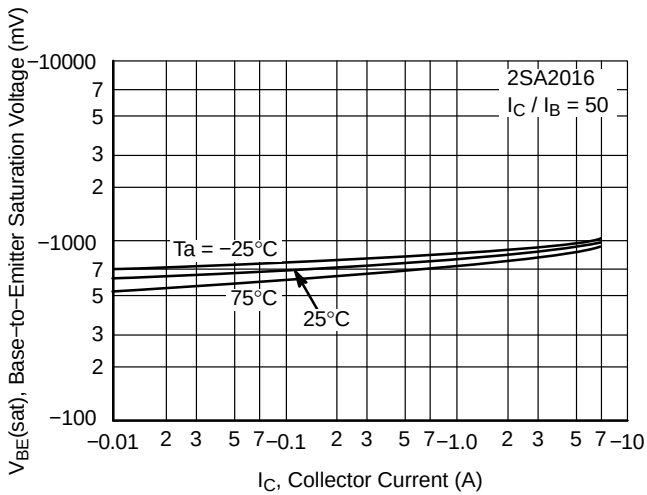


Figure 12. $V_{BE(sat)} - I_C$

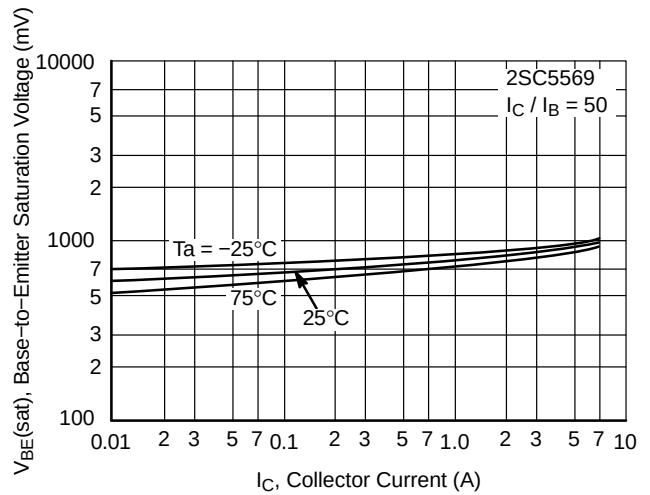


Figure 13. $V_{BE(sat)} - I_C$

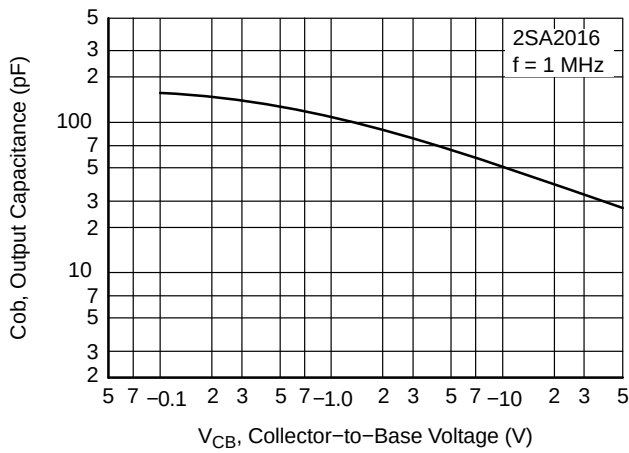


Figure 14. Cob - V_{CB}

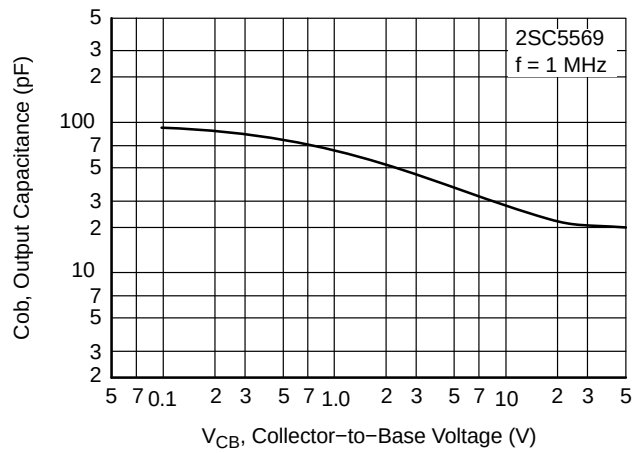


Figure 15. Cob - V_{CB}

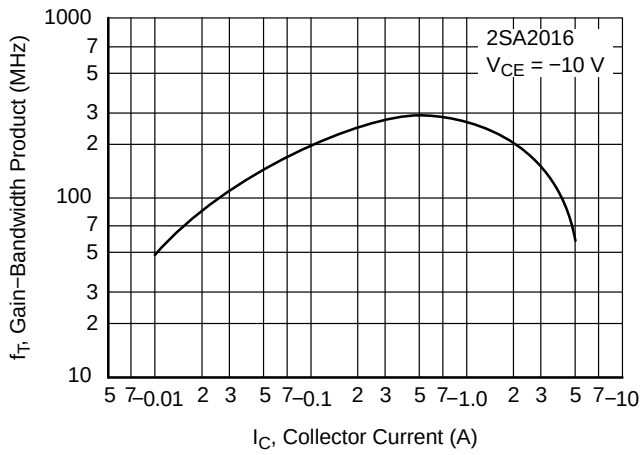


Figure 16. f_T - I_C

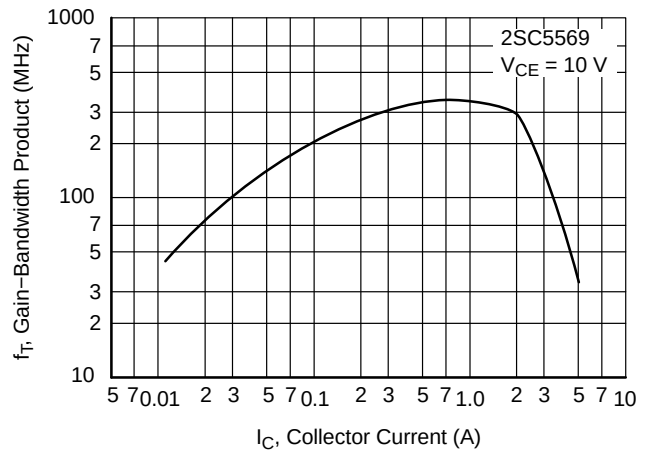


Figure 17. f_T - I_C

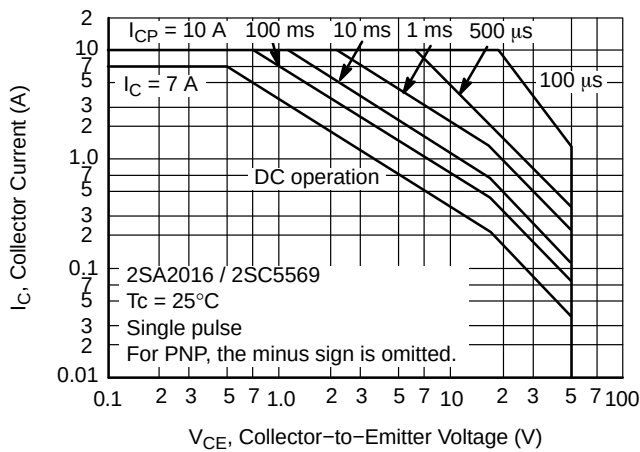


Figure 18. ASO

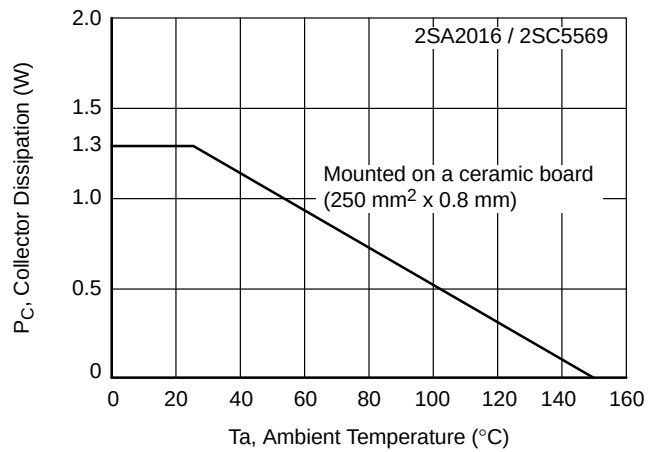


Figure 19. P_C - T_a

2SA2016, 2SC5569

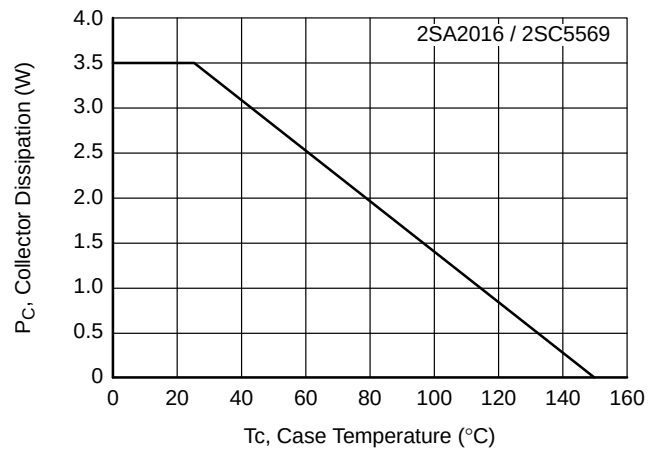
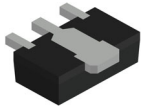
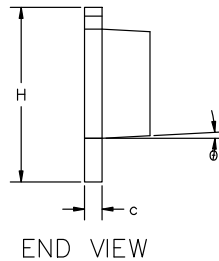
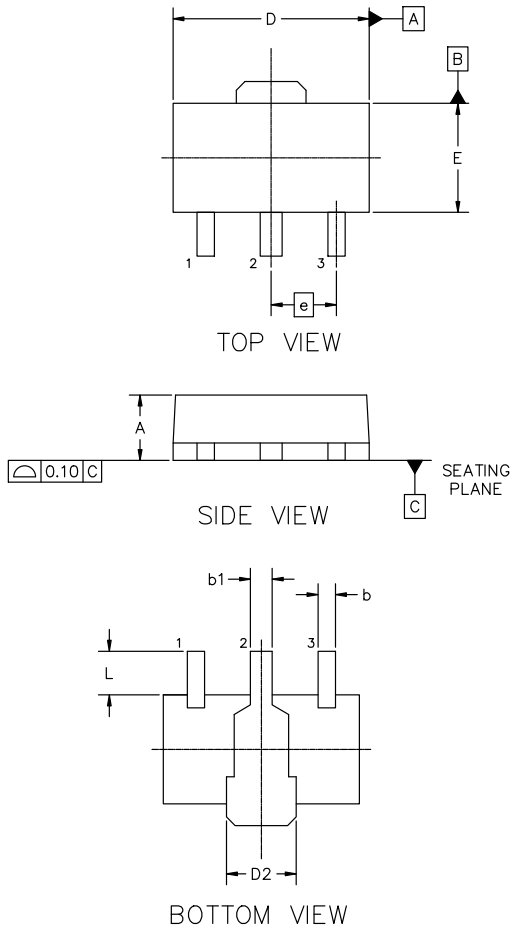


Figure 20. $P_C - T_C$



SOT-89 4.50x2.50x1.50 1.50P
CASE 419AU
ISSUE A

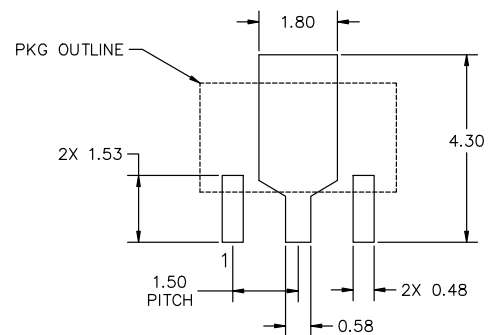
DATE 21 MAY 2025



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. LEAD THICKNESS INCLUDES LEAD FINISH.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

MILLIMETERS			
DIM	MIN	NOM	MAX
A	1.40	1.50	1.60
b	0.35	0.40	0.48
b1	0.40	0.50	0.55
c	0.37	0.40	0.43
D	4.40	4.50	4.60
D2	1.40	1.60	1.80
E	2.40	2.50	2.60
e	1.50 BSC		
H	3.80	4.00	4.20
L	0.80	1.00	1.20
Ø	0°	---	3°



RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

DOCUMENT NUMBER:	98AON79746E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	SOT-89 4.50x2.50x1.50 1.50P	PAGE 1 OF 1

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at
www.onsemi.com/support/sales