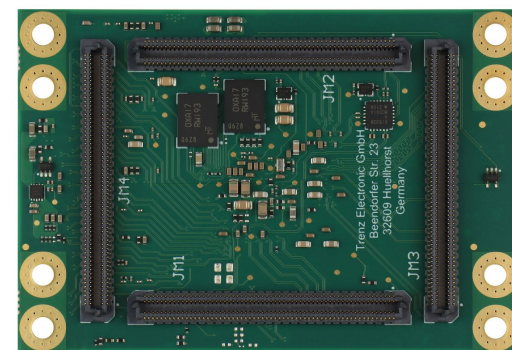
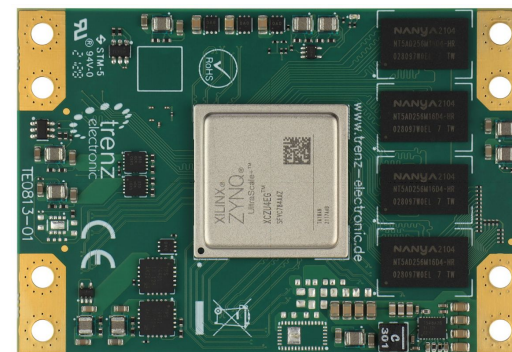
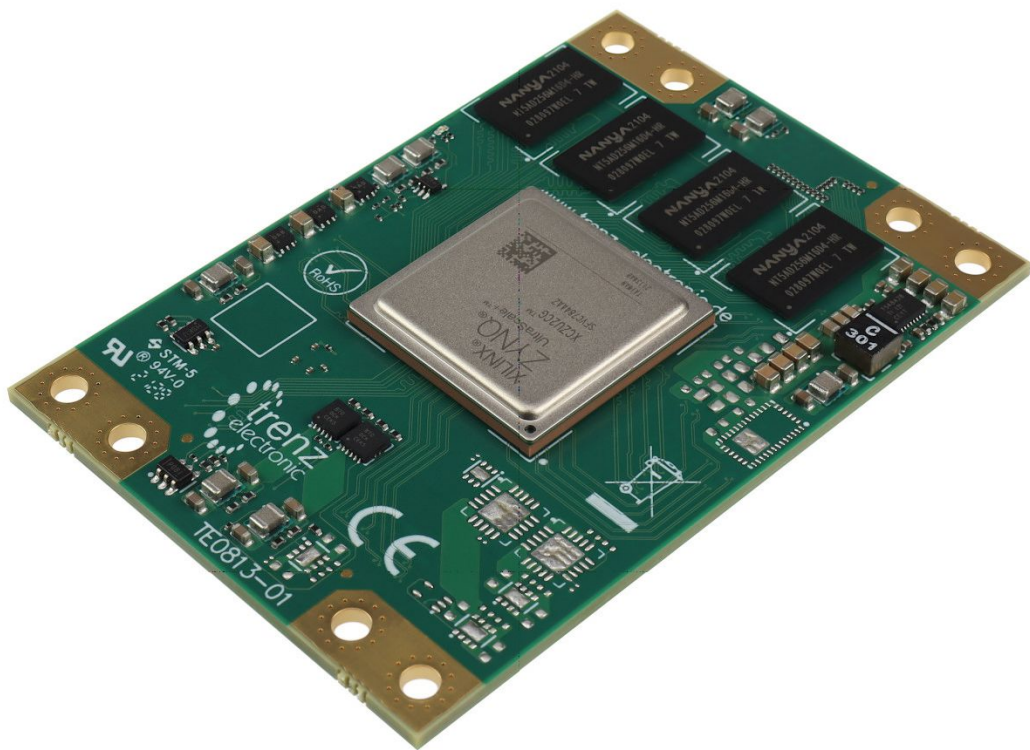




Regarding the usage of our schematics and alike documentation for Trenz module TE0813.

Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified.

TE0813 is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.

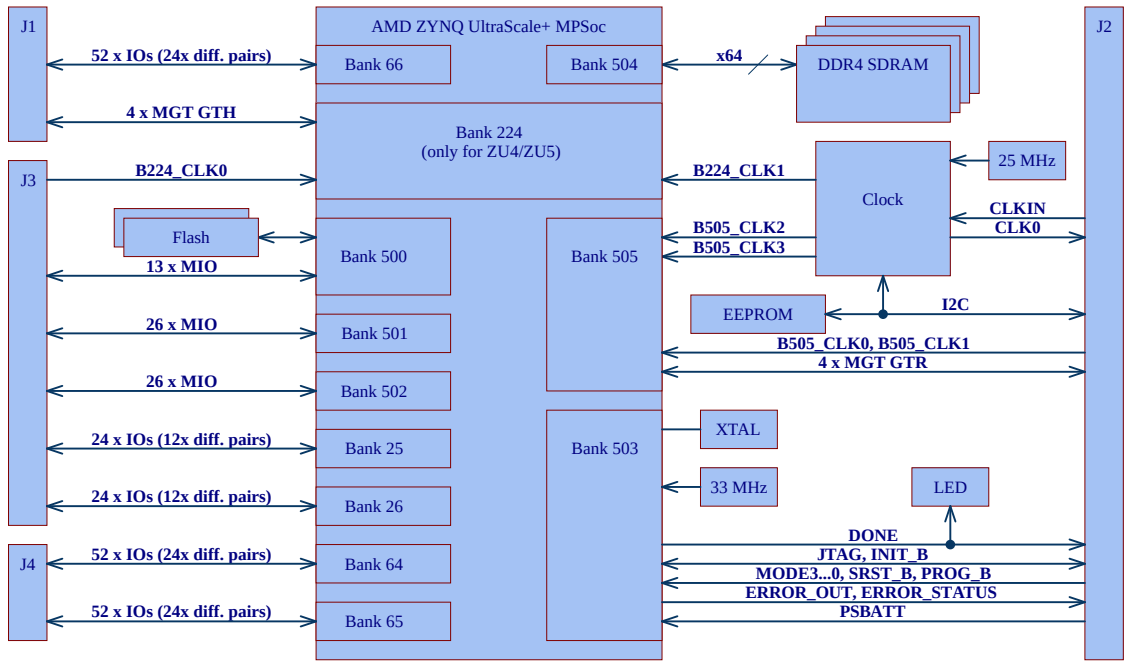
Schematics and other handouts serve for informational purposes only!

Drawn by	ED
Checked by	MR
Assembly variant	4DE81-A
Created by	ED
Modified by	ED
Modified at	2023-05-17



Title: TE0813 - Legal Notices

A4	Number: TE0813 4DE81-A	Rev. 02
Date: 14.06.2023	Copyright: Trenz Electronic GmbH	
Page 1 of 30		
Filename: Legal Notices Modules.SchDoc		



Supported Voltage Ranges:

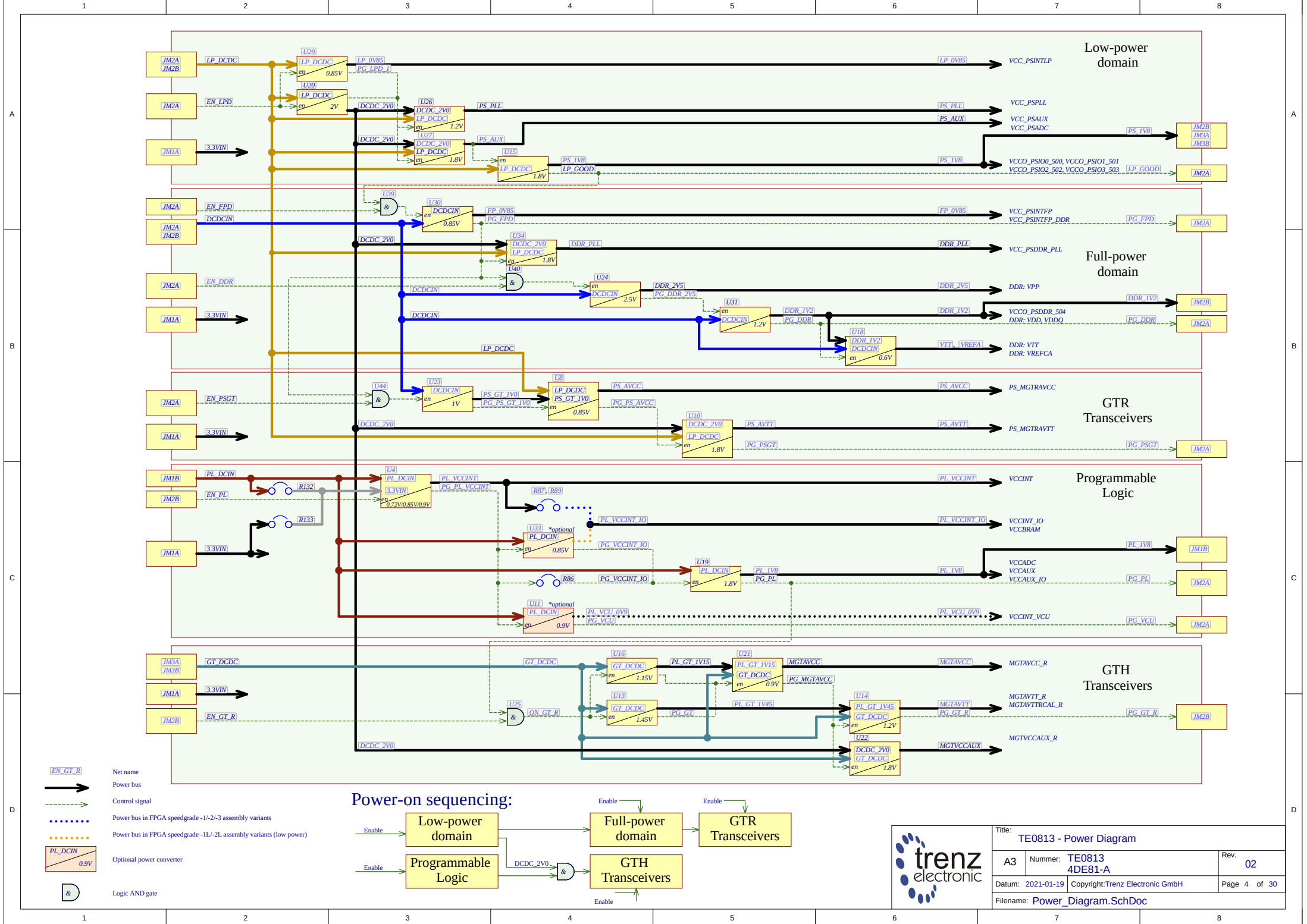
Power Rail	Direction	Range	Tolerance	Description	Note
3.3VIN	IN	3.3V	+/- 5 %	Micromodule Power	Management voltage rail, supplied by power rail
PL_DCIN	IN	3.3V	+/- 5 %	Micromodule Power	Programmable Logic, supplied by power rail
LP_DCDC	IN	3.3V	+/- 3 %	Micromodule Power	Low-Power Domain, supplied by power rail
GT_DCDC	IN	3.3V	+/- 3 %	Micromodule Power	GTH Transceiver, supplied by power rail
DCDCIN	IN	3.3V	+/- 5 %	Micromodule Power	Full-Power Domain and GTR, supplied by power rail
VCCO_64	IN	1.0 V - 1.8 V	+/- 3 %	HP IO Bank 64	Supplied by external power rail via B2B connector
VCCO_65	IN	1.0 V - 1.8 V	+/- 3 %	HP IO Bank 65	Supplied by external power rail via B2B connector
VCCO_66	IN	1.0 V - 1.8 V	+/- 3 %	HP IO Bank 66	Supplied by external power rail via B2B connector
VCCO_25	IN	1.2 V - 3.3 V	+/- 3 %	HD IO Bank 25	Supplied by external power rail via B2B connector
VCCO_26	IN	1.2 V - 3.3 V	+/- 3 %	HD IO Bank 26	Supplied by external power rail via B2B connector
PSBATT	IN	1.2 V - 1.5 V	-	RTC / BBRAM	Supplied by external power rail via B2B connector
PL_1V8	OUT	1.8 V	+/- 3 %	Power for Carrier	Micromodule: Programmable Logic
PS_1V8	OUT	1.8 V	+/- 3 %	Power for Carrier	Micromodule: Processing System
DDR_1V2	OUT	1.2 V	+/- 3 %	Power for Carrier	Micromodule: PS DDR I/O Supply

I2C Address:

Device	I2C ADDR	Note
PLL U5	0x70	-
EEPROM U28	0x50	-

TE0813	DDR4-TERM	ZU_PS_POWER	ZU_POWER
POWER	POWER_1	POWER_2	POWER_3

	Title: TE0813 - System Overview		
	A4	Number: TE0813 4DE81-A	Rev. 02
	Date: 14.06.2023	Copyright: Trenz Electronic GmbH	Page 2 of 30
	Filename: TE0813-Overview.SchDoc		



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Special notes:

A

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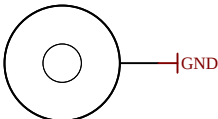
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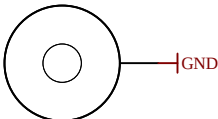
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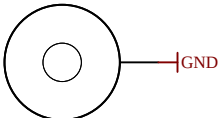
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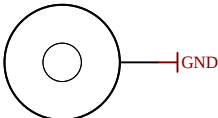
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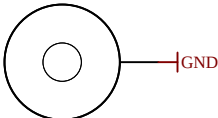
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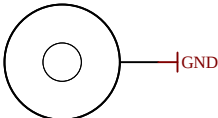
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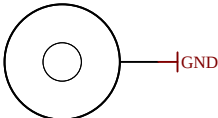
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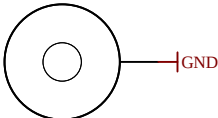
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Mount.Hole 3.2mm für Unterlegscheibe

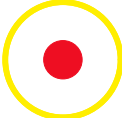


Mount.Hole 3.2mm für Unterlegscheibe



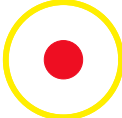
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PM1

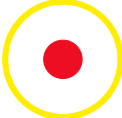


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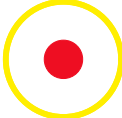
PM2



FIDU-DOT - mini PM4



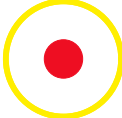
FIDU-DOT - mini PM5



FIDU-DOT - mini PM6



FIDU-DOT - mini



FIDU-DOT - mini

UKCA1

UKCA Logo on Top Overlay

UKCA-TOPOVERLAY

CE1

CE Logo on Top Overlay

CE-TOPOVERLAY

MECH1

TE Address Overlay

LOGO ADDRESS

LOGO1

TE Logo PRINT Layer

LOGO PRINT

Serial

Serial

Serialnumber 6,3 x 6.3mm



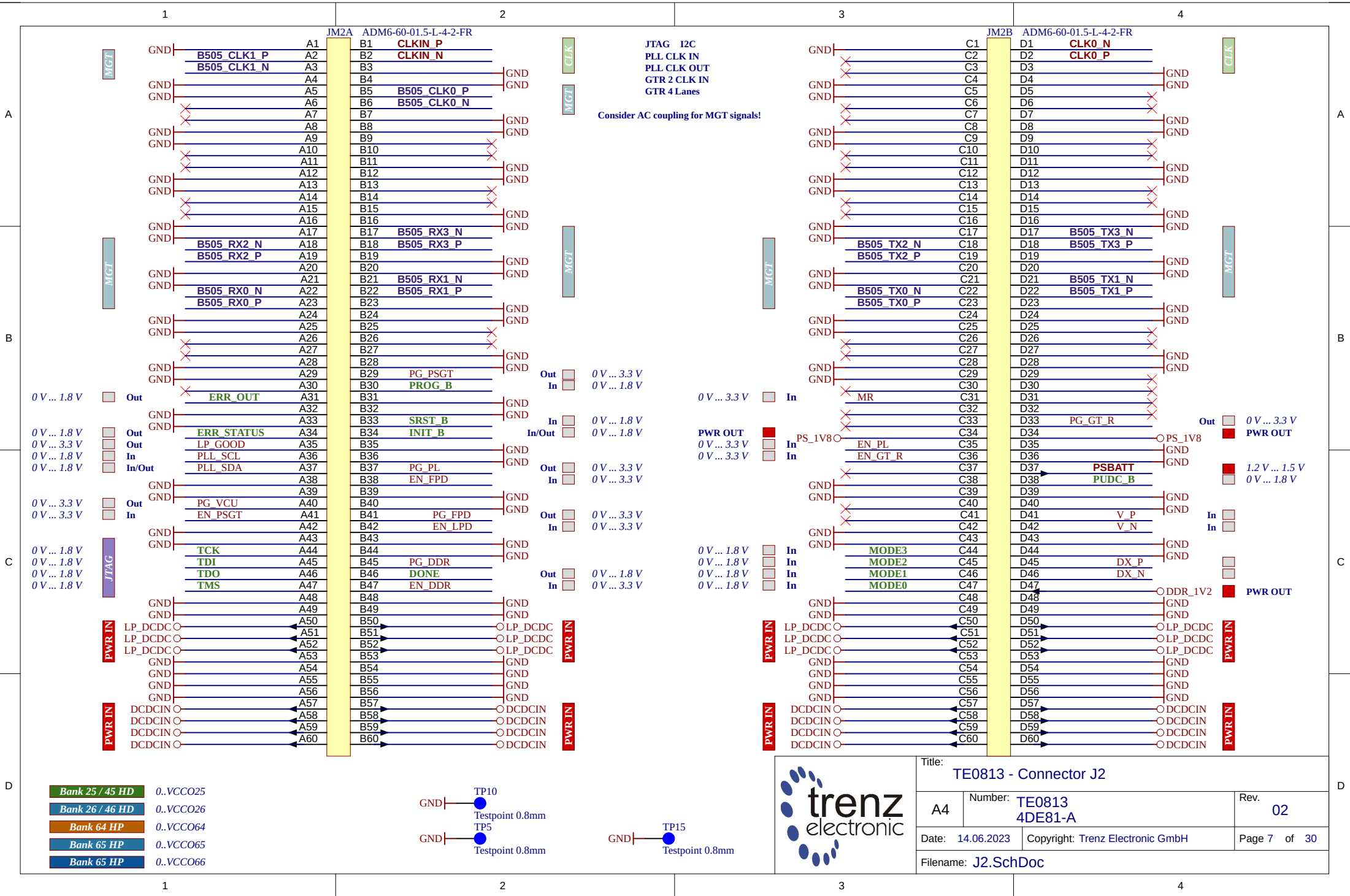
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Date: 13.06.2023	Copyright: Trenz Electronic GmbH	Page 5 of 30
Filename: TE0813.SchDoc		

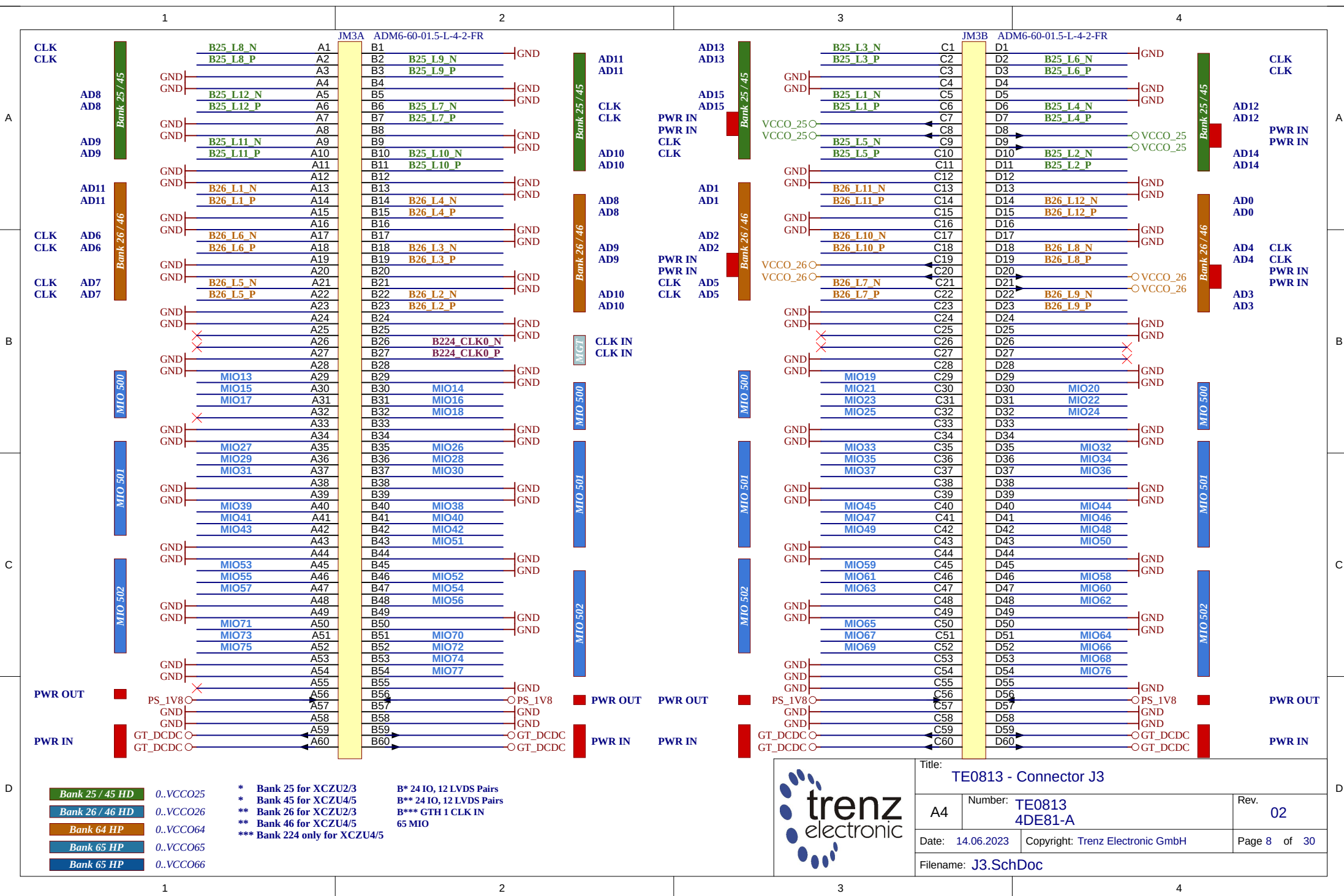
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A

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C

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A

B

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D

JM4A ADM6-60-01.5-L-4-2-FR

JM4B ADM6-60-01.5-L-4-2-FR

Bank 64

B64 52 IO, 24 LVDS Pairs
B64 52 IO, 24 LVDS Pairs

Bank 65

Bank 65

Bank 25 / 45 HD 0..VCCO25
Bank 26 / 46 HD 0..VCCO26
Bank 64 HP 0..VCCO64
Bank 65 HP 0..VCCO65
Bank 65 HP 0..VCCO66



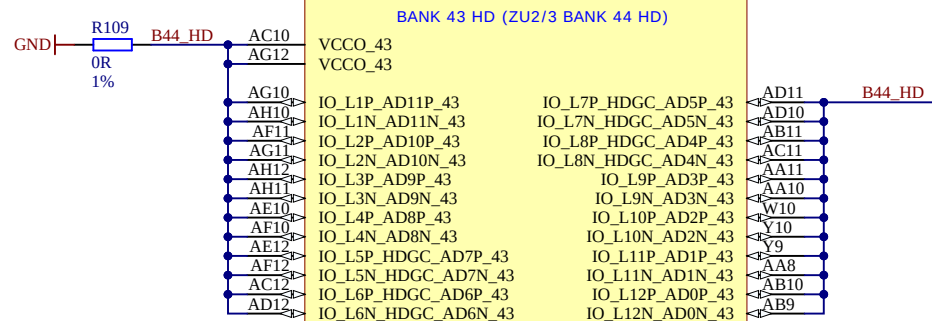
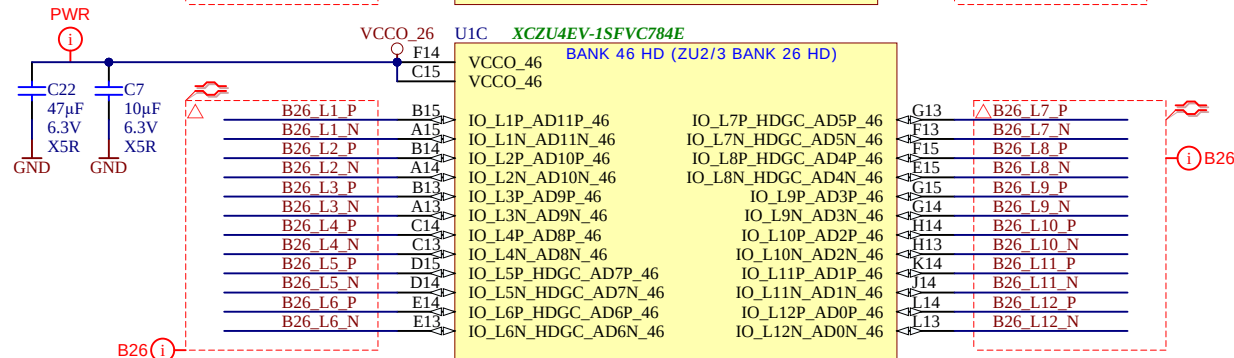
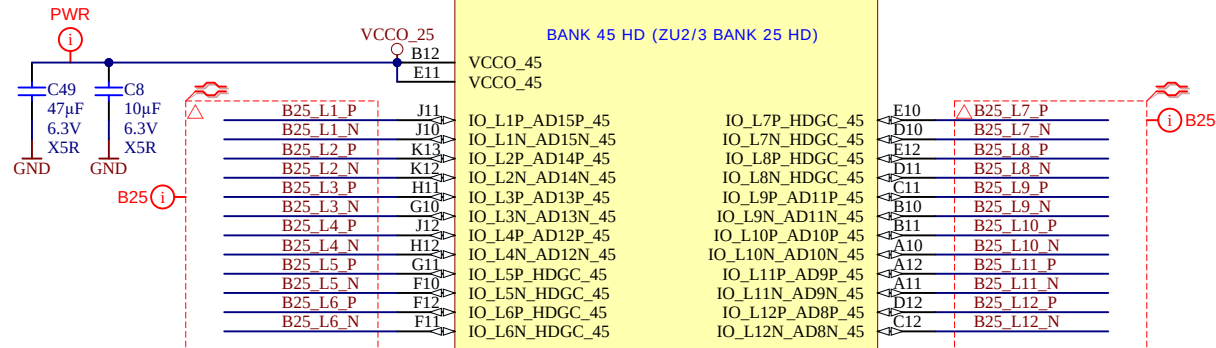
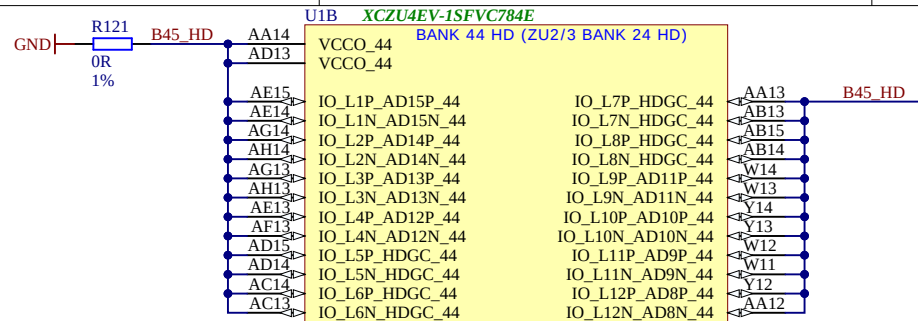
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A4	Number: TE0813 4DE81-A	Rev. 02
Date: 14.06.2023	Copyright: Trenz Electronic GmbH	Page 9 of 30
Filename: J4.SchDoc		

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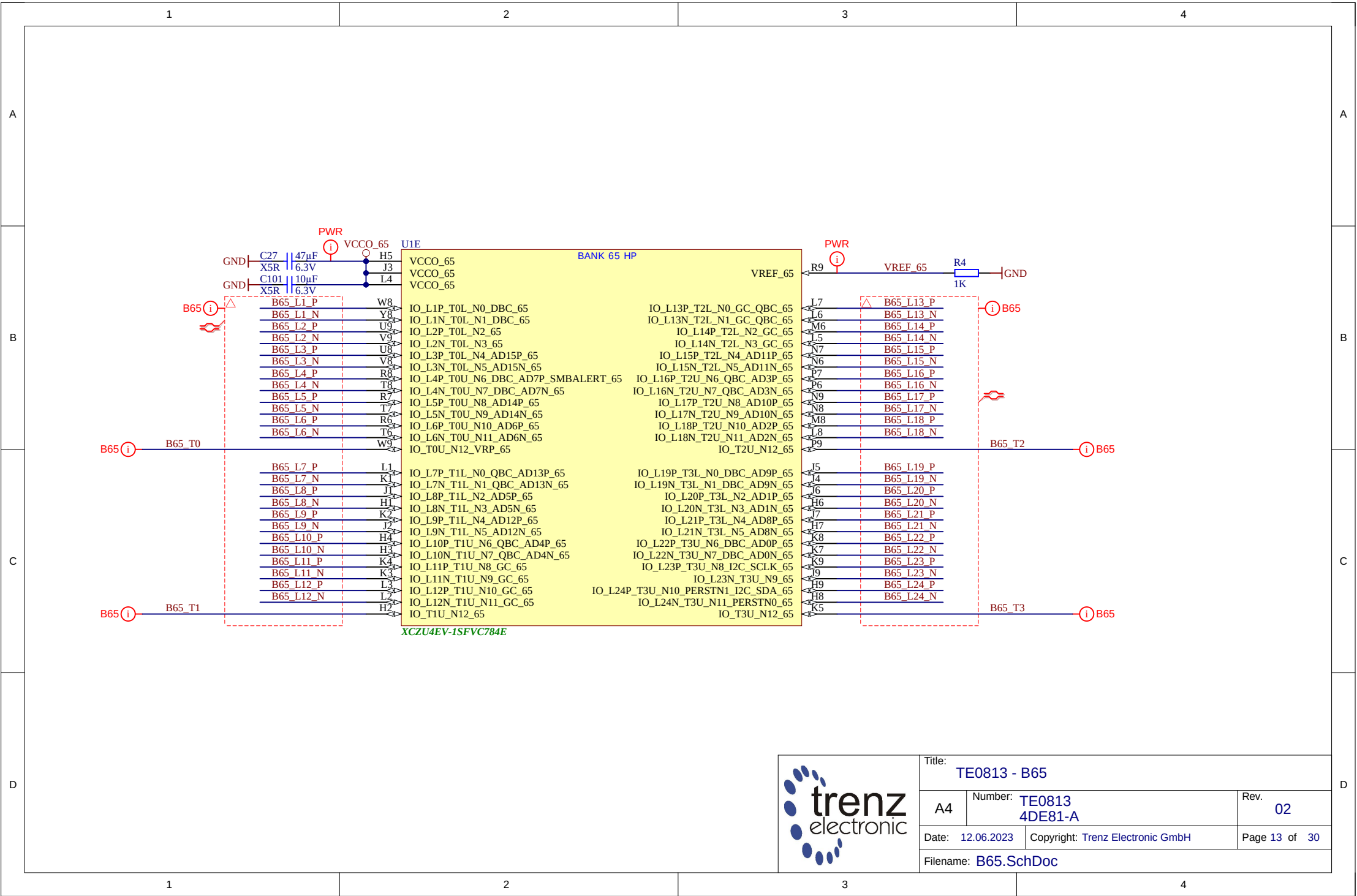
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A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 10 of 30
Filename: B_HD.SchDoc		

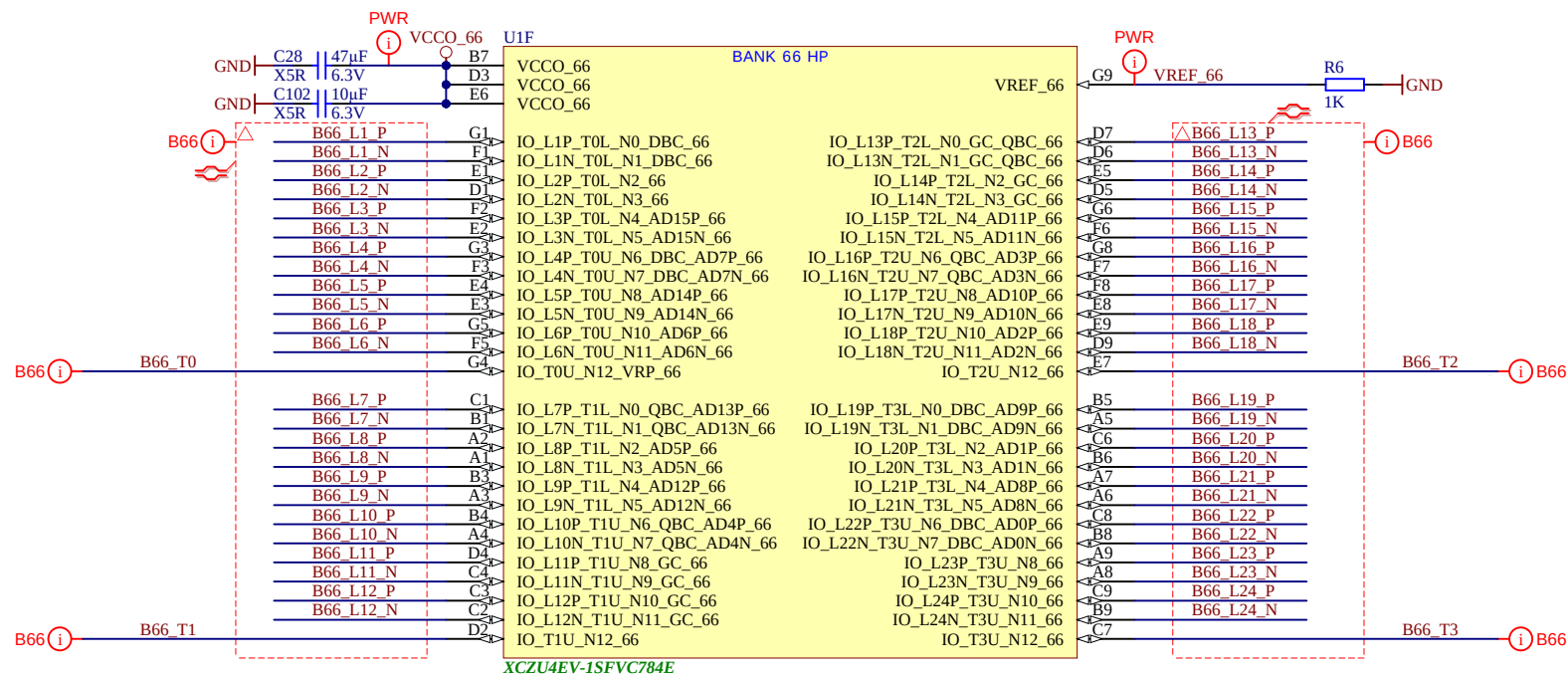
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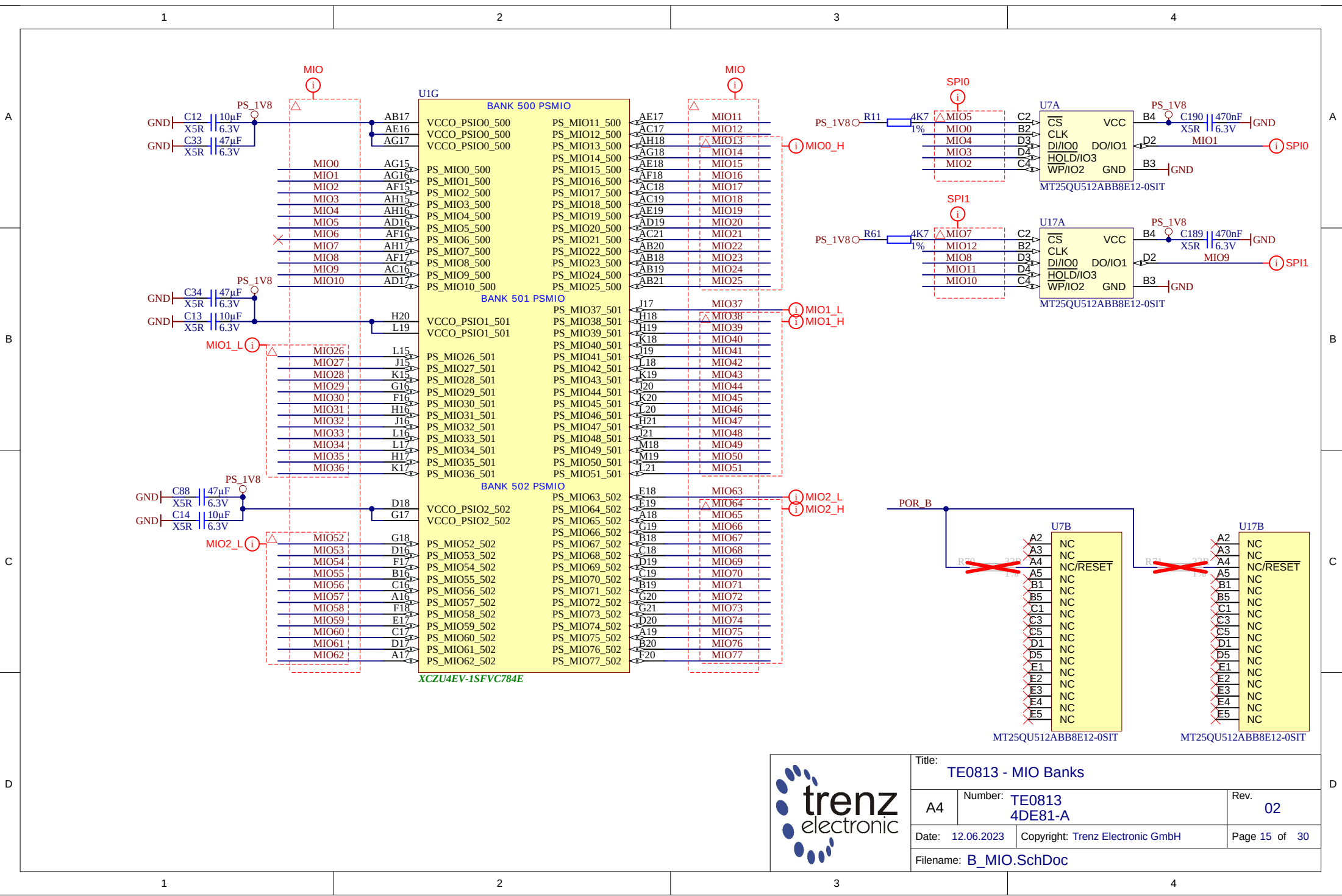
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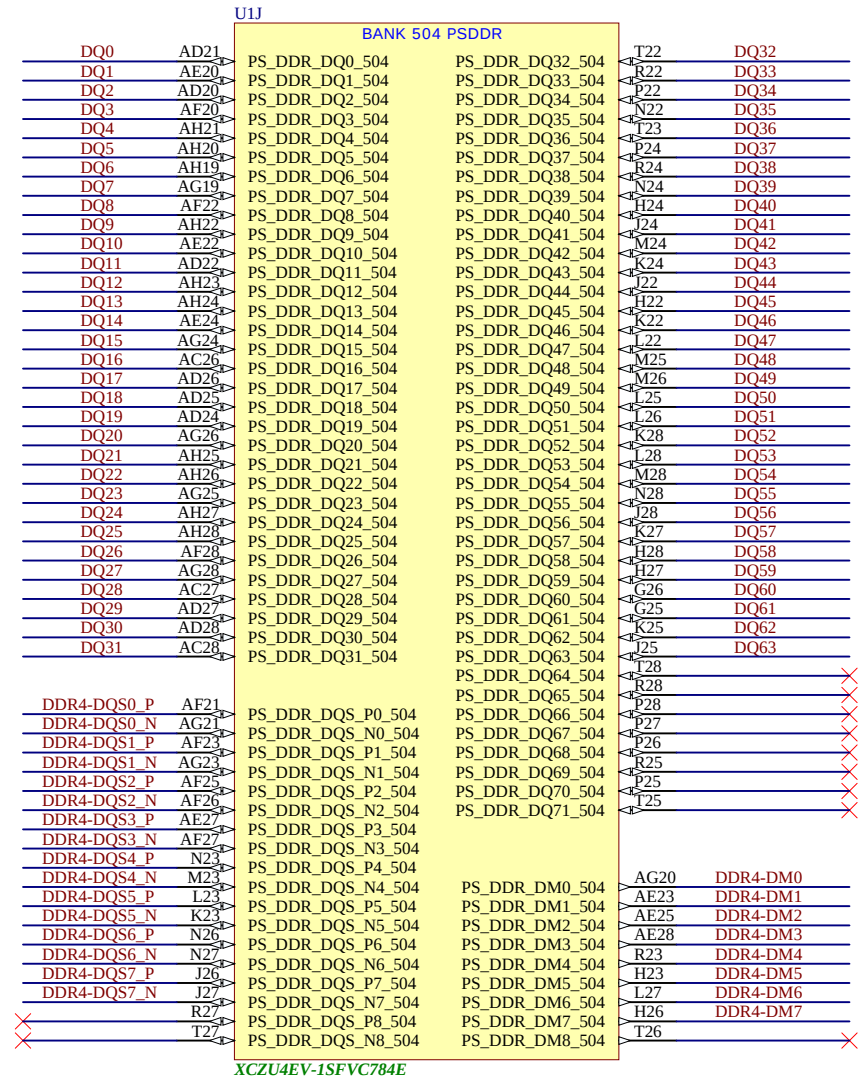
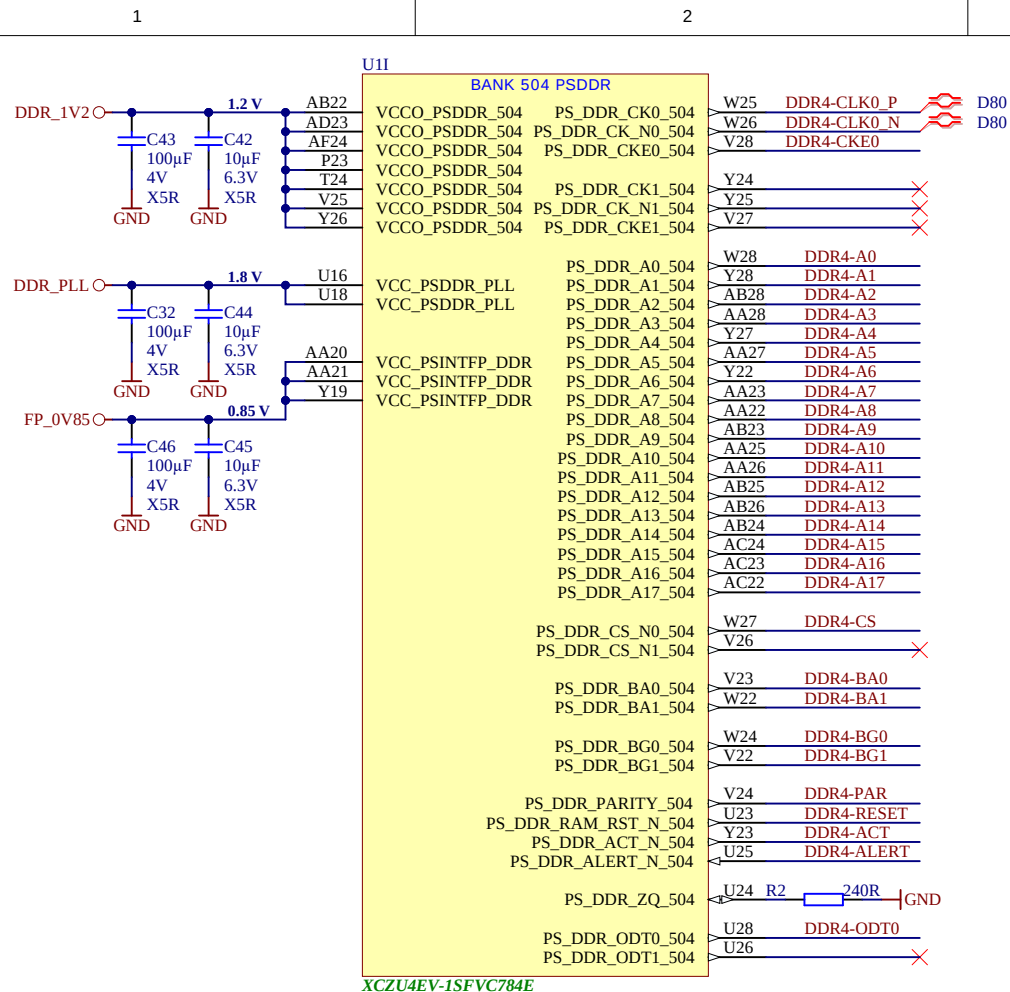




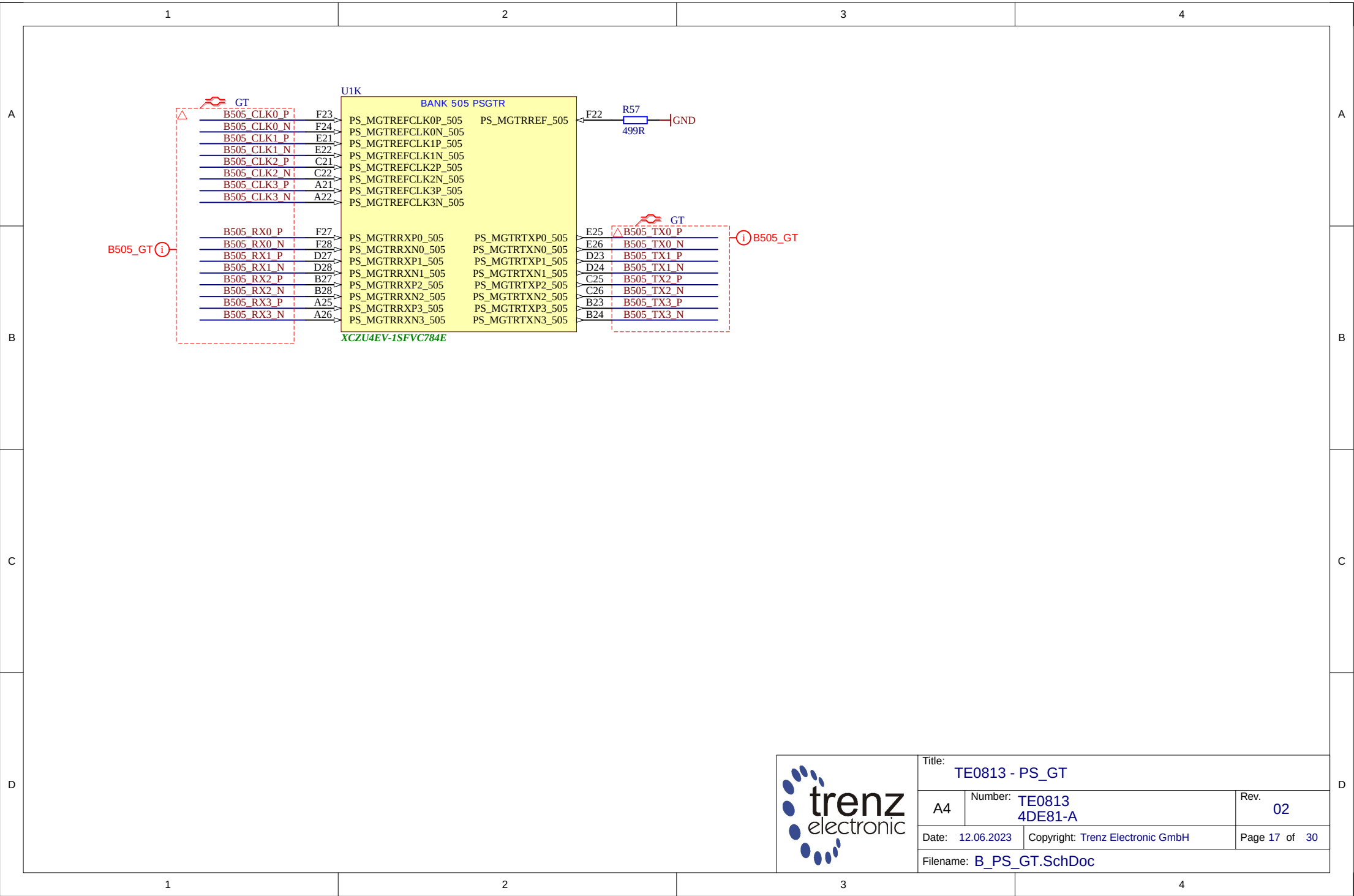
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Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 14 of 30
Filename: B66.SchDoc		

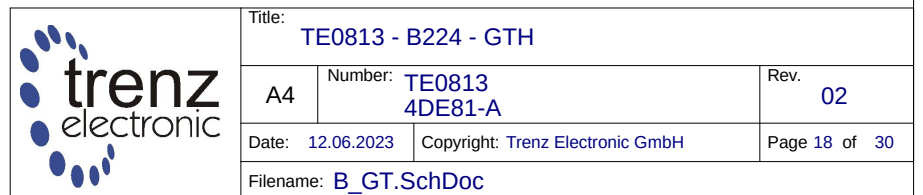


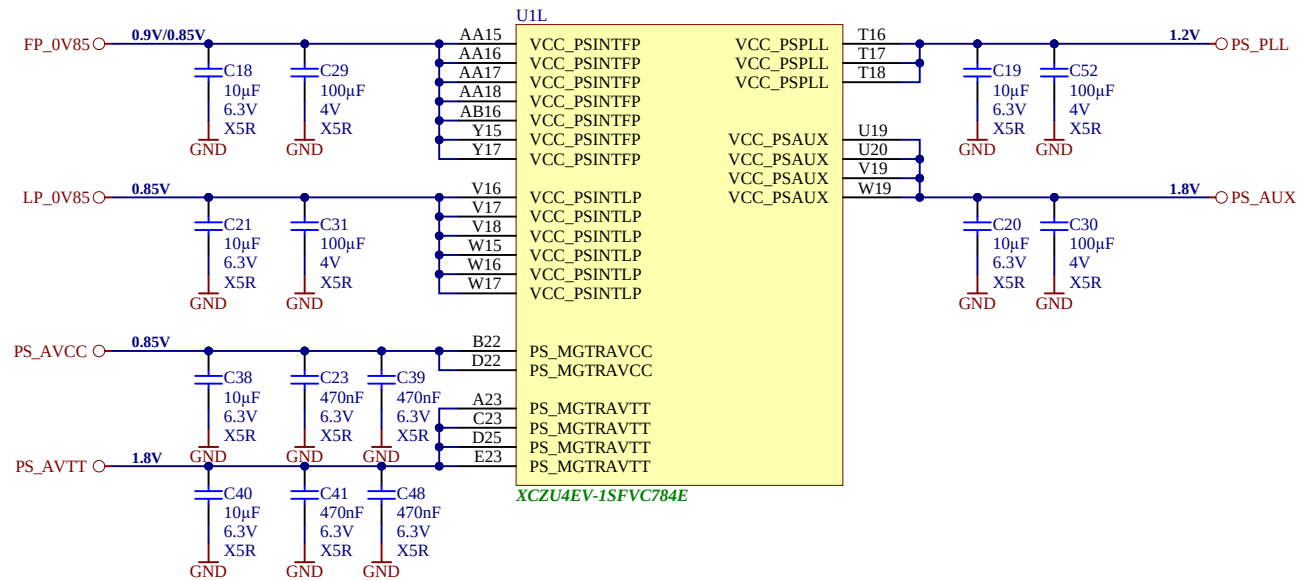
Title: TE0813 - MIO Banks		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 15 of 30
Filename: B_MIO.SchDoc		



Title: TE0813 - PS_DDR		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 16 of 30
Filename: PS_DDR.SchDoc		







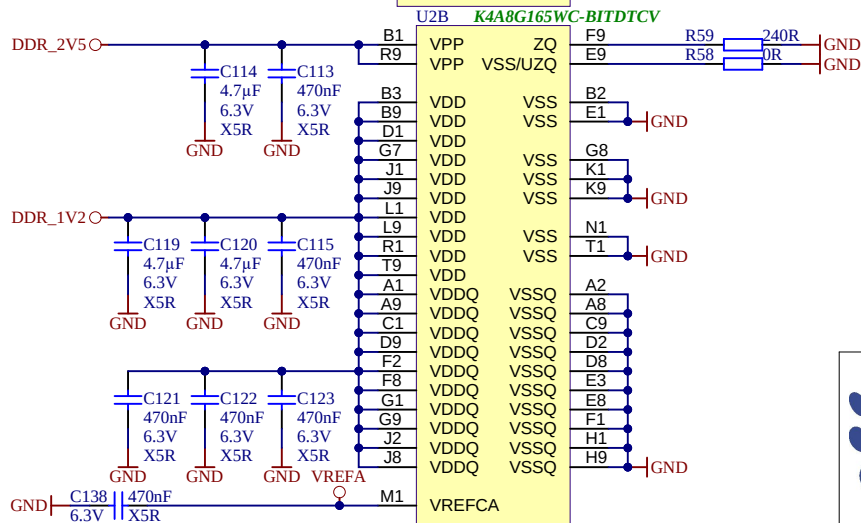
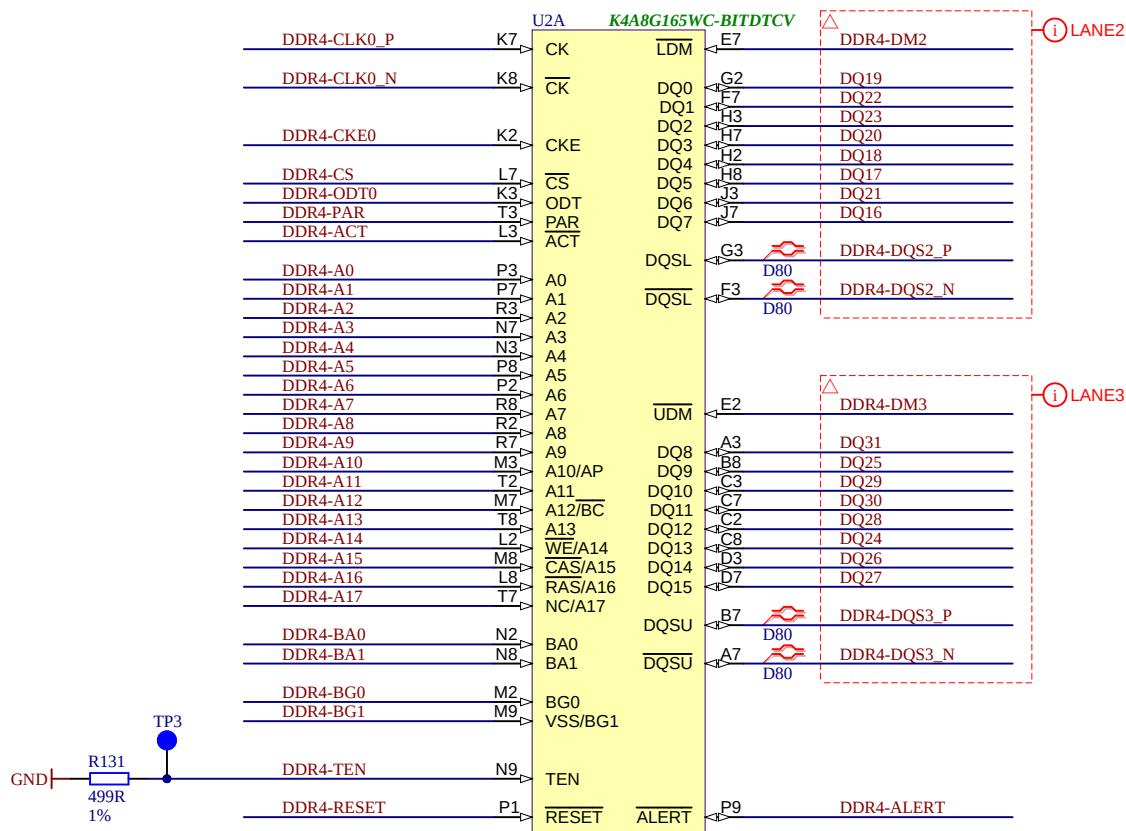
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	A4	Number: TE0813 4DE81-A	Rev. 02
	Date: 13.06.2023	Copyright: Trenz Electronic GmbH	Page 19 of 30
	Filename: ZU_PS_POWER.SchDoc		

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Title: TE0813 - DDR4_1_RAM		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 21 of 30
Filename: DDR4-RAM.SchDoc		

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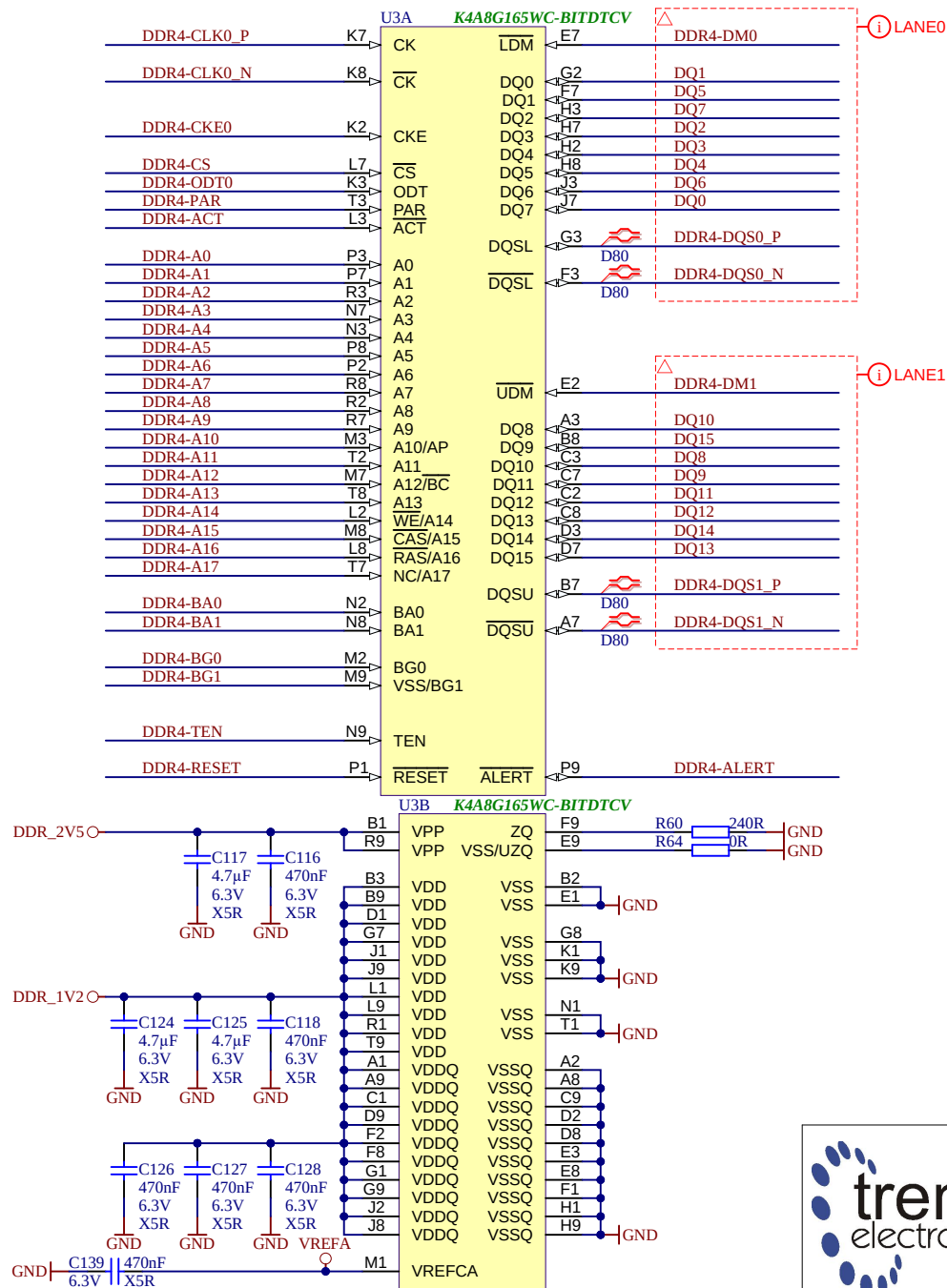
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Title: TE0813 - DDR4_2_RAM		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 22 of 30
Filename: DDR4-RAM_2.SchDoc		

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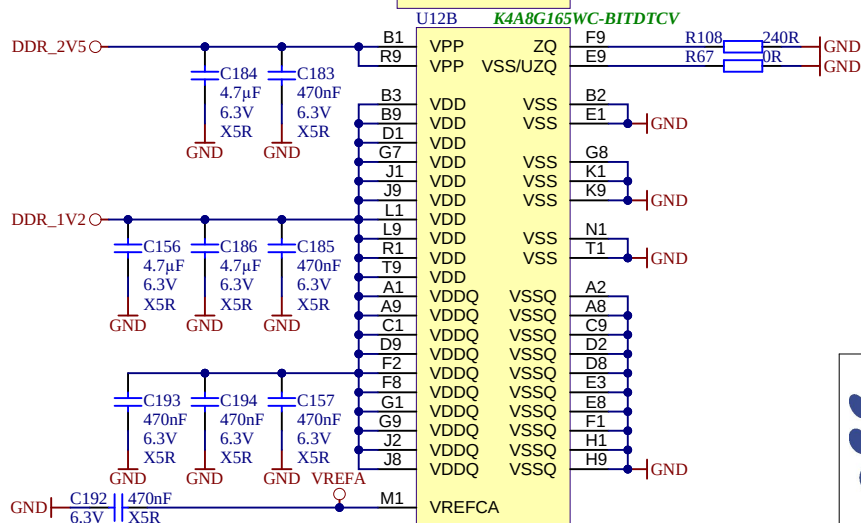
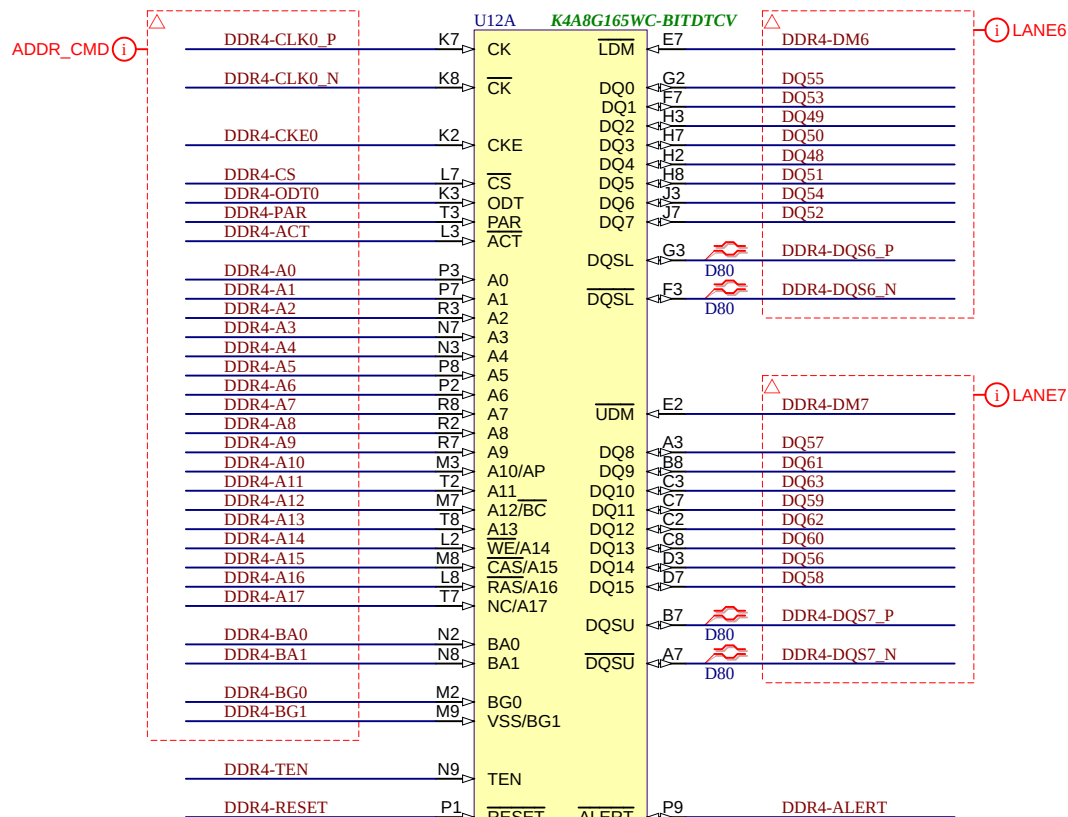
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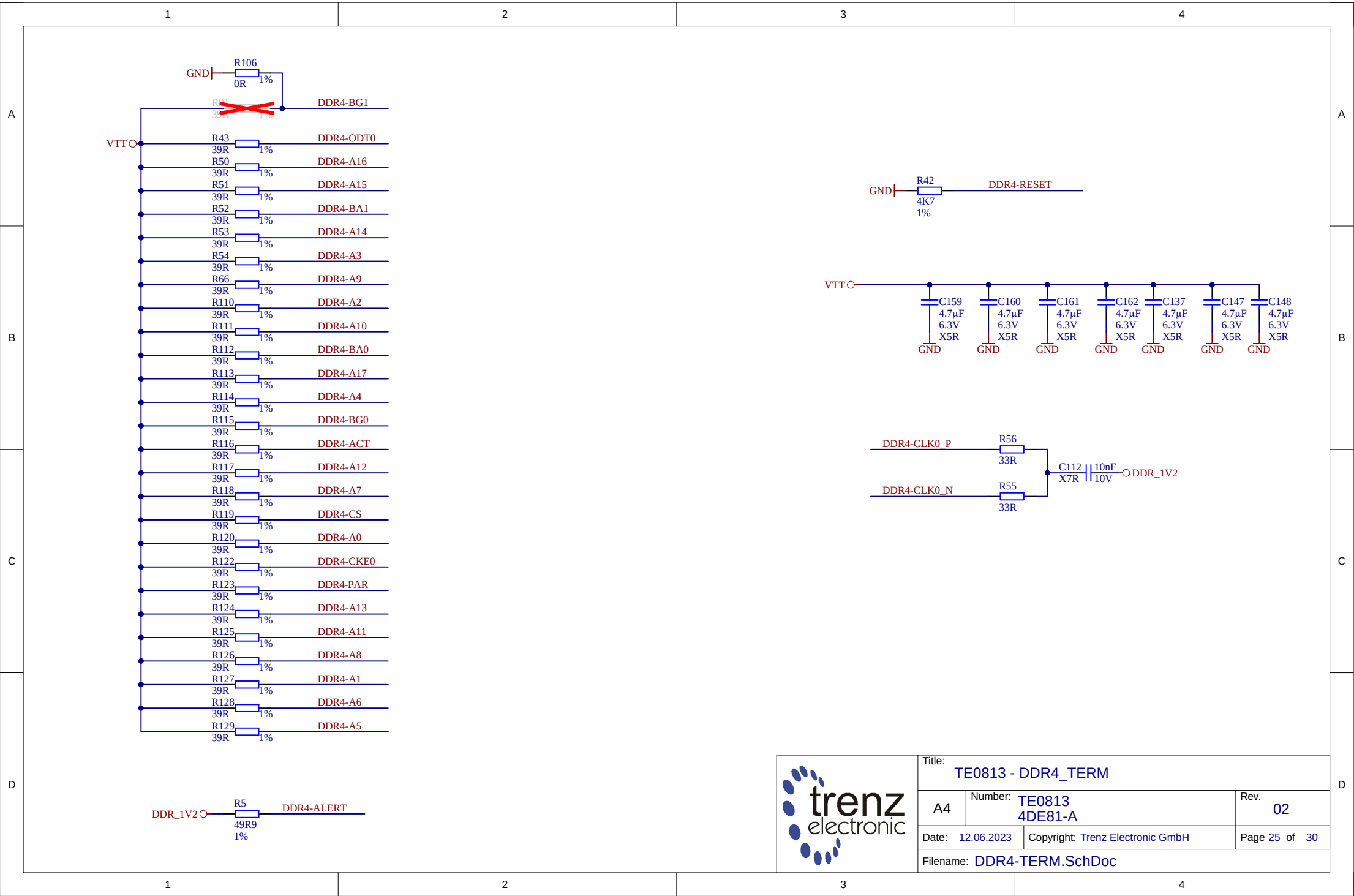
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A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 24 of 30
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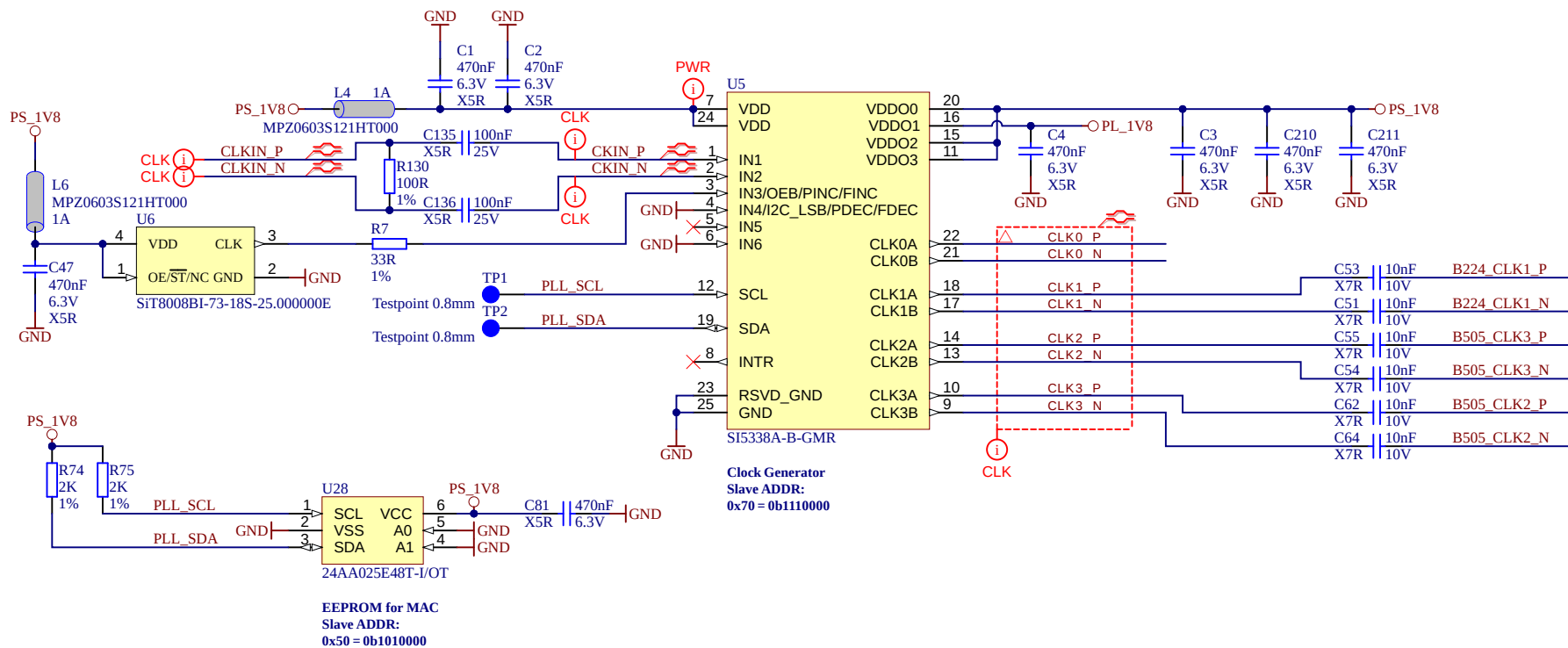
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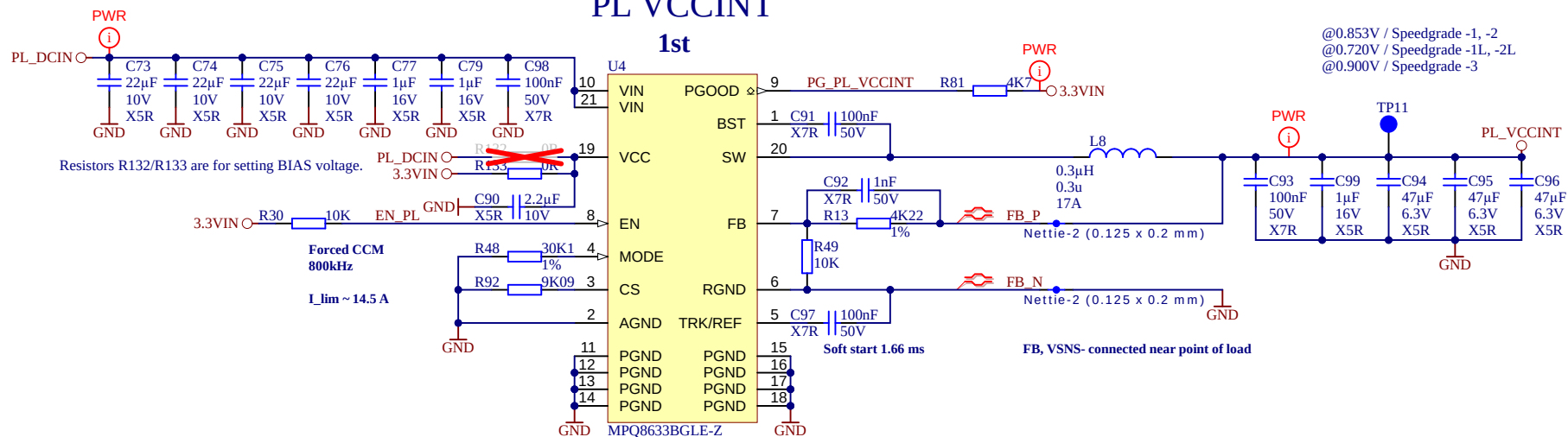
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Title: TE0813 - CLOCK		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 12.06.2023	Copyright: Trenz Electronic GmbH	Page 26 of 30
Filename: Clock.SchDoc		

PL VCCINT
1st



FPGA Speedgrade	R13	R49	PL_VCCINT
-1LI	2 kOhm	10 kOhm	0.720 V
-2LE	2 kOhm	10 kOhm	0.720 V
-1	4.22 kOhm	10 kOhm	0.853 V
-2	4.22 kOhm	10 kOhm	0.853 V
-3E	10 kOhm	20 kOhm	0.900 V

U4 pin compatible with

- TPS548B28 (20A)
- TPS548A28 (15A)
- TPS54JA20 (12A)



Title:	TE0813 - POWER_1
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A4	Number: TE0813 4DE81-A
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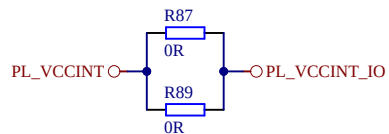
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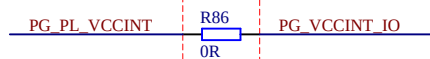
Rev.	02
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Page 27 of 30

Remove R87/89 when -1L1/2LE FPGA is used

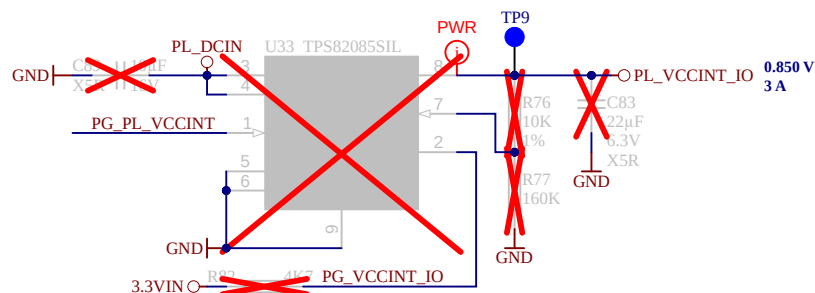


Remove R86 when -1L1/2LE FPGA is used

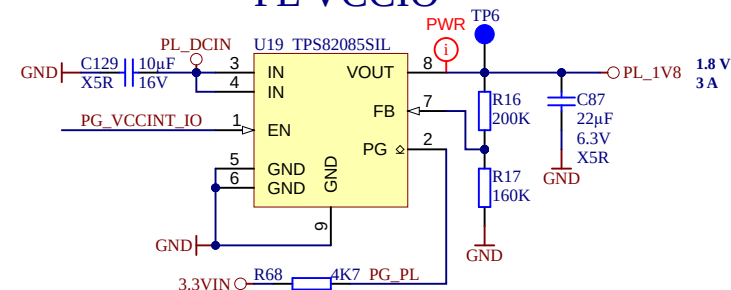


VCCINT_IO & VCCBRAM

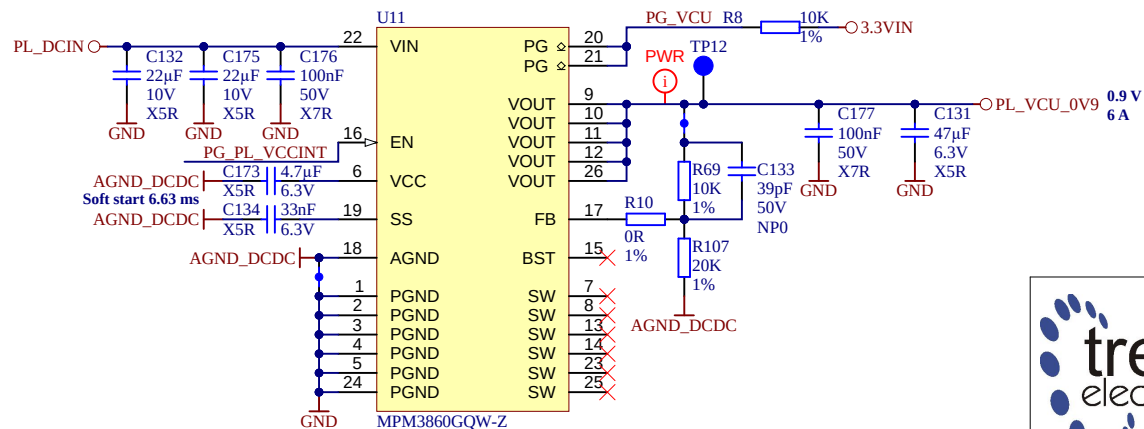
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PL VCCIO

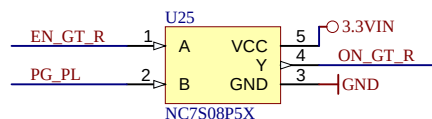
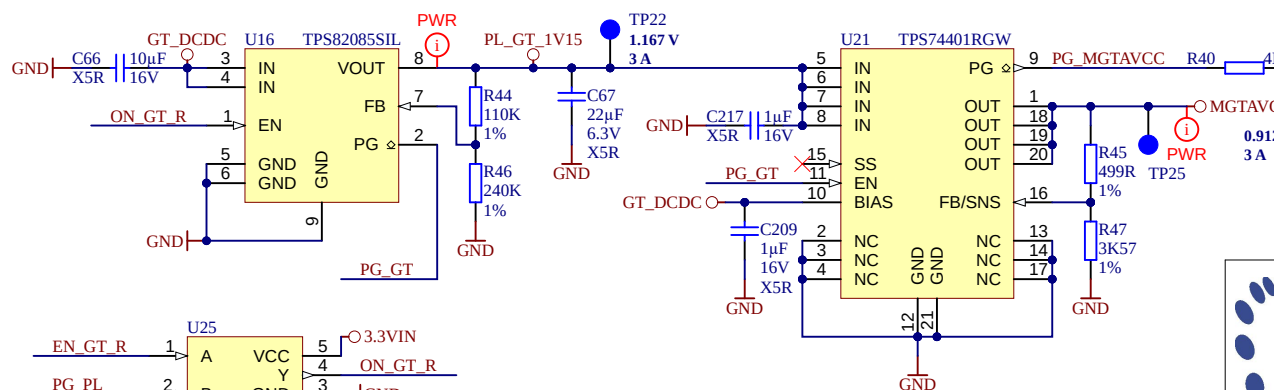
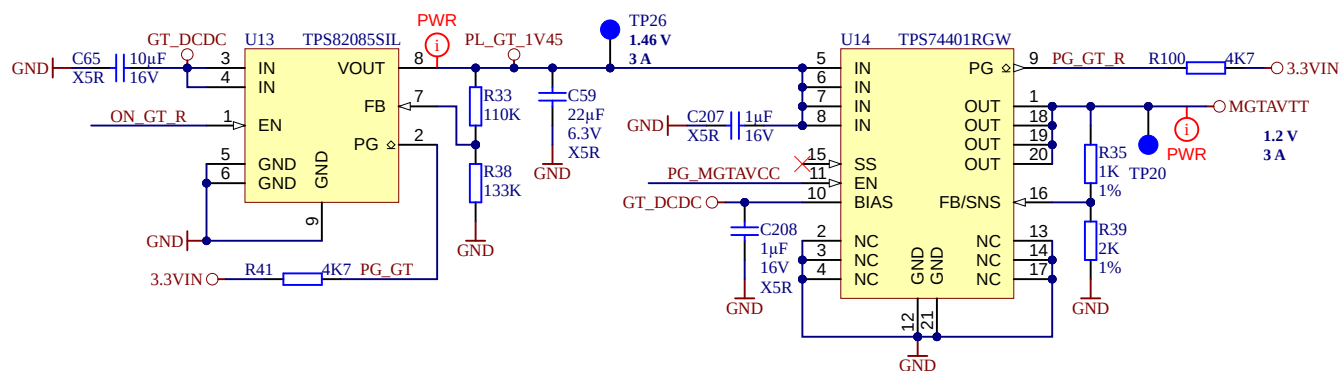
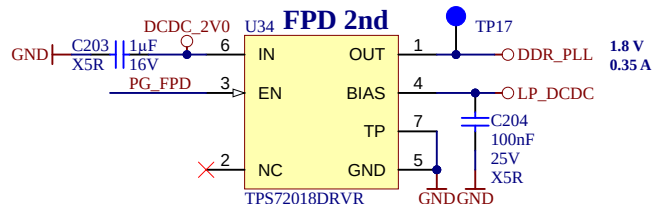
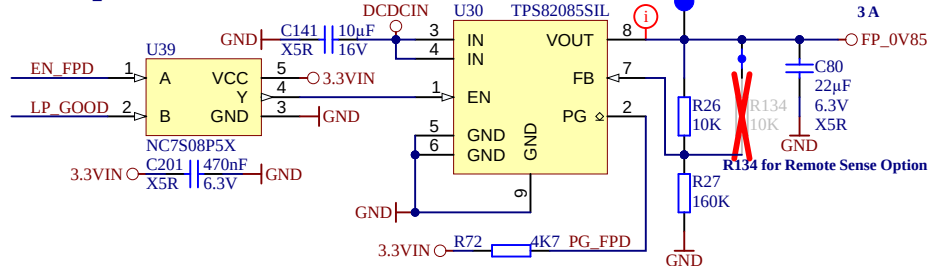


VCU

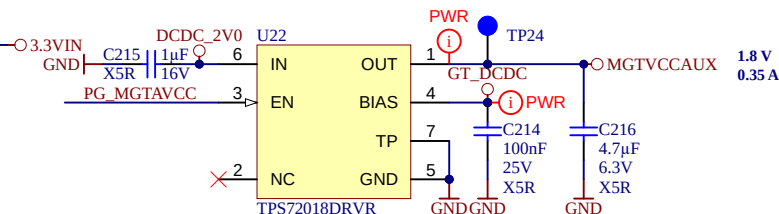
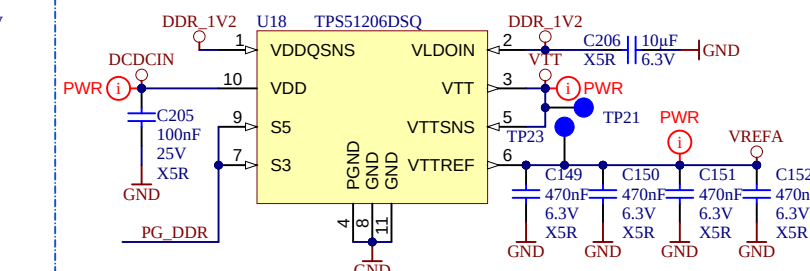
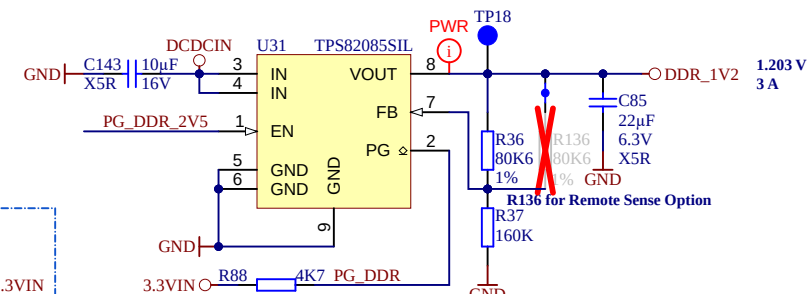
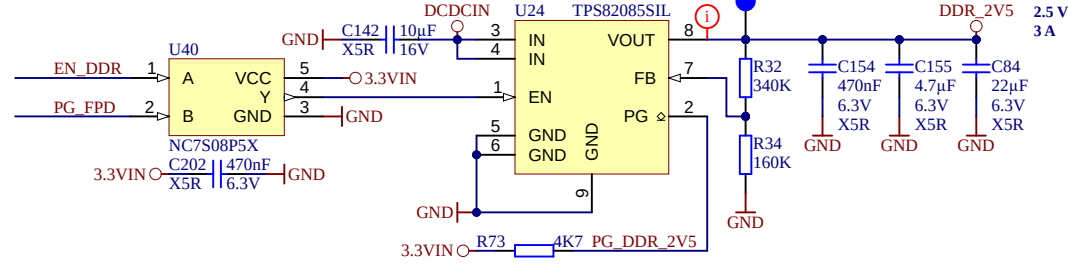


Title: TE0813 - POWER_2		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 14.06.2023	Copyright: Trenz Electronic GmbH	Page 28 of 30
Filename: POWER_1.SchDoc		

Full-power domain



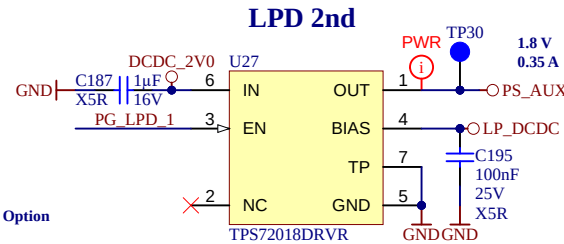
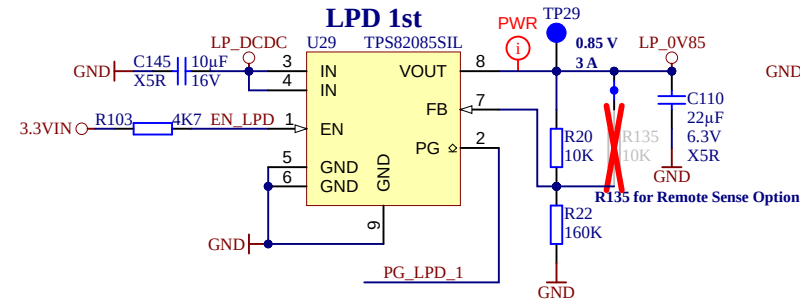
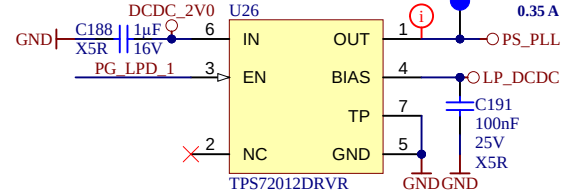
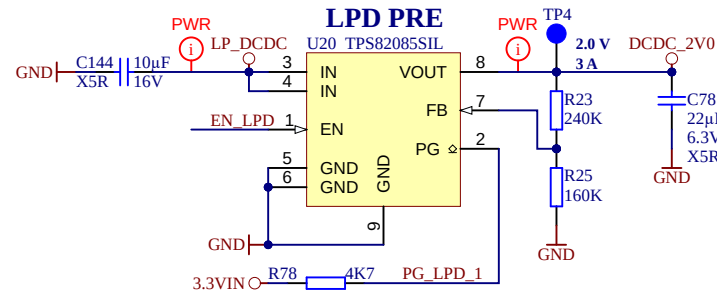
DDR4



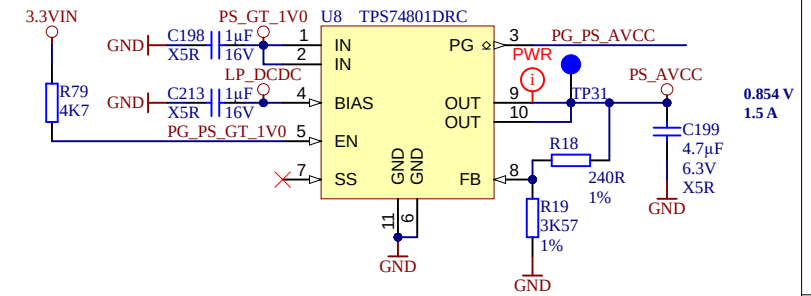
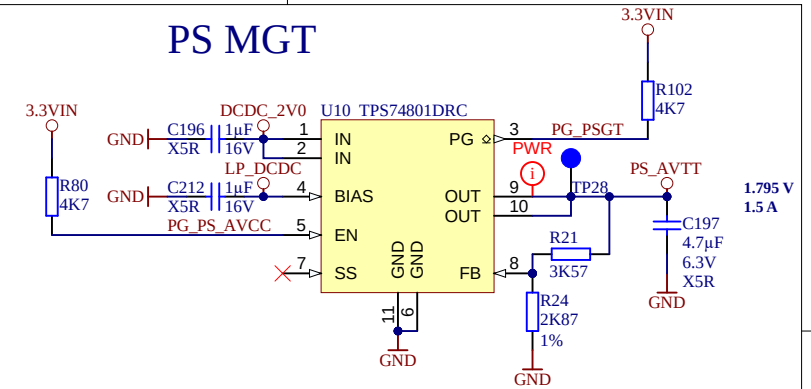
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A4	Number: TE0813 4DE81-A	Rev. 02
Date: 14.06.2023	Copyright: Trenz Electronic GmbH	Page 29 of 30
Filename: POWER_2.SchDoc		

PL MGT

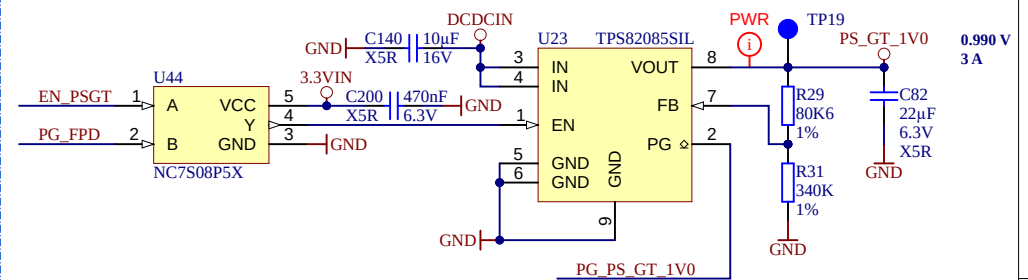
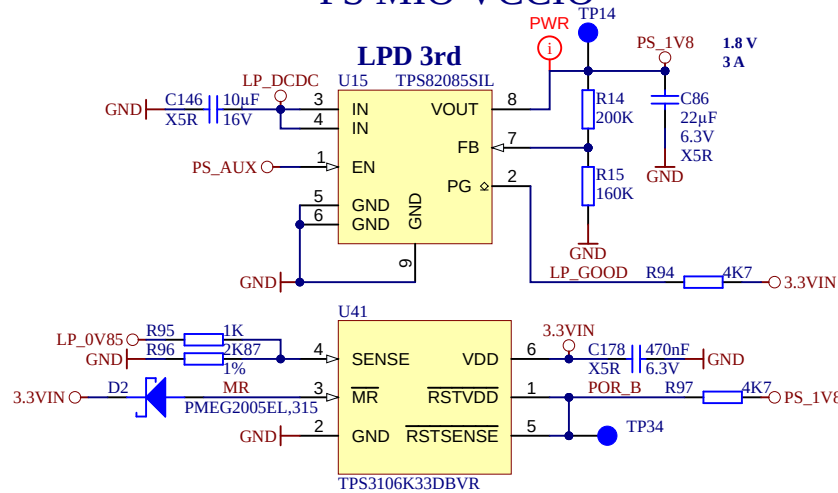
Low-power domain



PS MGT



PS MIO VCCIO



Net Name	Voltage Rail	Low Detect
LP_0V85	0.85 V	0.743 V
3.3VIN	3.3 V	2.941 V



Title: TE0813 - POWER_4		
A4	Number: TE0813 4DE81-A	Rev. 02
Date: 14.06.2023	Copyright: Trenz Electronic GmbH	
Filename: POWER_3.SchDoc		Page 30 of 30

Mouser Electronics

Authorized Distributor

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