

MGV High Current Molded SMT Power Inductors MGV0402 Series

FEATURES AND APPLICATIONS

Laird MGV series high current power inductors improve performance, reliability and power efficiency. A lower profile benefits consumer electronics and telecom design. Products feature extremely low DCR with greater efficiency and enable a large current in a small size. Inductors are of magnetic shielding and molded construction and perform in operating temperatures ranging from -40 C to 125 C including self-heating rise in temperature.

FEATURES

- Magnetic shielded structure
- Low DCR and high efficiency
- Low profile and miniaturization
- High reliability

APPLICATIONS

- DC-DC Converter and Power Suppliers
- LCD TV'S and Gaming Console
- Tablet, Notebooks, Servers and Printers
- Networking and Data storage
- GPS, Set-top-box and Base stations
- Smart meters and Medical instruments

PART NUMBER EXPLANATION



MGV	0402	4 R 7	7 M -	- 10
Product series code	Product size code	Inductance value code (i.e. 4R7: 4.7	Tolerance % (i.e. M: \pm 20%)	Standard Catalog P.N

Note: Automotive grade parts are also available, a specific P.N will be assigned upon request. Please contact laird local sales for details.

ELECTRICAL SPECIFICATIONS

- Tolerance: M: ±20% or N: ±30%
- Inductance tested at 100KHz, 1.0V
- Heat Rated Current (Irms) is defined based on temperature rise approximate 40°C without core loss (ambient temperature 25±5°C)
- Saturation Current (Isat) is the DC current at which the inductance drops off approximately 30% from its value without current. (ambient temperature 25±5°C)
- Operating temperature range: -40°C~+125°C (including self-heating temperature rise)
- Storage temperature range (packaging conditions): -10°C~+40°C and RH 60%(MAX.)

Note: Heat Rated Current (Irms) is tested on a typical PCB and apply a constant current in still air.

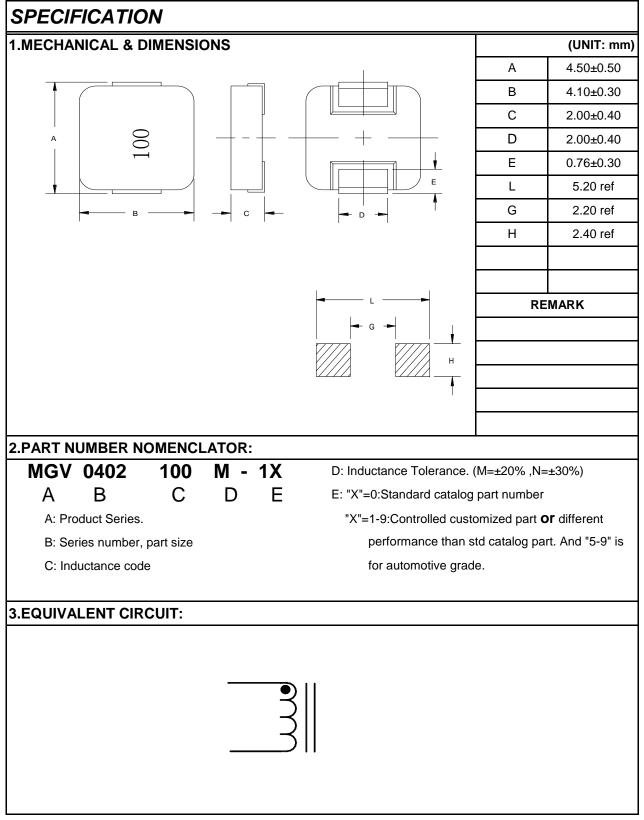
The temperature rise is dependent on the application system condition including PCB PAD pattern, trace width and thickness and adjacent components etc. It's suggested to verify the temperature rise of the component under the real operation application conditions.



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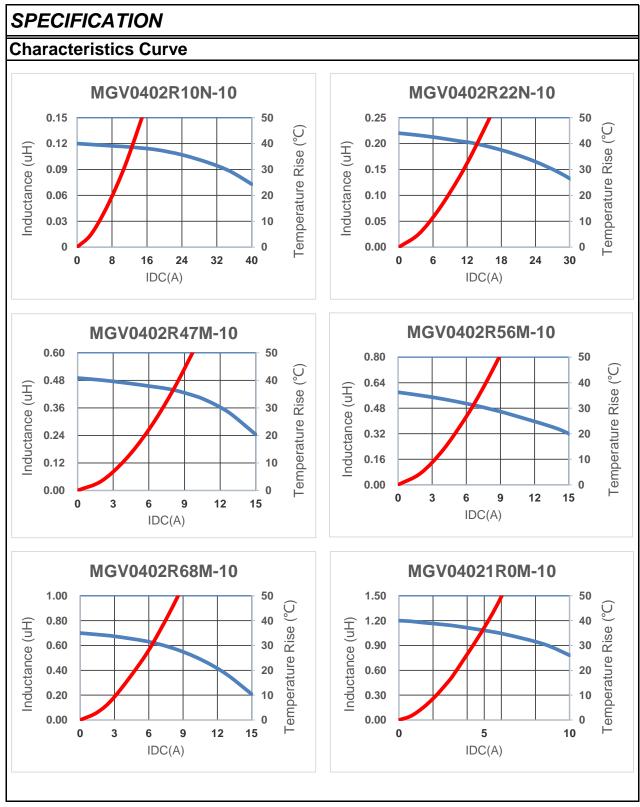
(uH) 0.10±30% 0.22±30%	Irms(A) Typ. 12.0		DCR(mΩ) Typ	DCR(mΩ) Max	REMARK
		35.0	3.2	4.0	
0.22±30%	13.0	24.0	6.6	7.3	
0.47±20%		12.0	11.2	14.0	
	8.0				
4.70±20%		3.5	95.0	105.0	
6.80±20%	2.1	2.8	150.0	172.0	
10.0±20%	1.8	2.3	215.0	243.0	
15.0±20%	1.5	1.9	325.0	374.0	
22.0±20%	1.2	1.4	470.0	500.0	
ICATION:					
at 100KHz, 0.25V					
nt (Irms) is defined	based on tem	perature rise	approximate 40°	C without core lo	SS
ture 25±5°C)					
t (Isat) is the DC cu	rrent at which	the inductan	ice drops off app	roximately 30% fi	rom
current. (ambient 1	temperature 2	5±5°C)			
ature range: -40°C	~+125°C (inclu	iding self-hea	ting temperature	rise)	
ure range (packagi	ng conditions)	: -10°C~+40°C	Cand RH 60%(MA	X.)	
	$10.0\pm20\%$ $15.0\pm20\%$ $22.0\pm20\%$ FICATION: at 100KHz, 0.25V at (Irms) is defined at ure 25\pm5°C) t (Isat) is the DC cu current. (ambient the ature range: -40°C	$0.68\pm20\%$ 7.0 $1.00\pm20\%$ 5.0 $1.20\pm20\%$ 4.8 $1.50\pm20\%$ 4.5 $2.20\pm20\%$ 4.0 $3.30\pm20\%$ 3.5 $4.70\pm20\%$ 2.6 $6.80\pm20\%$ 2.1 $10.0\pm20\%$ 1.8 $15.0\pm20\%$ 1.5 $22.0\pm20\%$ 1.2 FICATION: at 100KHz, $0.25V$	$0.68\pm20\%$ 7.010.0 $1.00\pm20\%$ 5.0 8.5 $1.20\pm20\%$ 4.8 7.8 $1.50\pm20\%$ 4.5 7.0 $2.20\pm20\%$ 4.0 6.0 $3.30\pm20\%$ 3.5 4.0 $4.70\pm20\%$ 2.6 3.5 $6.80\pm20\%$ 2.1 2.8 $10.0\pm20\%$ 1.8 2.3 $15.0\pm20\%$ 1.5 1.9 $22.0\pm20\%$ 1.2 1.4 FICATION:at 100KHz, $0.25V$ at 100KHz, $0.25V$ true $25\pm5^{\circ}C$)ture $25\pm5^{\circ}C$)ature range: -40°C~+125°C (including self-head)	0.68±20% 7.0 10.0 16.0 1.00±20% 5.0 8.5 23.0 1.20±20% 4.8 7.8 25.0 1.50±20% 4.5 7.0 34.8 2.20±20% 4.0 6.0 51.0 3.30±20% 3.5 4.0 69.0 4.70±20% 2.6 3.5 95.0 6.80±20% 2.1 2.8 150.0 10.0±20% 1.8 2.3 215.0 15.0±20% 1.5 1.9 325.0 22.0±20% 1.2 1.4 470.0 FICATION: at 100KHz, 0.25V true z5±5°C) t (Isat) is the DC current at which the inductance drops off approximate 40° ture 25±5°C) ature range: -40°C~+125°C (including self-heating temperature	0.68±20% 7.0 10.0 16.0 19.0 1.00±20% 5.0 8.5 23.0 27.0 1.20±20% 4.8 7.8 25.0 30.0 1.50±20% 4.5 7.0 34.8 42.0 2.20±20% 4.0 6.0 51.0 61.0 3.30±20% 3.5 4.0 69.0 76.0 4.70±20% 2.6 3.5 95.0 105.0 6.80±20% 2.1 2.8 150.0 172.0 10.0±20% 1.8 2.3 215.0 243.0 15.0±20% 1.2 1.4 470.0 500.0 22.0±20% 1.2 1.4 470.0 500.0 21.0±20% 1.2 1.4 470.0 500.0 21.0±20% 1.2 1.4 470.0 500.0 21.0±20% 1.2 1.4 470.0 500.0 21.0±20% 1.2 1.4 470.0 500.0 21.0±255°C) 1.2



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Molded SMT Power Inductors

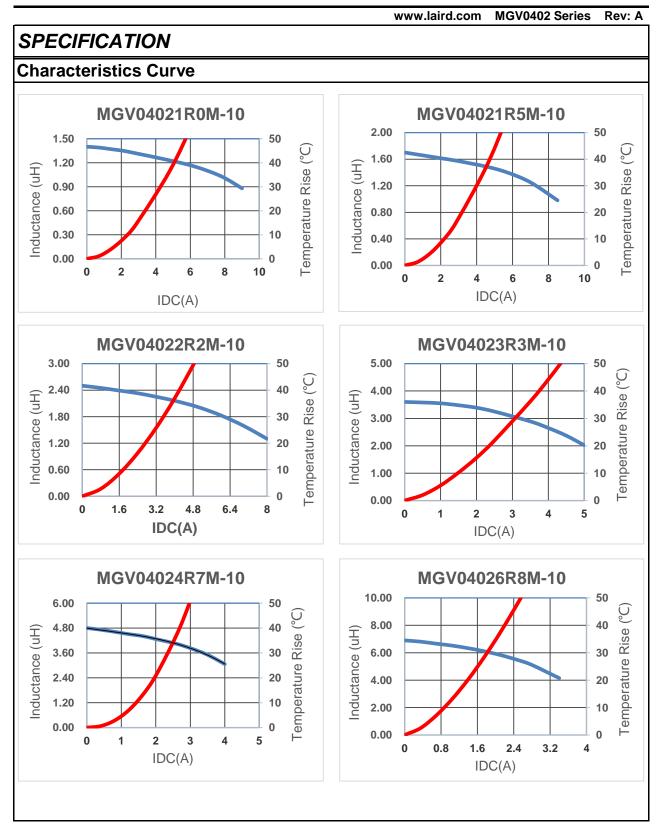
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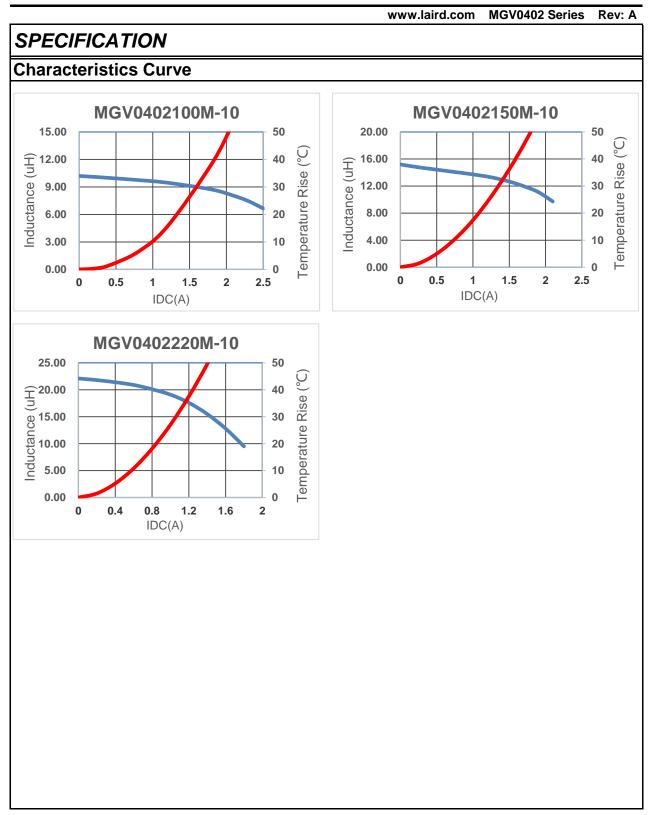
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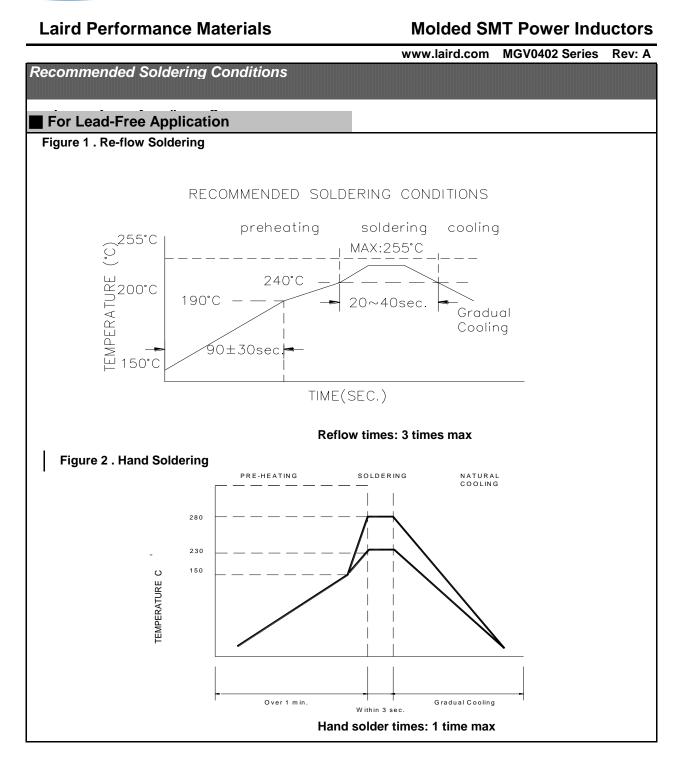


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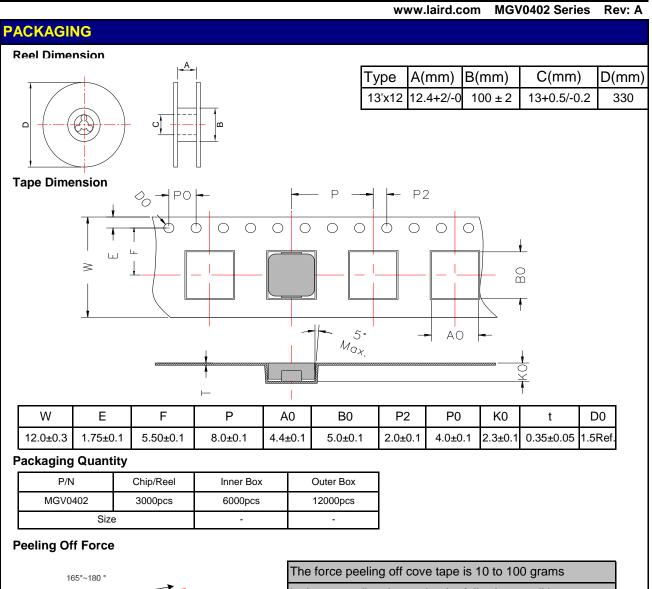
Molded SMT Power Inductors

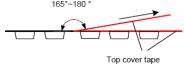
		www.laird.com MGV0402 Series Rev: A						
Reliability and Te	stina Conditions / Pin Tvpe Po	wer Inductors						
SMD series(Consumer)								
Item	Reference	Additional Requirements						
Operating temperature range	-55℃~ +125℃ (Including self-temperature rise)							
Storage temperature and humidity range	-10°⊂ to +40°⊂ , 60% RH Max							
High Temperature Exposure (Storage)	MIL-STD-202 Method 108	85±2℃, 168+24hours						
Temperature Cycling	JESD22 Method JA-104	-40°C→+85, transforming interval:20s, 100cycles						
Operational Life	MIL-PRF-2	85±℃, 168+24hours Apply maximum rated voltage and current according part drawing						
External Visual	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship. Electrica Test not required.						
Physical Dimension	JESD22 Method JB-100	Verify physical dimensions to the applicable device detail specification. Note: User(s) and Suppliers spec. Electrical Test not required						
Vibration	MIL-STD-202 Method 204	10~55Hz,1.5mm, 2 hours in each 3mutually perpendicular directions (total of 6 hours)						
Resistance to Soldering Heat	MIL-STD-202 Method 210	1. Max. 260±5 °C,10±1s, 2 times 2.Solder Composition: Sn/3Ag/0.5Cu						
Solderability	J-STD-002	245±5°C, 5±1sec, Solder: Sn/3.0Ag/0.5Cu						
Electrical Characterization	Print Spec	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max Operating temperatures						
Board Flex	AEC-Q200-005	2mm,30±1s						
Terminal Strength(SMD)	AEC-Q200-006	10N, 5S, X,Y direct						



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The force peeling off cove tape is 10 to 100 grams						
in the arrow direction under the following conditions						
Room Temp	Room	Room atrn	Teaming			
(°C)	Humidity	(hPa)	Speed			
5~35	45~85	860~1060	300			

Storage Conditions

1. Temperature and humidity conditions: -10-+40 $^\circ\!\mathrm{C}$

- and 60% RH.
- 2. Recommended products should be used within 12 month
- from the time of manufacturing.

3. The packaging material should be kept where no chloring

- or sulfur exists in the air.
- 4. Allowable stacking condition of Packaging box: max height 1.5m or 5 boxes stacking

Mouser Electronics

Authorized Distributor

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Laird Performance Materials:

<u>MGV04021R5M-10</u> <u>MGV04022R2M-10</u> <u>MGV04023R3M-10</u> <u>MGV0402R47M-10</u> <u>MGV0402R56M-10</u> <u>MGV04026R8M-10</u> <u>MGV0402R22N-10</u> <u>MGV0402100M-10</u> <u>MGV0402R68M-10</u> <u>MGV0402R10N-10</u> <u>MGV04024R7M-10</u> <u>MGV04021R2M-10</u> <u>MGV0402150M-10</u> <u>MGV0402220M-10</u>