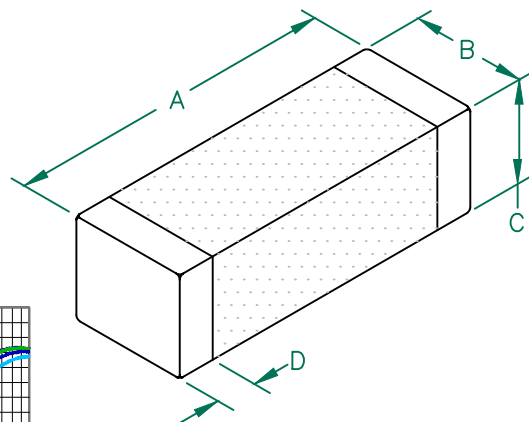


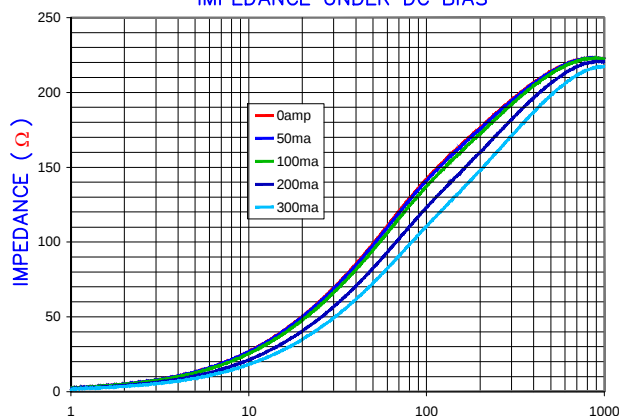
LI1806C151R-10

PHYSICAL DIMENSIONS:

A	4.50 [.177]	+ 0.25 [.010]
B	1.60 [.063]	+ 0.25 [.010]
C	1.60 [.063]	+ 0.25 [.010]
D	0.51 [.020]	+ 0.25 [.010]

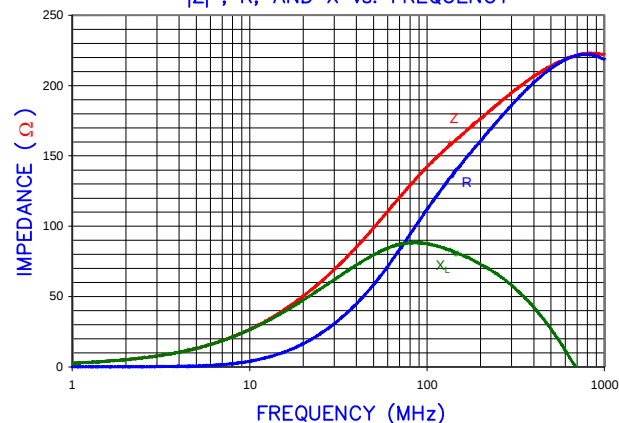


Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

|Z|, R, AND X vs. FREQUENCY



FREQUENCY (MHz)

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3124

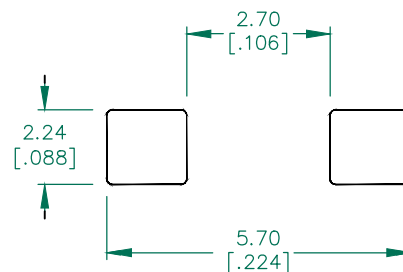
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	150	
Minimum	113	
Maximum	188	0.500 300 mA

NOTES: UNLESS OTHERWISE SPECIFIED

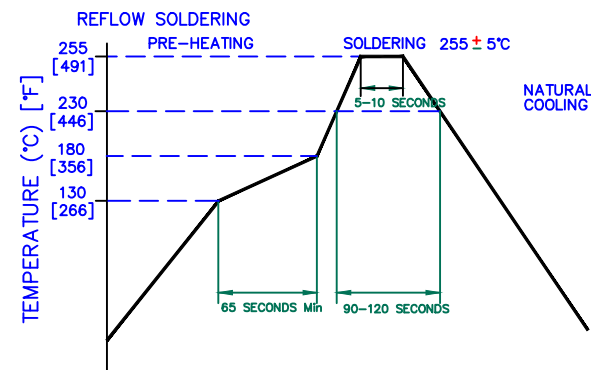
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2,000 PCS/REEL.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



RoHS

DIMENSIONS ARE IN mm [INCHES]				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
E	ADD NOTES 3 AND NOTES 4 UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU				
D	CHANGE REEL QTY FROM 3K TO 2K	11/05/08	JRK				
C	UPDATE COMPANY LOGO ADD ROHS SYM	07/22/08	JRK				
B	ADD DC BIAS CURVE CHG REFLOW TEMP. CORRECT LANDPATTERN DIMS	03/17/03	JRK				
A	ORIGINAL DRAFT	08/07/02	JRK				
REV	DESCRIPTION	DATE	INT	PROJECT/PART NUMBER: LI1806C151R-10 DATE: 08/07/02 CAD # SCALE: NTS TOOL # - SHEET: 1 of 1			

Mouser Electronics

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