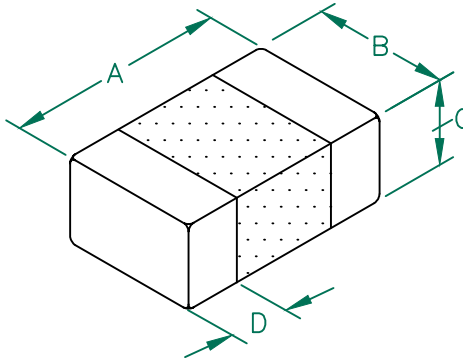


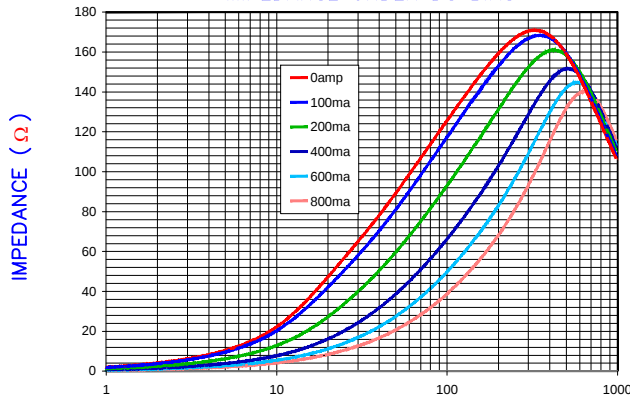
LI0805H121R-10

PHYSICAL DIMENSIONS:

A	2.00 [.079]	$\pm$	0.20 [.008]
B	1.25 [.049]	$\pm$	0.20 [.008]
C	0.90 [.035]	$\pm$	0.20 [.008]
D	0.51 [.020]	$\pm$	0.25 [.010]

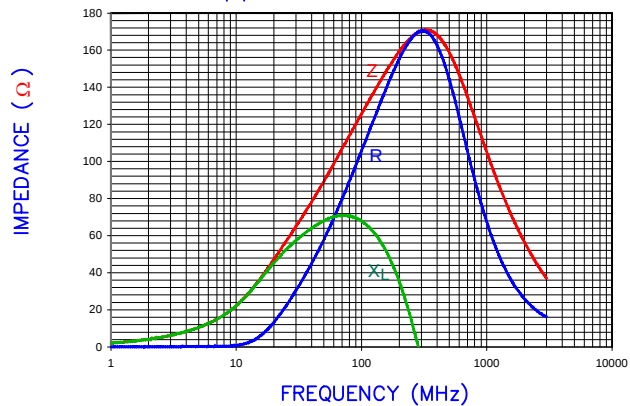


Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

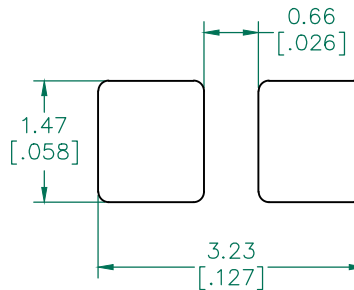
|Z|, R, AND X vs. FREQUENCY



FREQUENCY (MHz)

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3235

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

RoHS

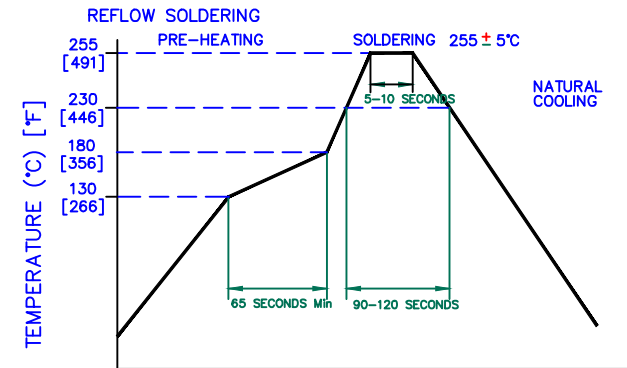
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	120	
Minimum	90	
Maximum	150	0.15
		800 mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$. (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES]

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E	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU
D	CHANGE TO PAPER TAPE	03/04/10	JUN
C	UPDATE COMPANY LOGO ADD ROHS SYM	07/21/08	JRK
B	ADD DC BIAS CURVE UPDATE ZRX CURVE	04/07/03	JRK
A	ORIGINAL DRAFT	10/30/02	JRK
REV	DESCRIPTION	DATE	INT
PROJECT/PART NUMBER: LI0805H121R-10			
DATE: 10/30/02		SCALE: NTS	SHEET: 1 of 1
CAD #		TOOL #	
LI0805H121R-10-E			

Laird

REV E PART TYPE: CO-FIRE DRAWN BY: JRK

Mouser Electronics

Authorized Distributor

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