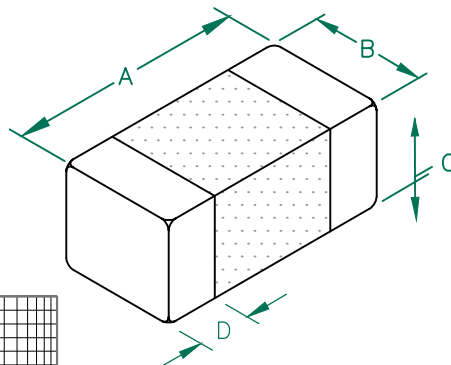


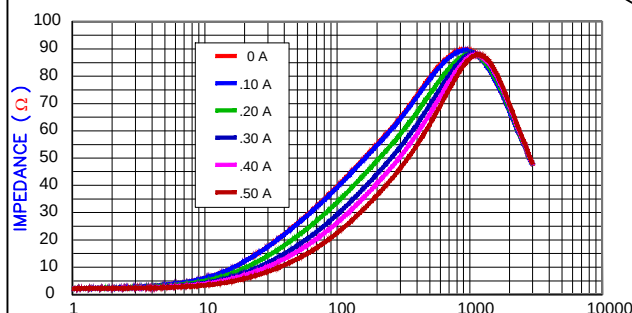
LI0603E470R-10

PHYSICAL DIMENSIONS:

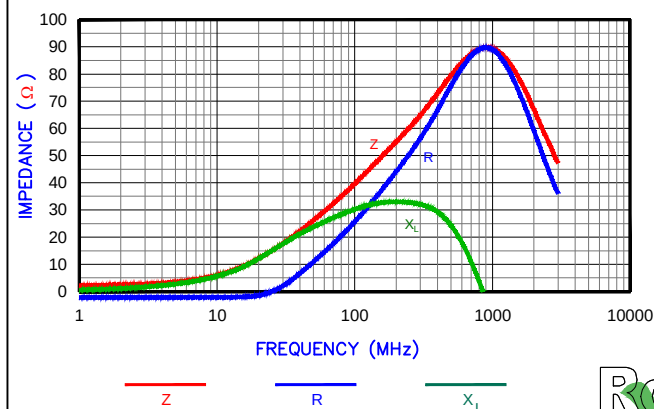
A	1.60 [.063]	+	0.15 [.006]
B	0.80 [.031]	+	0.15 [.006]
C	0.80 [.031]	+	0.15 [.006]
D	0.36 [.014]	+	0.15 [.006]



Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)
|Z|, R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 5544

RoHS

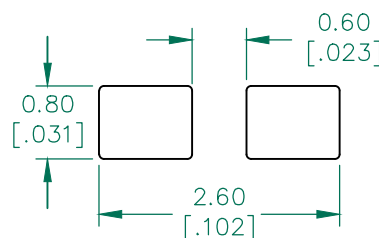
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	47	
Minimum	35	
Maximum	59	0.10
		500 mA

NOTES: UNLESS OTHERWISE SPECIFIED

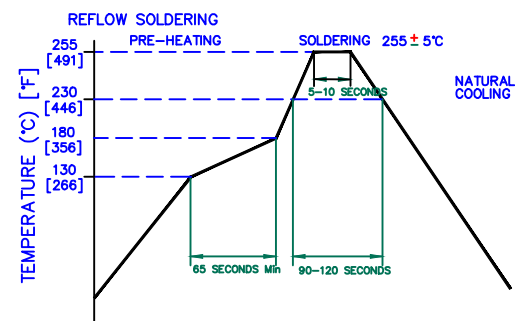
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES]				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
C	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	LI0603E470R-10	REV	C
B	UPDATE COMPANY LOGO	07/14/08	JRK	DATE	03/29/06	PART TYPE:	CO-FIRE
A	ORIGINAL DRAFT	03/29/08	JRK	SCALE	-	DRAWN BY:	JRK
REV	DESCRIPTION	DATE	INT	INT	LI0603E470R-10-C	SHEET:	1 of 1

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