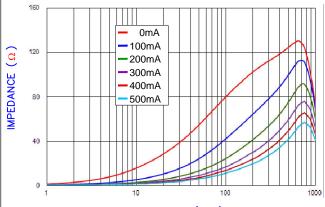
# LI0402E800R-10

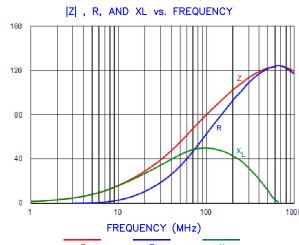
### PHYSICAL DIMENSIONS:

A 1.01 [.040] ± 0.18 [.007] ± 0.20 [.008] B 0.50 [.020] ± 0.20 [.008] C 0.50 [.020] D 0.30 [.012] MAX.

#### Z vs. FREQUENCY IMPEDANCE UNDER DC BIAS



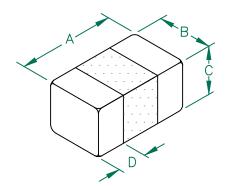
FREQUENCY (MHz)



AGILENT E4991A RF Impedance/Material Analyzer

AGILENT 16194A Test Fixture.

IMPEDANCE (Q)

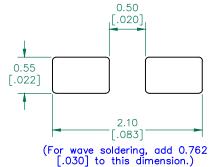


ELECT	ELECTRICAL CHARACTERISTICS:								
Z @ 100MHz (Ω)		DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current						
Nominal	80								
Minimum	60								
Maximum	100	0.17	500 mA						

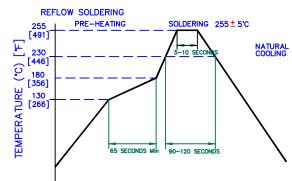
#### NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)
- 5. COSMETIC SPECIFICATION REFER TO WI-QA-124.

#### LAND PATTERNS FOR REFLOW SOLDERING



#### RECOMMENDED SOLDERING CONDITIONS





	DIMENSIONS ARE IN mm [INCHES]. This print is the property of Laird											
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				subject to return upon request an with the understanding that no								
				copies shall be made without the								
				written consent of Laird Tech. All								
				rights to design or invention are								
				reserved.								
				PROJECT/PART NUMBER:	REV	PART TO	rPE:	DRAWN BY:				
				110400E000D 10	ΙA	CO-	-FIRE	QU				
				LI0402E800R-10		CO-	FIRE	QU				
				DATE: 05/16/13 SC/	ALE: N	TS	SHEET:					
Α	ORIGINAL DRAFT	05/16/13	QU	CAD #	OL #		1 of 1					
REV	DESCRIPTION	DATE	INT	LI0402E800R-10-A	~ 7	-	'	01 1				

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