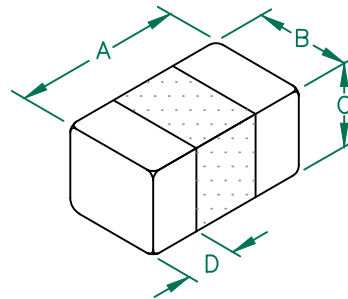


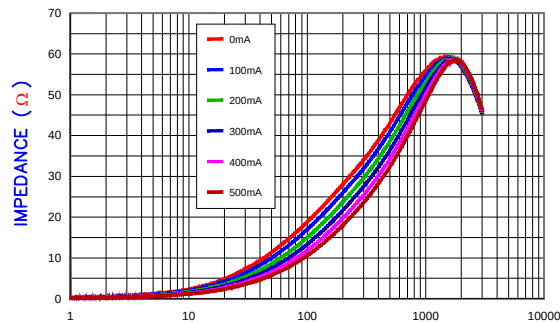
LI0402E190R-10

PHYSICAL DIMENSIONS:

A	1.00 [.040]	+	0.15 [.006]
B	0.50 [.020]	+	0.15 [.006]
C	0.50 [.020]	+	0.15 [.006]
D	0.30 [.012]	+	MAX.

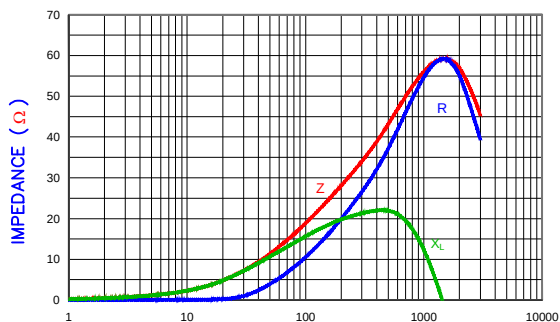


Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

|Z|, R, AND X_L vs. FREQUENCY

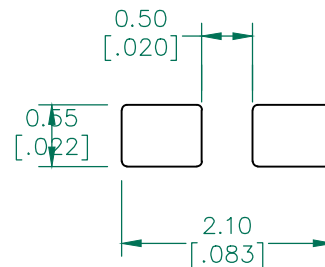


FREQUENCY (MHz)

— Z — R — X_L

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 6231

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

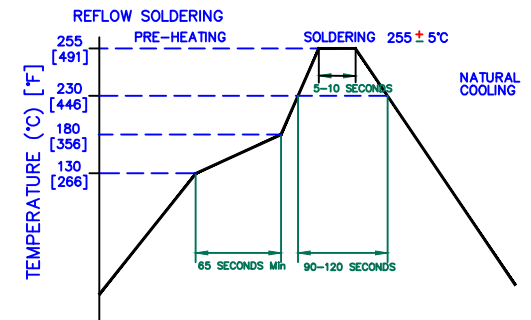
ELECTRICAL CHARACTERISTICS:

	Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	19		
Minimum	14		
Maximum	24	0.10	500 mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
C	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER: LI0402E190R-10			
B	UPDATE COMPANY LOGO	07/09/08	JRK	DATE	07/24/07	SCALE	NTS
A	ORIGINAL DRAFT	07/24/07	JRK	CAD #	LI0402E190R-10-C	TOOL #	-
REV	DESCRIPTION	DATE	INT	SHEET: 1 of 1			



REVISION: C PART TYPE: CO-FIRE DRAWN BY: JRK

Mouser Electronics

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