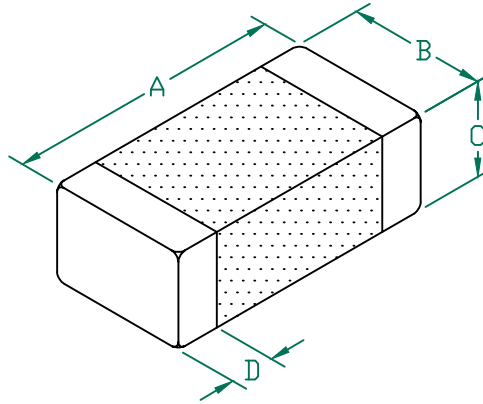


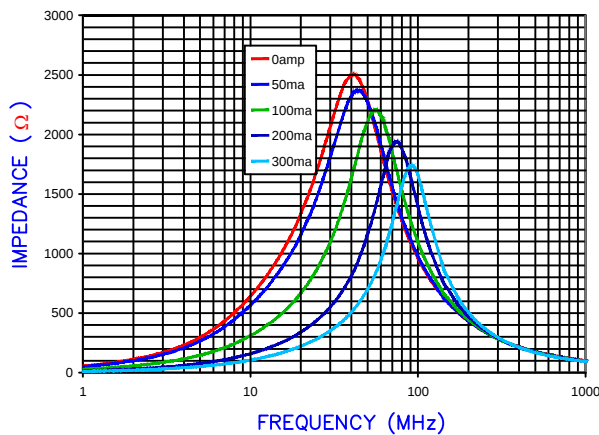
LF1206C202R-10

PHYSICAL DIMENSIONS:

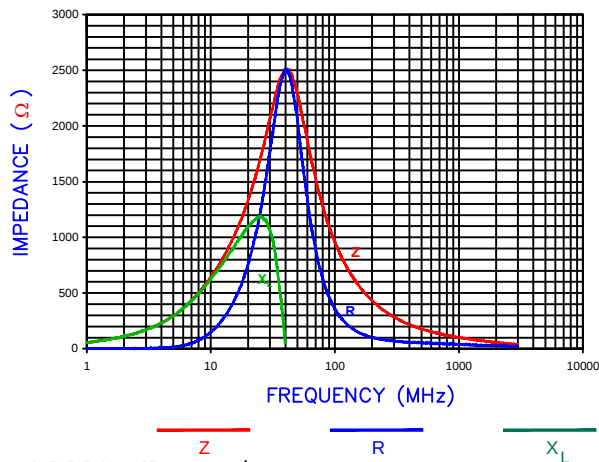
A	3.20 [.126]	+ 0.20 [.008]
B	1.60 [.063]	+ 0.20 [.008]
C	1.10 [.043]	+ 0.20 [.008]
D	0.51 [.020]	+ 0.25 [.010]



Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS

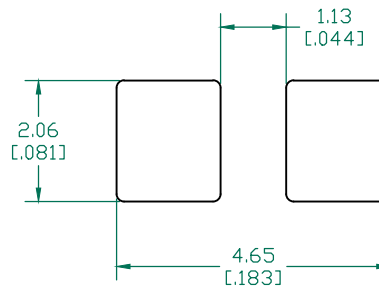


|Z|, R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3228

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762
[.030] to this dimension)



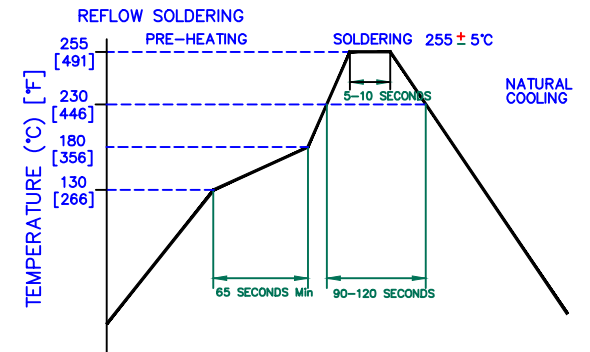
ELECTRICAL CHARACTERISTICS:

Z @ 30MHz (Ω)	DCR (Ω)	Rated Current
Nominal	2000	
Minimum	1500	
Maximum	2500	0.50
		300 mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 3000 PCS/REEL, EMBOSSED PLASTIC TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].

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PROJECT/PART NUMBER:				REV	PART TYPE:
C ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE				C	CO-FIRE
B UPDATE COMPANY LOGO				DRAWN BY: JRK	
A ORIGINAL DRAFT				DATE:	SCALE:
REV DESCRIPTION				09/13/06	NTS
DATE INT				CAD #	SHEET:
09/13/06 JRK				LF1206C202R-10-C	1 of 1
				TOOL #	
				-	

Mouser Electronics

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