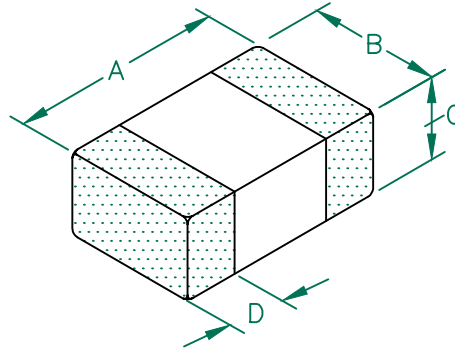


IC0805B182R-10

PHYSICAL DIMENSIONS:

A	2.00 [.079]	+ 0.20[.008]
B	1.25 [.049]	+ 0.20[.008]
C	0.85 [.033]	+ 0.20[.008]
D	0.50 [.020]	+ 0.30[.012]



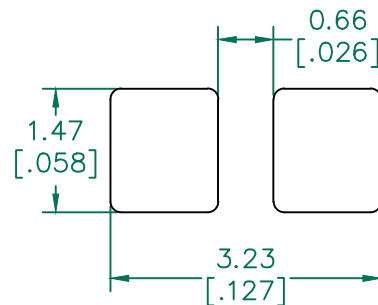
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, EMBOSSED PLASTIC TAPE.
2. TERMINATION FINISH IS 100% MATTE Sn OVER Ni.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. I (MAX.) IS BASED ON THE MAXIMUM SUSTAINED CURRENT APPLIED WHILE MAINTAINING A MAXIMUM TEMPERATURE RISE OF 40°C OVER AMBIENT.
5. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

ELECTRICAL CHARACTERISTICS:

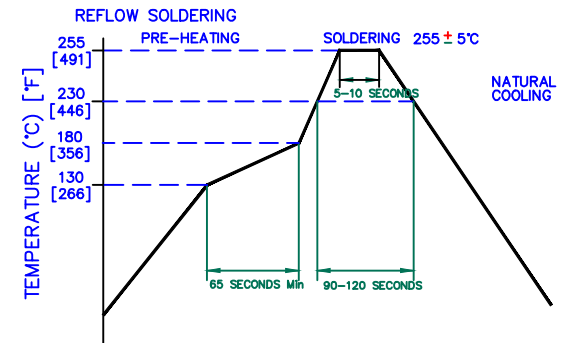
L (nH) ± 10%	1,980	Max
	1,800	Nom
	1,620	Min
Q (Min)	45	
Freq. (MHz)	10	
Self-Resonant Freq (MHz)	55	
DCR(Max) Ω	0.60	
I (Max)	250mA	

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



RoHS

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
F	REMOVE NOTE 5	07/21/16	QU				
E	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU				
D	UPDATE COMPANY LOGO	06/16/09	JRK				
C	MODIFY NOTES CHG C & D DIM. CHG SRF	01/17/07	JRK				
B	CHG P/N ADD 250mA TO MIN L & ADD I (OPERATING) TO ELECT BOX UPDATE LANDPATTERN DIMS REV/ADD NOTES	12/29/05	TMB				
A	ORIGINAL DRAFT	05/13/04	JRK	PROJECT/PART NUMBER: IC0805B182R-10 DATE: 05/13/04 SCALE: NTS SHEET: 1 of 1			
REV	DESCRIPTION	DATE	INT	CAD # IC0805B182R-10-F TOOL # -			

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