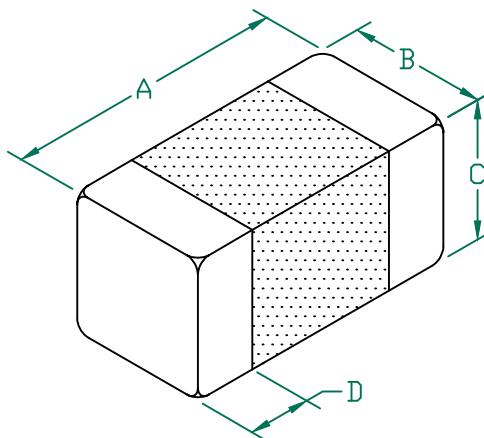


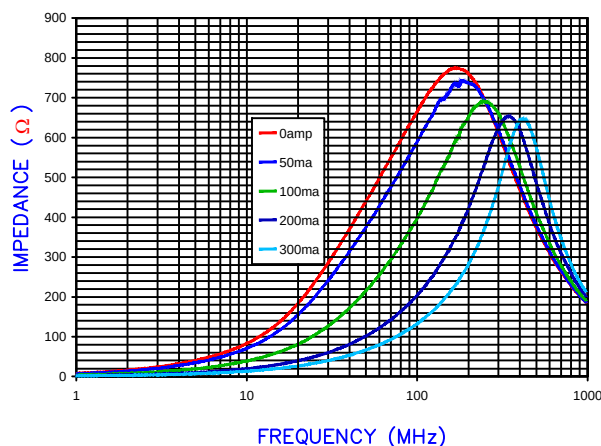
HZ0603C601R-10

PHYSICAL DIMENSIONS:

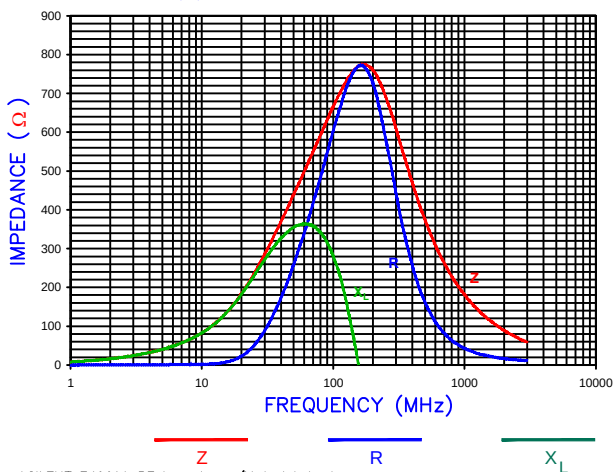
A	1.60 [.063]	+ 0.15	[.006]
B	0.80 [.031]	+ 0.15	[.006]
C	0.80 [.031]	+ 0.15	[.006]
D	0.36 [.014]	+ 0.15	[.006]



Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS

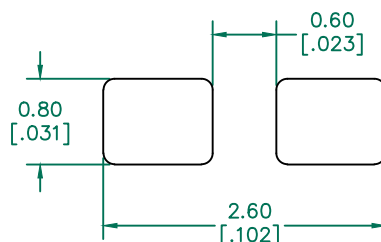


|Z|, R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3228

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762
(.030) to this dimension)

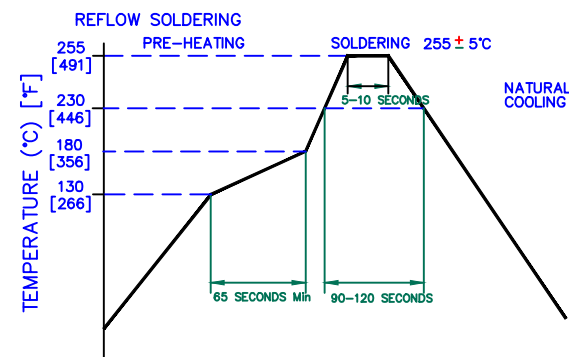
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	600	
Minimum	450	
Maximum	750	0.45
		300 mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS



RoHS

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	HZ0603C601R-10		
C	CHANGE SOLDERING TEMPERATURE FROM 250	07/12/10	JUN	REV	D	PART TYPE:	CO-FIRE
B	UPDATE COMPANY LOGO ADD ROHS	02/27/08	JRK	DATE:	03/26/04	SCALE:	NTS
A	ORIGINAL DRAFT	03/26/04	TMB	CAD #		SHEET:	1 of 1
REV	DESCRIPTION	DATE	INT	HZ0603C601R-10-D			

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