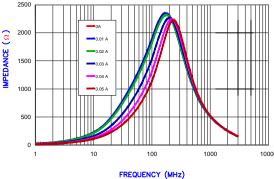
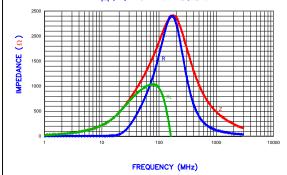
# HZ0603A182R-10

#### PHYSICAL DIMENSIONS:

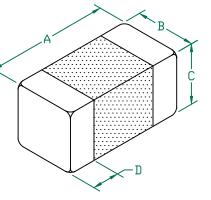
Z vs. FREQUENCY
IMPEDANCE UNDER DC BIAS



|Z| , R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyz HP 18194A Test Fixture. TEST REF. 5544



ELECTRICAL CHARACTERISTICS:									
Z @ 100MHz $\left(\begin{array}{c}\Omega\end{array}\right)$		DCR (Ω)	Rated Current						
Nominal	1800								
Minimum	1350								
Maximum	2250	1.5	50 mA						

NOTES: UNLESS OTHERWISE SPECIFIED

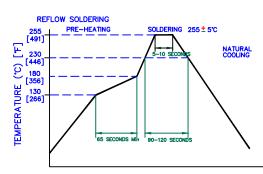
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

#### LAND PATTERNS FOR REFLOW SOLDERING

## 0.60 0.80 0.0313 2.60 0.1023

(For wave soldering, add 0.762 [.030] to this dimension)

### RECOMMENDED SOLDERING CONDITIONS



	DIMENSIONS ARE IN mm [INCHE	<u>:s].</u>		This print is the property of Laird				
				Tech. and is loaned in confidence subject to return upon request on with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are	L	.a	ir(	
			<u> </u>	reserved.				
D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	REV	PART TYPE: CO-FIRE		DRAWN BY: JRK
	UPDATE COMPANY LOGO	02/22/08	JRK	HZ0603A182R-10	ן יי	00	FIRE	UKK
С						•	SHEET:	•
В	ADD DC BIAS CURVE	07/25/06	JRK	DATE: 03/30/06 SC	ALE:	_	OFFEE 11	
_	ADD DC BIAS CURVE	07/25/06 03/30/06	JRK JRK	DATE: 03/30/06 SC	ALE:	-		of 1

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