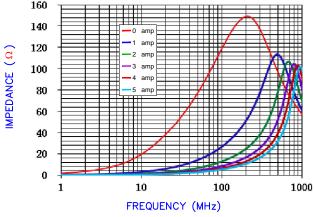
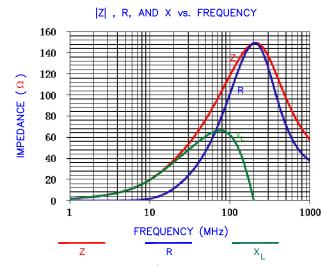
HI0805R121R-10

PHYSICAL DIMENSIONS:

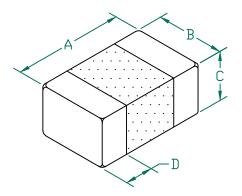
D 0.51 [.020] ± 0.25 [.010]

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS





AGILENT E4991A RF Impedance/Material Analyzer AGILENT 16194A.

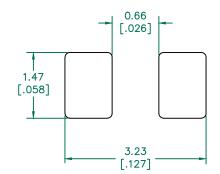


ELEC [*]	ELECTRICAL CHARACTERISTICS:										
Z @ 100N (Ω)		DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current								
Nominal	120										
Minimum	90										
Maximum	150	0.03	5000 mA								

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPER.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. TERMINATION FINISH IS 100% TIN.
- 4. OPERATEING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

REFLOW SOLDERING 255 PRE—HEATING 230 [446] 230 [446] 230 [356] 180 [356] 180 [266] 85 SECONDS Min 90–120 SECONDS

RECOMMENDED SOLDERING CONDITIONS

	DIMENSIONS ARE IN mm [INCHE	:S].		This print is the property of Lair Tech. and is loaned in confidenc subject to return upon request c with the understanding that no copies shall be made without the written consent of Laird Tech. Al rights to design or invention are reserved.	L	Laird			
				PROJECT/PART NUMBER:	- 1	REV	PART TY	-	DRAWN BY:
				HI0805R121R-10		Α	CO-FIRE		QU
			_	DATE: 5/30/13	CALE: NTS		TS	SHEET:	
Α	ORIGINAL DRAFT	5/30/13	QU		rooi	00L #			
REV	DESCRIPTION	DATE	INT	HI0805R121R-10-A		- "		1	of 1

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