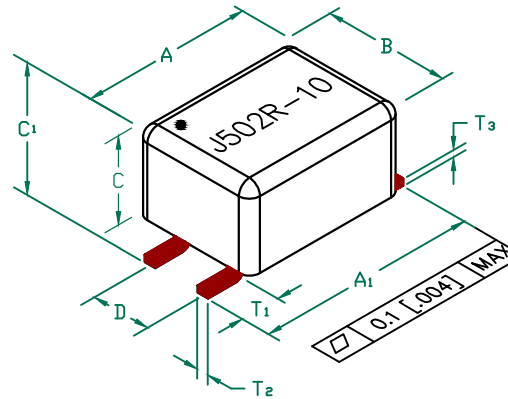


RoHS

CC2824J502R-10

ELECTRICAL SPECIFICATION @ 25°C
OPERATING TEMPERATURE RANGE: -40°C TO +125°C

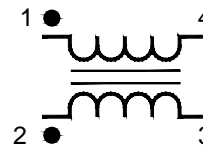
PARAMETER SYMBOL	TEST LEADS	CONDITIONS	REQUIREMENTS		
			MIN	TYP	MAX
Lp (μH)	1-4/2-3	@ 100KHz/ 10mV	3.5	5	6.5
L LEAKAGE (μH)	1-4 (s' 2-3)	@ 100KHz/ 10mV		0.05	
TRP			1 : 1 +/- 5%		
DCR(Ω)	1-4/2-3			0.10	0.13
Hi-pot	wdg-wdg	(Vac) .5mA, 2sec.	250		
RATED CURRENT (mA)					1200
RATED VOLTAGE (Vdc)					80



PHYSICAL DIMENSIONS:

A	7.50 [0.295]	+	MAX.
A ₁	9.00 [0.354]	+	0.40 [0.016]
B	5.50 [0.217]	+	MAX.
C	3.80 [0.150]	+	MAX.
C ₁	4.90 [0.193]	+	MAX.
D	2.54 [0.100]	+	0.10 [0.004]
T ₁	1.75 [0.069]	+	TYP.
T ₂	0.50 [0.020]	+	0.10 [0.004]
T ₃	0.30 [0.012]	+	0.10 [0.004]

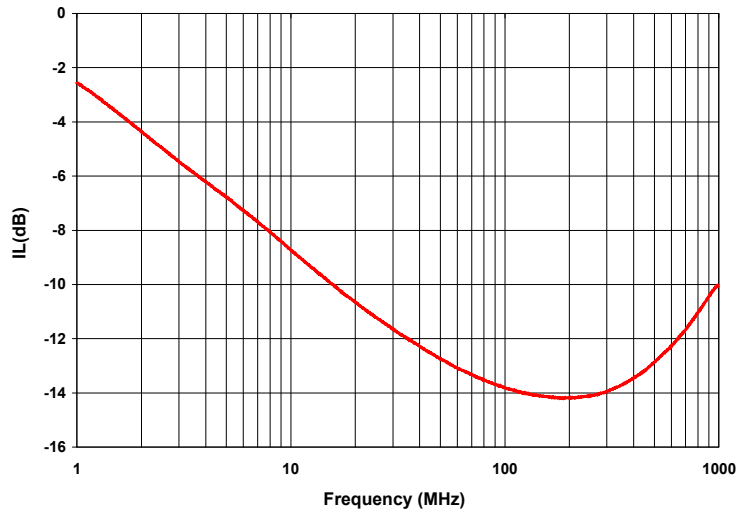
WEIGHT/1000 0.28 kgs [0.62 Lbs]



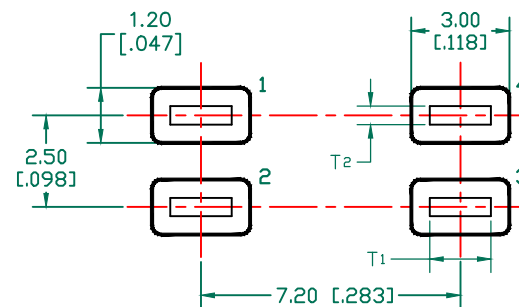
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED ON 13" REELS, 1500 PCS/REEL.
2. OPEN BOTTOM CONSTRUCTION.

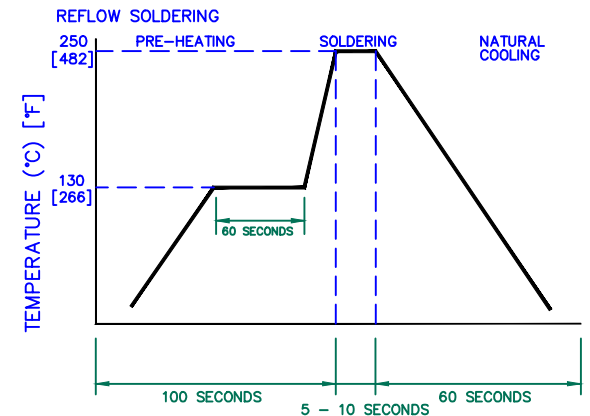
Typical Insertion Loss @ 50 ohm



LAND PATTERNS FOR REFLOW SOLDERING



RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
F	CHANGE MARKING	07/18/16	QIU	PROJECT/PART NUMBER: CC2824J502R-10			
E	ADD RATING VOLTAGE	07/25/13	QIU				
D	CHANGE THE INDUCTANCE TEST VOLTAGE	03/11/10	JUN				
C	UPDATE COMPANY LOGO	04/24/09	JRK				
B	UPDATE COMPANY LOGO	05/10/07	JRK				
A	ORIGINAL DRAFT	06/09/05	JRK	DATE:	06/09/05	SCALE:	NTS
REV	DESCRIPTION	DATE	INT	CAD #	CC2824J502R-10-F	TOOL #	-
				SHEET:		1 of 1	

Laird

REV F PART TYPE: ASSEMBLY DRAWN BY: JRK

Mouser Electronics

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