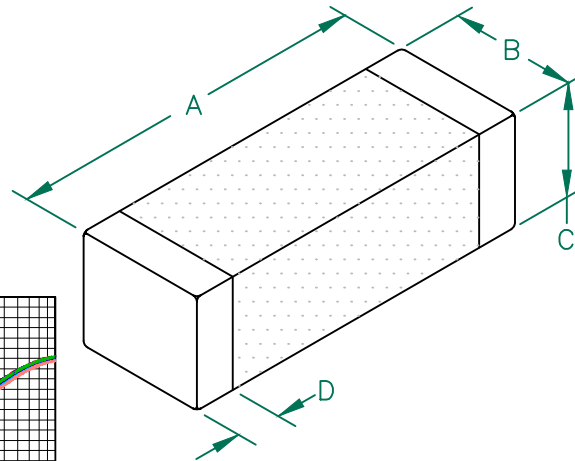


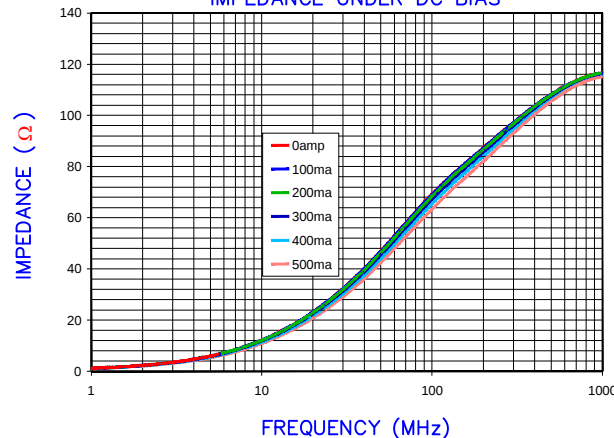
LI1806E800R-10

PHYSICAL DIMENSIONS:

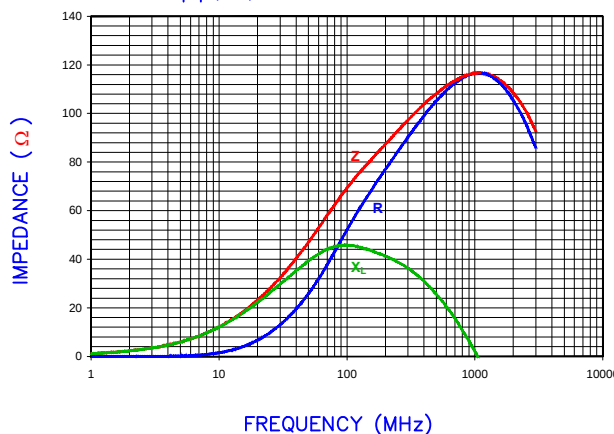
A	4.50 [.177]	+ 0.25 [.010]
B	1.60 [.063]	+ 0.25 [.010]
C	1.60 [.063]	+ 0.25 [.010]
D	0.51 [.020]	+ 0.25 [.010]



Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



|Z|, R, AND X vs. FREQUENCY



Z R X_L

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3298

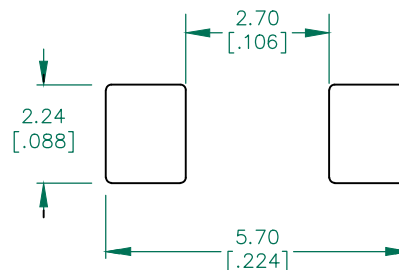
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	80	
Minimum	60	
Maximum	100	0.30 500 mA

NOTES: UNLESS OTHERWISE SPECIFIED

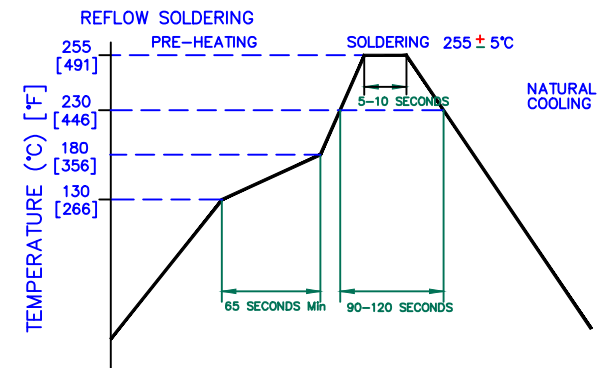
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2,000 PCS/REEL.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES]				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
F	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU				
E	CHANGE REEL QTY FROM 3K TO 2K	11/05/08	JRK				
D	UPDATE COMPANY LOGO	07/24/08	JRK				
C	CORRECT REF # FROM 3289 TO 3298	07/22/05	JRK				
B	CORRECT LANDPATTERN DIM AND TEMP IN REFLOW SOLDERING, ADD DC CURVE	04/14/03	JRK				
A	ORIGINAL DRAFT	08/07/02	JRK	LI1806E800R-10			
REV	DESCRIPTION	DATE	INT	DATE: 08/07/02 SCALE: NTS SHEET: 1 of 1 CAD # LI1806E800R-10-F TOOL # -			

Mouser Electronics

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