



Model 654P/L

Advanced PLL LVPECL or LVDS Clock

Features

- Ceramic Surface Mount Package
- Low Phase Jitter Performance, 500fs Typical
- Advanced PLL Design w/ Low Fundamental Crystal
- Frequency Range 10MHz – 800MHz *
- +2.5V or +3.3V Operation
- Output Enable Standard, Pin 2 Option Available
- Tape and Reel Packaging, EIA-418

Applications

- Broadcast Video Systems
- Storage Area Networking
- Broadband Access
- PCI Express
- Networking Equipment
- Ethernet/GbE/SyncE
- Fiber Channel
- Test and Measurement

Description

CTS Model 654P/L is a low cost, high performance PLL clock oscillator supporting differential LVPECL or LVDS outputs. Employing the latest IC technology, M654P/L has excellent stability and low phase jitter performance.

Ordering Information

Model	Output Type	Frequency Code [MHz]	Frequency Stability	Temperature Range	Supply Voltage	Packaging																																								
654	P	XXX or XXXX	3	I	3	T																																								
↓ <table><tr><th>Code</th><th>Output</th></tr><tr><td>P</td><td>LVPECL - Pin 1 Enable</td></tr><tr><td>L</td><td>LVDS - Pin 1 Enable</td></tr><tr><td>E</td><td>LVPECL - Pin 2 Enable</td></tr><tr><td>V</td><td>LVDS - Pin 2 Enable</td></tr></table>		Code	Output	P	LVPECL - Pin 1 Enable	L	LVDS - Pin 1 Enable	E	LVPECL - Pin 2 Enable	V	LVDS - Pin 2 Enable	↓ <table><tr><th>Code</th><th>Frequency</th></tr><tr><td colspan="2">Product Frequency Code ¹</td></tr></table>	Code	Frequency	Product Frequency Code ¹		↓ <table><tr><th>Code</th><th>Stability</th></tr><tr><td>6</td><td>±20ppm ²</td></tr><tr><td>5</td><td>±25ppm ³</td></tr><tr><td>4</td><td>±30ppm</td></tr><tr><td>3</td><td>±50ppm</td></tr></table>	Code	Stability	6	±20ppm ²	5	±25ppm ³	4	±30ppm	3	±50ppm	↓ <table><tr><th>Code</th><th>Temp. Range</th></tr><tr><td>C</td><td>-20°C to +70°C</td></tr><tr><td>I</td><td>-40°C to +85°C</td></tr></table>	Code	Temp. Range	C	-20°C to +70°C	I	-40°C to +85°C	↓ <table><tr><th>Code</th><th>Voltage</th></tr><tr><td>2</td><td>+2.5Vdc</td></tr><tr><td>3</td><td>+3.3Vdc</td></tr></table>	Code	Voltage	2	+2.5Vdc	3	+3.3Vdc	↓ <table><tr><th>Code</th><th>Packing</th></tr><tr><td>T</td><td>1k pcs./reel</td></tr></table>	Code	Packing	T	1k pcs./reel
Code	Output																																													
P	LVPECL - Pin 1 Enable																																													
L	LVDS - Pin 1 Enable																																													
E	LVPECL - Pin 2 Enable																																													
V	LVDS - Pin 2 Enable																																													
Code	Frequency																																													
Product Frequency Code ¹																																														
Code	Stability																																													
6	±20ppm ²																																													
5	±25ppm ³																																													
4	±30ppm																																													
3	±50ppm																																													
Code	Temp. Range																																													
C	-20°C to +70°C																																													
I	-40°C to +85°C																																													
Code	Voltage																																													
2	+2.5Vdc																																													
3	+3.3Vdc																																													
Code	Packing																																													
T	1k pcs./reel																																													

Notes:

- 1] Refer to document 016-1454-0, Frequency Code Tables.
3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.
- 2] 6I Stability/Temperature combination not available.
- 3] Check factory availability when paired with 'I' temperature code.

Not all performance combinations and frequencies may be available.
Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.



Electrical Specifications

Operating Conditions

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Maximum Supply Voltage	V_{CC}	-	-0.5	-	4.0	V
Supply Voltage	V_{CC}	$\pm 5\%$	2.375 3.135	2.5 3.3	2.625 3.465	V
Supply Current						
LVPECL	I_{CC}	Maximum Load Maximum Current Value @ +3.3V	-	54	88	mA
LVDS			-	23	65	
Operating Temperature	T_A	-	-20 -40	+25	+70 +85	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-	-55	-	+125	$^{\circ}\text{C}$

Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Frequency Range	f_O	-		10 - 800		MHz
Frequency Stability [Note 1]	$\Delta f/f_O$	-		20, 25 or 50		$\pm\text{ppm}$
Aging	$\Delta f/f_{25}$	First Year @ +25 $^{\circ}\text{C}$, nominal V_{CC}	-3	-	3	ppm

1.] Inclusive of initial tolerance at time of shipment, changes in supply voltage, load, temperature and 1st year aging.

Output Parameters

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Output Type	-	-		LVPECL		-
Output Load	R_L	Terminated to $V_{CC} - 2.0\text{V}$	-	50	-	Ohms
Output Voltage Levels	V_{OH} V_{OL}	PECL Load	$V_{CC} - 1.03$ $V_{CC} - 1.85$	- -	$V_{CC} - 0.60$ $V_{CC} - 1.60$	V
Output Duty Cycle	SYM	@ $V_{CC} - 1.3\text{V}$	45	-	55	%
Rise and Fall Time	T_R, T_F	@ 20%/80% Levels, $R_L = 50\text{ Ohms}$	-	0.25	0.60	ns
Output Type	-	-		LVDS		-
Output Load	R_L	Between Outputs	-	100	-	Ohms
Output Voltage Levels	V_{OH} V_{OL}	LVDS Load	- 0.90	1.43 1.10	1.60 -	V
Output Duty Cycle	SYM	@ 1.25V	45	-	55	%
Differential Output Voltage	V_{OD}	$R_L = 100\text{ Ohms}$	175	330	454	mV
Offset Voltage	V_{OS}	LVDS Load	1.20	1.25	1.30	V
Rise and Fall Time	T_R, T_F	@ 20%/80% Levels, $R_L = 100\text{ Ohms}$	-	-	0.5	ns

Electrical Specifications

Output Parameters

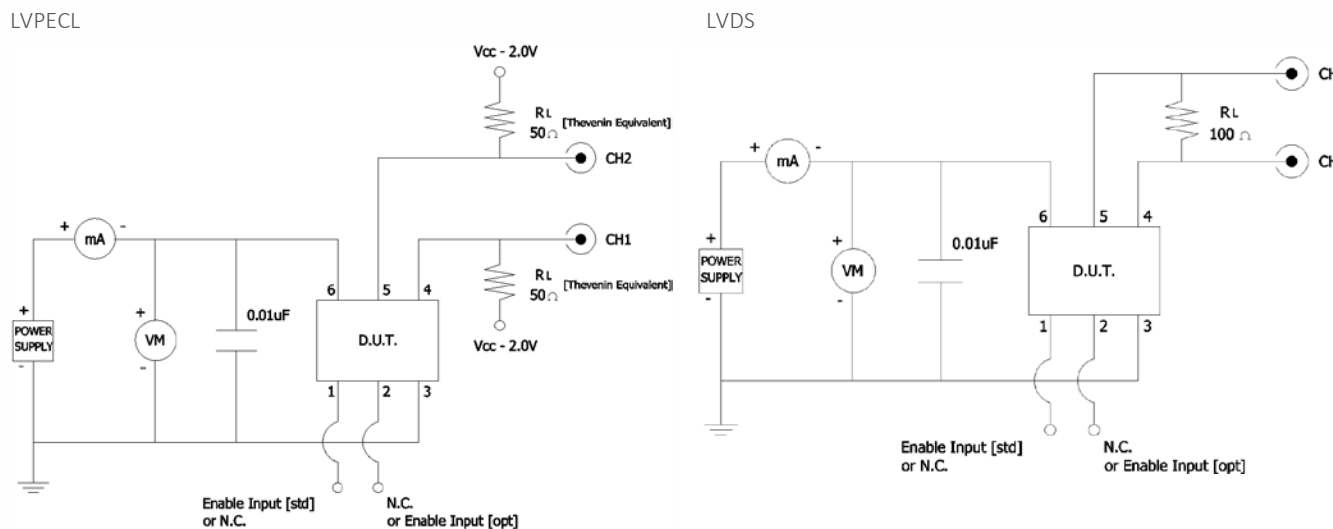
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Start Up Time	T _S	Application of V _{CC}	-	3	5	ms
Enable Function [Tri-State]						
Enable Input Voltage	V _{IH}	Pin 1 or 2 Logic '1', Output Enabled	0.7V _{CC}	-	-	V
Disable Input Voltage	V _{IL}	Pin 1 or 2 Logic '0', Output Disabled	-	-	0.3V _{CC}	V
Disable Current	I _{IL}	Pin 1 or 2 Logic '0', Output Disabled	-	16	22	mA
Enable Time	T _{PLZ}	Pin 1 or 2 Logic '1', Output Enabled	-	-	200	ns
Phase Jitter, RMS	tjrms	Bandwidth 12 kHz - 20 MHz	-	500	<1000	fs
Period Jitter, RMS	pjrms	-	-	2.5	-	ps
Period Jitter, pk-pk	pjpk-pk	-	-	25	-	ps

Enable Truth Table

Pin 1 or Pin 2	Pin 4 & Pin 5
Logic '1'	Output
Open	Output
Logic '0'	High Imp.

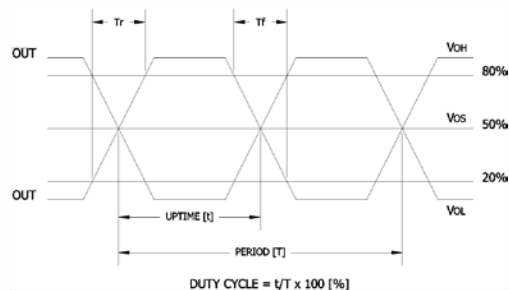
Test Circuit

LVPECL



Output Waveform

LVPECL or LVDS

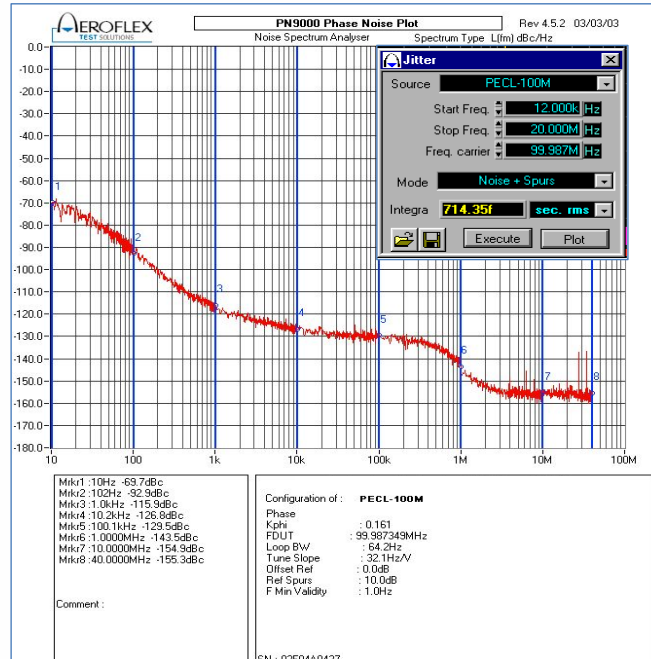


Electrical Specifications

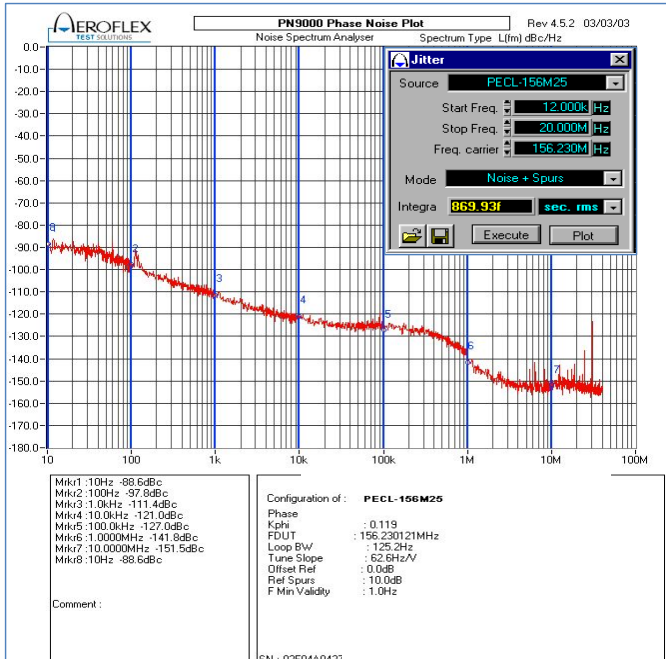
Performance Data

Phase Noise [typical]

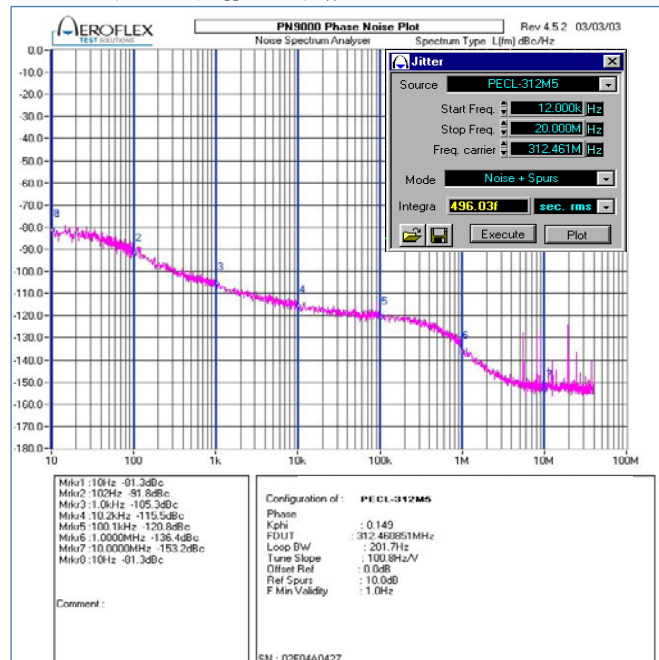
100.00MHz, LVPECL, $V_{CC} = 3.3V$, $T_A = +25^\circ C$



156.25MHz, LVPECL, $V_{CC} = 3.3V$, $T_A = +25^\circ C$



312.50MHz, LVPECL, $V_{CC} = 3.3V$, $T_A = +25^\circ C$





Performance Data

Phase Noise Tabulated

Typical, HCMOS, $V_{CC} = 3.3V$, $T_A = +25^{\circ}C$

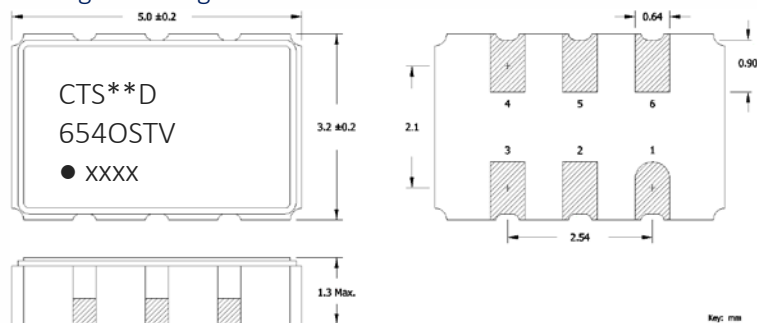
PARAMETER	SYMBOL	CONDITIONS	TYP	UNIT
LVPECL @ 100.00MHz				
Phase Noise	Single Side Band			
	@ 10Hz		-69.70	
	@ 100Hz		-92.90	
	@ 1kHz		-115.90	
	@ 10kHz	-	-126.80	dBc/Hz
	@ 100kHz		-129.50	
	@ 1MHz		-143.50	
	@ 10MHz		-154.90	
	@ 40MHz		-155.30	
Phase Jitter, RMS	tjrms	Integration Bandwidth 12kHz - 20MHz	714.35	fs

PARAMETER	SYMBOL	CONDITIONS	TYP	UNIT
LVPECL @ 312.50MHz				
Phase Noise	Single Side Band			
	@ 10Hz		-81.30	
	@ 100Hz		-91.80	
	@ 1kHz		-105.30	
	@ 10kHz	-	-115.50	dBc/Hz
	@ 100kHz		-120.80	
	@ 1MHz		-136.40	
	@ 10MHz		-153.20	
	@ 40MHz		-153.20	
Phase Jitter, RMS	tjrms	Integration Bandwidth 12kHz - 20MHz	496.03	fs

PARAMETER	SYMBOL	CONDITIONS	TYP	UNIT
LVPECL @ 156.25MHz				
Phase Noise	Single Side Band			
	@ 10Hz		-88.60	
	@ 100Hz		-97.80	
	@ 1kHz		-111.40	
	@ 10kHz	-	-121.00	dBc/Hz
	@ 100kHz		-127.00	
	@ 1MHz		-141.80	
	@ 10MHz		-151.50	
	@ 40MHz		-153.30	
Phase Jitter, RMS	tjrms	Integration Bandwidth 12kHz - 20MHz	869.93	fs

Mechanical Specifications

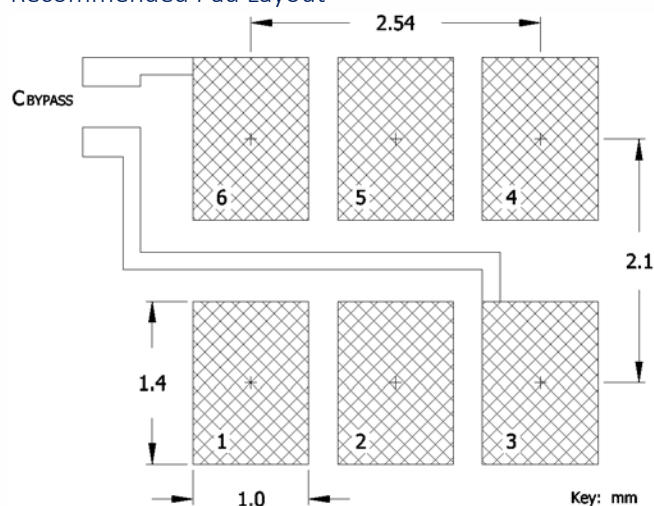
Package Drawing



Marking Information

1. ** - Manufacturing Site Code.
2. D - Date Code. See Table I for codes.
3. O - Output Type; P = LVPECL, L = LVDS.
[Refer to Ordering Information]
4. V - Voltage Code; 3 = 3.3V, 2 = 2.5V.
5. xxxx - Frequency Code.
3-digits, frequencies below 100MHz
4-digits, frequencies 100MHz or greater
[See document 016-1454-0, Frequency Code Tables.]

Recommended Pad Layout



Notes

1. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
2. Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
3. MSL = 1.

Pin Assignments

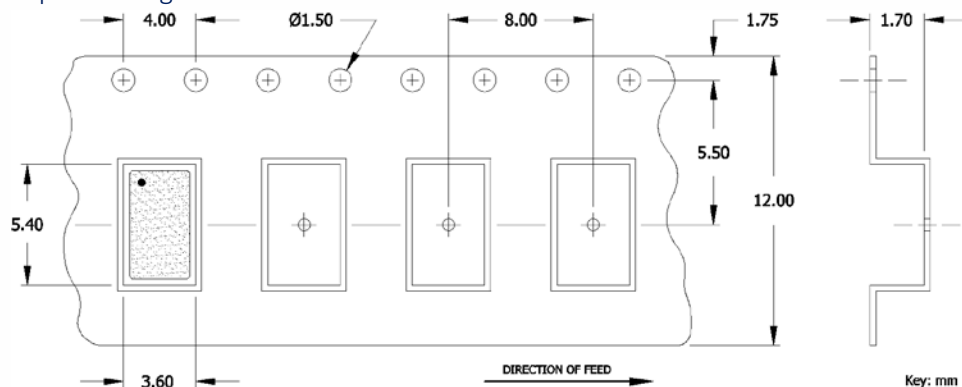
Pin	Symbol	Function
1	EOH or N.C.	Enable [std] or No Connect
2	N.C. or EOH	No Connect or Enable [opt]
3	GND	Circuit & Package Ground
4	Output	RF Output
5	Output	Complimentary RF Output
6	V _{CC}	Supply Voltage

Table I - Date Code

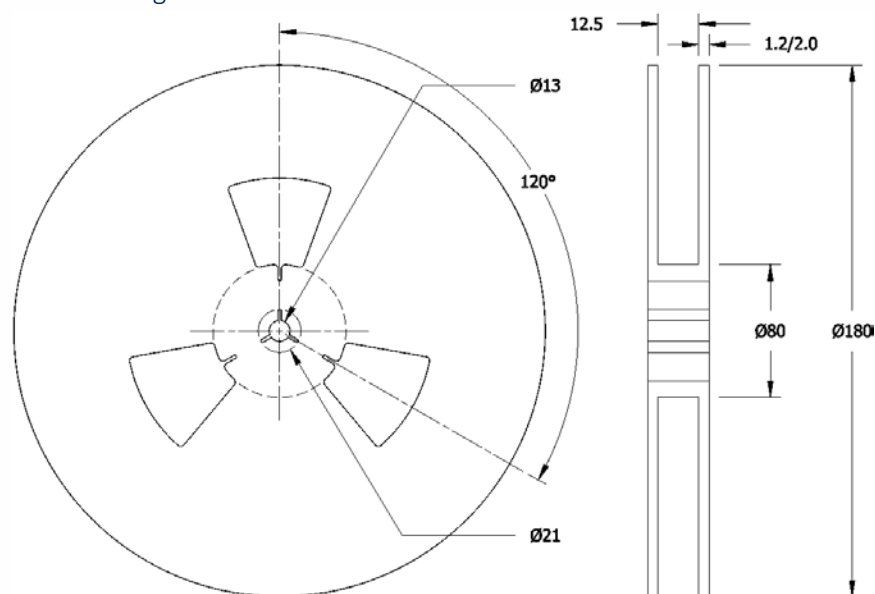
YEAR					MONTH											
2001	2002	2003	2004	2005	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
2006	2007	2008	2009	2010	A	B	C	D	E	F	G	H	J	K	L	M
2011	2012	2013	2014	2015	N	P	Q	R	S	T	U	V	W	X	Y	Z
2016	2017	2018	2019	2020	a	b	c	d	e	f	g	h	j	k	l	m
					n	p	q	r	s	t	u	v	w	x	y	z

Packaging - Tape and Reel

Tape Drawing



Reel Drawing



Notes

1. Device quantity is 1k pieces per 180mm reel.
2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.



Addendum

Additional Developed Frequencies – MHz

FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE
38.840000	38D	77.760000	777	135.000000	1350	184.320000	1843
38.880000	388	86.700000	867	144.500000	1445	204.800000	2048
43.350000	433	87.351542	873	148.351600	148A	233.000000	2330
45.000000	450	92.160000	921	148.351648	148B	245.760000	2457
54.000000	540	96.000000	960	150.000000	1500	300.000000	3000
62.500000	625	104.000000	1040	156.253906	156A	349.400000	3494
74.175800	74A	122.880000	1228	160.000000	1600		
76.800000	768	132.000000	1320	161.132800	1611		

Frequency Codes for Cover Page Table – MHz

FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE
25.000000	250	106.250000	1062	156.250000	1562	400.000000	4000
27.000000	270	125.000000	1250	200.000000	2000		
50.000000	500	148.500000	1485	250.000000	2500		
74.250000	742	153.600000	1536	312.500000	3125		
100.000000	1000	155.520000	1555	322.265625	322A		

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

CTS:

[654P148B3I2T](#) [654P148B4C3T](#) [654P15005C3T](#) [654P15363C2T](#) [654P18433C3T](#) [654P20005C2T](#) [654P23304C3T](#)
[654P23304I2T](#) [654P25003I3T](#) [654P30003C3T](#) [654P30003I2T](#) [654P31255C2T](#) [654P31255C3T](#) [654P38D3C3T](#)
[654P40005C2T](#) [654P40005C3T](#) [654P5403I2T](#) [654P6254I3T](#) [654P74A4C3T](#) [654P7773I2T](#) [654P8675C2T](#)
[654P9214I2T](#) [654V10003I2T](#) [654V10003I3T](#) [654P8734I3T](#) [654P9603I3T](#) [654P9604C2T](#) [654V10403C3T](#)
[654P25005C3T](#) [654P30005C3T](#) [654P31253C2T](#) [654P3883C3T](#) [654P3883I3T](#) [654P3884C2T](#) [654P4334I3T](#)
[654P5003C3T](#) [654P5405C2T](#) [654P6253C3T](#) [654P7424I2T](#) [654P7425C2T](#) [654P7683C3T](#) [654P8673C3T](#)
[654P8734I2T](#) [654P8735C2T](#) [654P9603I2T](#) [654P9604C3T](#) [654V10005C2T](#) [654V10004C3T](#) [654V10404C2T](#)
[654P156A5C2T](#) [654P156A5C3T](#) [654P16003C2T](#) [654P18433I2T](#) [654P18433I3T](#) [654P18434C2T](#) [654P20005C3T](#)
[654P20483C2T](#) [654P23304C2T](#) [654P30003I3T](#) [654P31254I2T](#) [654P34944C2T](#) [654P38D3C2T](#) [654P38D3I2T](#)
[654P40004I2T](#) [654P40004I3T](#) [654P4503I3T](#) [654P4504C2T](#) [654P4504I2T](#) [654P5403I3T](#) [654P7773I3T](#)
[654P9214C2T](#) [654V10003C2T](#) [654V10003C3T](#) [654P10625C2T](#) [654P13203C2T](#) [654P13204I3T](#) [654P13205C3T](#)
[654P13504I2T](#) [654P14855C3T](#) [654P148A3C3T](#) [654P15623C2T](#) [654P156A4C2T](#) [654P16003I3T](#) [654P16004C3T](#)
[654P18434C3T](#) [654P20483C3T](#) [654P23303C2T](#) [654P23305C3T](#) [654P24573C3T](#) [654P24575C2T](#) [654P322A3C3T](#)
[654P38D4C3T](#) [654P40003I2T](#) [654P4333C3T](#) [654P4504I3T](#) [654P5004I2T](#) [654P7423C3T](#) [654P7423I3T](#)
[654P9214I3T](#) [654P9215C3T](#)