

Crystek's Model CVHD-957 HCMOS VCXO family has been designed specifically for High Definition Audio (HD Audio). It features a typical low close-in phase noise of -90 dBc/Hz @ 10 Hz offset, and a noise floor of -168 dBc/Hz. With this extreme low phase noise performance, you will "Hear the Difference". It also features a "Standby Function", that is, when placed in disable mode, the internal oscillator is completely shut down in addition to its output buffer being placed in Tri-State. This family is housed in a 9×14 mm SMT package and operates with a +3.3V power supply.

Applications include:

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CORPORA

Digital Audio Broadcasting (DAB) Professional CD audio equipment DACs and ADCs for HD audio

Rev: L
Date: 15-Jan-2019
Page 1 of 2





CVHD-957 Model 9×14 mm SMD, 3.3V, HCMOS

CVHD-957 Ultra-Low Phase Noise VCXO with Standby Mode

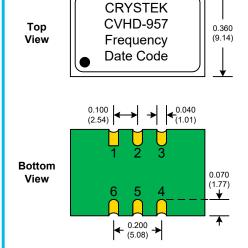


Frequency Range: 10 MHz to 50 MHz Mechanical: **Temperature Range:** 0°C to +70°C Shock[.] MIL-STD-883. Method 2002. Condition B (Option M) -20°C to +70°C Solderability: MIL-STD-883, Method 2003 (Option X) -40°C to +85°C Vibration: MIL-STD-883, Method 2007, Condition A -45°C to 90°C Storage: MIL-STD-202, Method 215 Solvent Resistance: MIL-STD-202, Method 210, Condition I or J Resistance to Soldering Heat: **Input Voltage:** 3.3V ±5% **Input Current:** 15mA Typical, 25mA Max Environmental: Input Current (Disabled Mode): 1.5mA Max Thermal Shock: MIL-STD-883, Method 1011, Condition A Input: Modulation Bandwidth: >10 kHz @ -3 dB Moisture Resistance: MIL-STD-883, Method 1004 50 kOhm **Impedance: Control Voltage:** 1.65V ±1.65V Developed **Tuning Sensitivity:** +85 ppm/V Typical Frequencies **Frequency Pulling:** ±100ppm Min, ±75ppm Min for 10 MHz variant **Output: HCMOS** 22.5792 MHz 40/60% Max @ 50%Vcc Symmetry: 24.576 MHz **Rise/Fall Time:** 3ns Max @ 20% to 80% Vcc 45.1584 MHz "0" = 10% Vcc Max Logic: 49.152 MHz "1" = 90% Vcc Min Load: 15pF CRYSTEK CORPORATION **Output Current:** ±24mA Max **Disable Time:** 200ns Max **Start-up Time:** 1ms Typical, 2ms Max **Pin 1 Disable Current:** -350µA Max **Phase Noise:** -90 dBc/Hz at 10 Hz Typical for 22.5792 MHz and 24.576 MHz -80 dBc/Hz at 10 Hz Typical for 45.1584 MHz and 49.152 MHz **Phase Noise Floor:** -168 dBc/Hz Typical, -165 dBc/Hz Max **Sub-harmonics:** None

<3ppm 1st year, <1ppm thereafter

RECOMMENDED REFLOW SOLDERING PROFILE 900034 (See App Note listed on website)

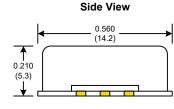
http://www.crystek.com/specification/reflow/900034.pdf



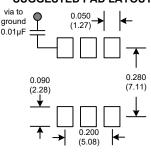
0.560

(14.2)

Aging:

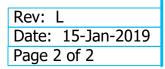


SUGGESTED PAD LAYOUT



Tri-State/Standby Function	
Function pin 2	Output pin
Open	Active
"1" level 0.7×Vcc Min	Active
"0" level 0.3×Vcc Max	High Z

PIN	Function
1 2 3 4 5	Control Volt E/D GND OUT NC
6	Vcc



PAD FINISH: Immersion Gold (ENIG); 5 micro inches maximum

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Mouser Electronics

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Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Crystek: <u>CVHD-957-24.576</u>