

CSO-018T Model

5x7 mm SMD, 3.3V, HCMOS

Frequency Range: 1.544 MHz to 125 MHz

Frequency Stability: $\pm 25\text{ppm}$ to $\pm 100\text{ppm}$

Temperature Range:

Operating: 0°C to 70°C

(Option M) -20°C to 70°C

(Option X) -40°C to 85°C

Storage: -45°C to 120°C

Input Voltage: $3.3\text{V} \pm 0.3\text{V}$

Input Current: 45mA Max

Standby Current: 10uA Max

Output: HCMOS

Symmetry: 40/60% Max @ 50% Vdd

(Option Y) 45/55% Max @ 50% Vdd

Rise/Fall Time: 10ns Max @ 20% to 80% Vdd

Logic: "0" = 10% Vdd Max

"1" = 90% Vdd Min

Load: 15pF Max

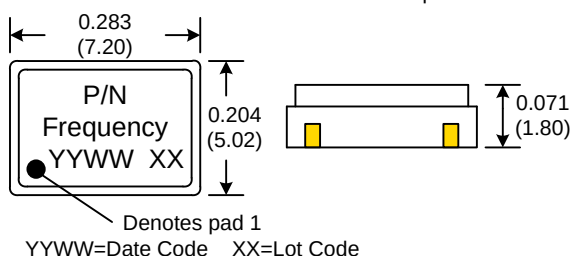
Aging: $< 3\text{ppm}$ 1st year, 1ppm every year thereafter



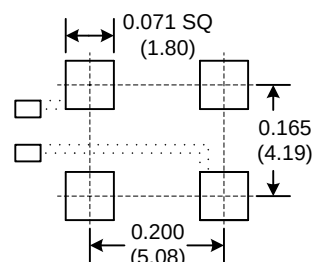
Designed to meet today's requirements for economical 3.3V applications. Available on 16mm tape and reel in quantities of 1K.

Dimensions inches (mm)

All dimensions are Max unless otherwise specified.

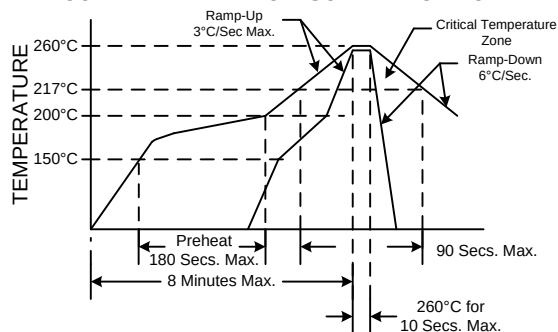


SUGGESTED PAD LAYOUT



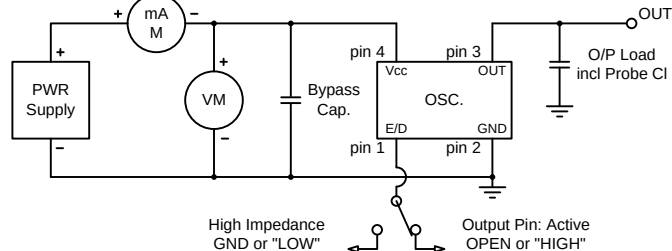
0.01uF Bypass Capacitor Recommended

RECOMMENDED REFLOW SOLDERING PROFILE



NOTE: Reflow Profile with 240°C peak also acceptable.

TEST CIRCUIT



Crystek Part Number Guide

CSO-018T X Y - 25 - 49.152

#1 #2 #3 #4 #5 #6

#1 Crystek Clock Osc.
#2 Model
#3 Temp. Range: Blank = $0/70^{\circ}\text{C}$, M = $-20/70^{\circ}\text{C}$, X = $-40/85^{\circ}\text{C}$
#4 Symmetry: Blank = (40/60), Y = (45/55)
#5 Stability: (see Table 1)
#6 Frequency in MHz: 3 or 6 decimal places

Stability Indicator

Blank (std) $\pm 100\text{ppm}$
50 $\pm 50\text{ppm}$
25 $\pm 25\text{ppm}$

Table 1

Example:
CSO-018TX-25-25.000 = 3.3V Tristate, $-40/85^{\circ}\text{C}$, 45/55, 25ppm, 25.000 MHz
CSO-018T-50-19.660800 = 3.3V Tristate, $0/70$, 40/60, 50ppm, 19.660800 MHz

Enable/Disable

Function pin 1	Output pin
Open	Active
"1" level 2.4V Min	Active
"0" level 0.4V Max	High Z

Rev: K

Date: 25-Feb-2016

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Specifications subject to change without notice.

Mouser Electronics

Authorized Distributor

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Crystek:

[CSO-018TXY-50-50.000](#) [CSO-018TXY-50-33.000](#) [CSO-018T-25-20.000](#) [CSO-018T-25-34.560](#) [CSO-018TX-25-2.048](#)
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