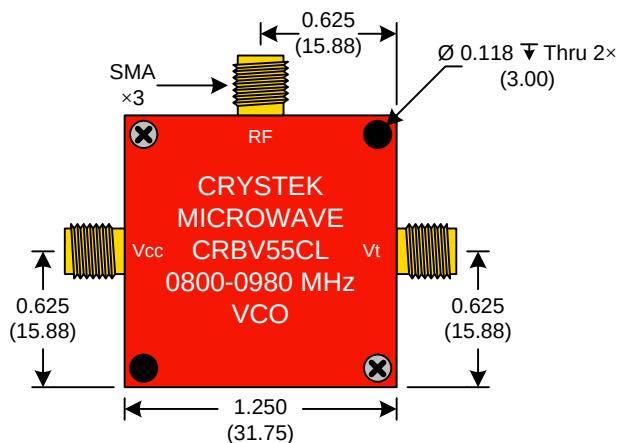
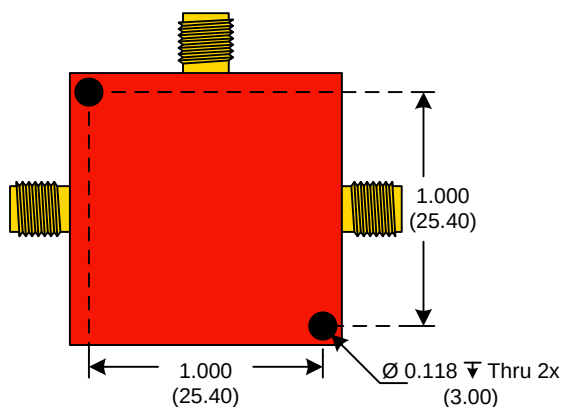


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			800	MHz
Upper Frequency:	980			MHz
Tuning Voltage:	0.5		5.0	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	0	+3.0	+6.0	dBm
Supply Current:			25	mA
Harmonic Suppression (2 nd Harmonic)		-22		dBc
Pushing:			2.0	MHz/V
Pulling, all Phases:			2.0	MHz pk-pk
Tuning Sensitivity:		75		MHz/V
Phase Noise @ 10kHz offset:		-99		dBc/Hz
Phase Noise @ 100kHz offset:		-121		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			47	pF
Operating Temperature Range:	-40		+85	°C
Storage Temperature Range:	-45		+90	°C

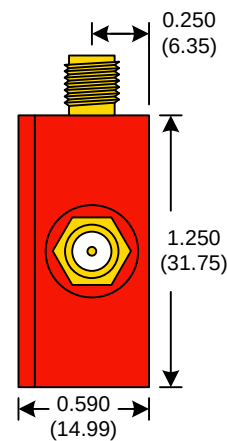
FRONT



BACK



SIDE

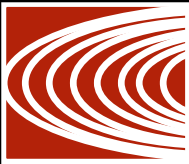


● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{(\text{mm})}$

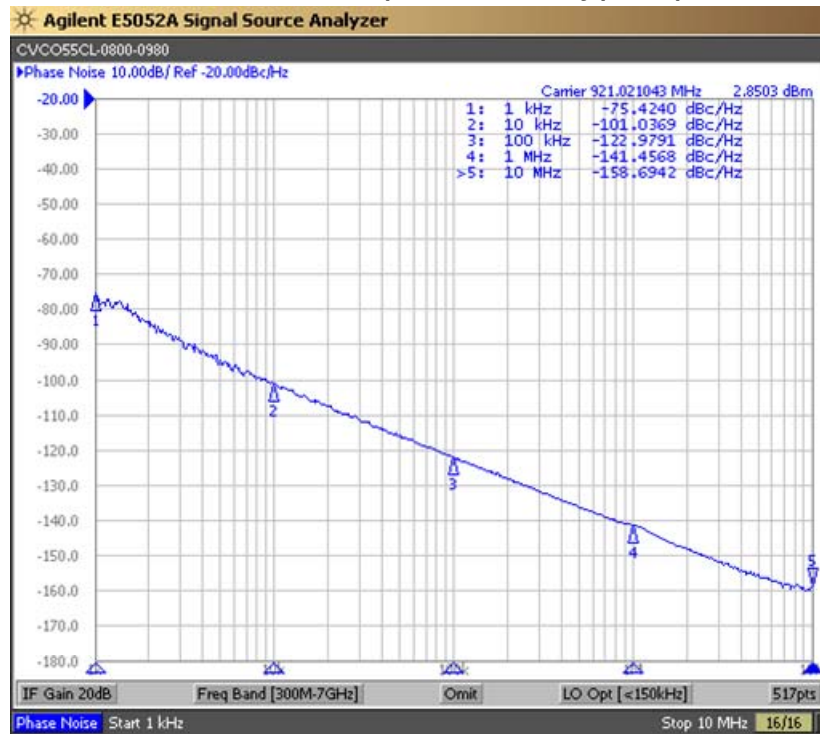
Product Control:

Crystek Part Number:	CRBV55CL-0800-0980	Release Date:	21-May-2015
Revision Level:	B	Responsible:	C. Vales

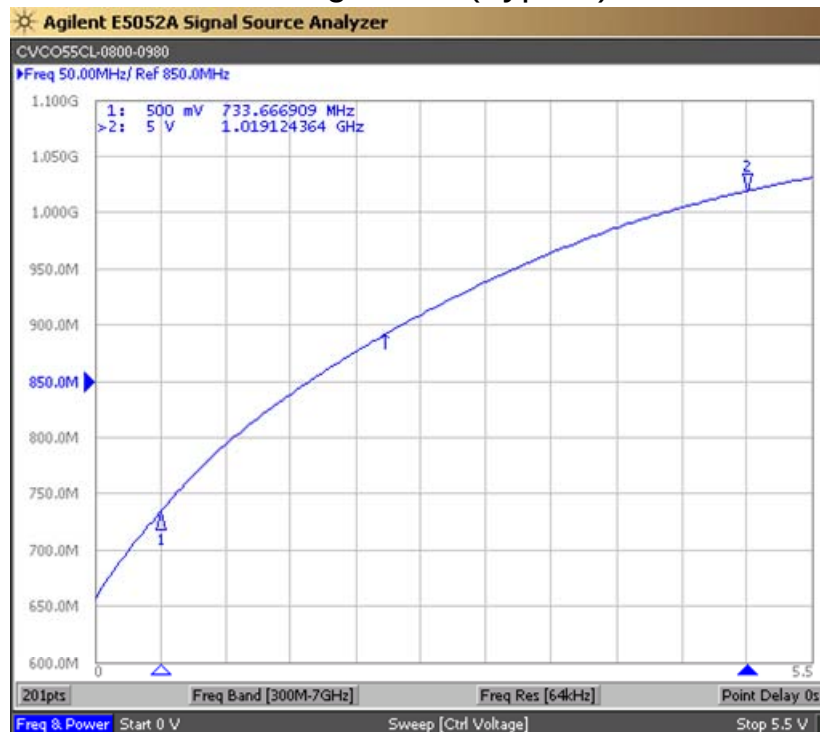




Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0800-0980	Release Date:	21-May-2015
Revision Level:	B	Responsible:	C. Vales

Specification is subject to change without notice



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