PHP20NQ20T

N-channel TrenchMOS standard level FET Rev. 02 — 16 December 2010

Product data sheet

Product profile 1.

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product is designed and qualified for use in computing, communications, consumer and industrial applications only.

1.2 Features and benefits

- Higher operating power due to low thermal resistance
- Low conduction losses due to low on-state resistance
- Suitable for high frequency applications due to fast switching characteristics

1.3 Applications

DC-to-DC converters

General purpose switching

1.4 Quick reference data

Table 1. Quick reference data

Parameter	Conditions	Min	Тур	Max	Unit
drain-source voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}$	-	-	200	V
drain current	$T_{mb} = 25 ^{\circ}C; V_{GS} = 10 V$	-	-	20	Α
total power dissipation	T _{mb} = 25 °C	-	-	150	W
acteristics					
drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 10 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	120	130	mΩ
naracteristics					
gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 20 \text{ A};$ $V_{DS} = 160 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	22	-	nC
	drain-source voltage drain current total power dissipation acteristics drain-source on-state resistance	drain-source voltage $T_j \ge 25 ^{\circ}\text{C}; T_j \le 175 ^{\circ}\text{C}$ drain current $T_{mb} = 25 ^{\circ}\text{C}; V_{GS} = 10 ^{\circ}\text{V}$ total power $T_{mb} = 25 ^{\circ}\text{C}$ dissipation acteristics drain-source $T_{mb} = 25 ^{\circ}\text{C}$	drain-source voltage $T_j \ge 25 ^{\circ}\text{C}$; $T_j \le 175 ^{\circ}\text{C}$ - drain current $T_{mb} = 25 ^{\circ}\text{C}$; $V_{GS} = 10 ^{\circ}\text{V}$ - total power $T_{mb} = 25 ^{\circ}\text{C}$ - dissipation acteristics drain-source $V_{GS} = 10 ^{\circ}\text{V}$; $V_{D} = 10 ^{\circ}\text{A}$; $V_{D} = 25 ^{\circ}\text{C}$ - on-state resistance paracteristics gate-drain charge $V_{GS} = 10 ^{\circ}\text{V}$; $V_{D} = 20 ^{\circ}\text{A}$; -	drain-source voltage $T_j \ge 25 ^{\circ}\text{C}$; $T_j \le 175 ^{\circ}\text{C}$ drain current $T_{mb} = 25 ^{\circ}\text{C}$; $V_{GS} = 10 ^{\circ}\text{C}$ total power dissipation $T_{mb} = 25 ^{\circ}\text{C}$ dissipation $T_{mb} = 25 ^{\circ}\text{C}$ dissipation $T_{mb} = 25 ^{\circ}\text{C}$ 120 on-state resistance $T_{mb} = 25 ^{\circ}\text{C}$ - 120 on-state resistance $T_{max} = 10 ^{\circ}\text{C}$ - 120 on-state resistance $T_{max} = 10 ^{\circ}\text{C}$ - 120 on-state resistance $T_{max} = 10 ^{\circ}\text{C}$ - 22	drain-source voltage $T_j \ge 25~^\circ\text{C}; T_j \le 175~^\circ\text{C}$ 200 drain current $T_{mb} = 25~^\circ\text{C}; V_{GS} = 10~\text{V}$ 20 total power $T_{mb} = 25~^\circ\text{C}$ 150 dissipation acteristics drain-source $V_{GS} = 10~\text{V}; I_D = 10~\text{A}; T_j = 25~^\circ\text{C}$ - 120 130 on-state resistance paracteristics gate-drain charge $V_{GS} = 10~\text{V}; I_D = 20~\text{A};$ - 22 -



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain	mb	D
3	S	source		$G \longrightarrow A$
mb	D	mounting base; connected to drain	1 2 3 SOT78 (TO-220AB)	mbb076 S

3. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PHP20NQ20T	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78				

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

	_				
Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	200	V
V_{DGR}	drain-gate voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$	-	200	V
V_{GS}	gate-source voltage		-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C	-	14	Α
		V _{GS} = 10 V; T _{mb} = 25 °C	-	20	Α
I _{DM}	peak drain current	pulsed; T _{mb} = 25 °C	-	80	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C	-	150	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
Source-drain	n diode				
Is	source current	T _{mb} = 25 °C	-	20	Α
I _{SM}	peak source current	pulsed; T _{mb} = 25 °C	-	80	Α
Avalanche r	uggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 19 A; V_{sup} ≤ 25 V; unclamped; t_p = 100 μ s; R_{GS} = 50 Ω	-	252	mJ
I _{AS}	non-repetitive avalanche current	$V_{sup} \le 25 \text{ V}$; $V_{GS} = 10 \text{ V}$; $T_{j(init)} = 25 ^{\circ}\text{C}$; $R_{GS} = 50 \Omega$; unclamped	-	20	Α

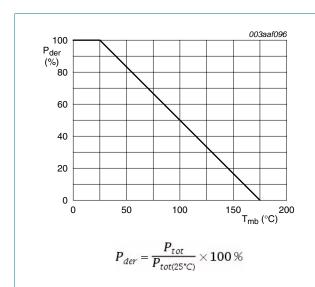


Fig 1. Normalized total power dissipation as a function of mounting base temperature

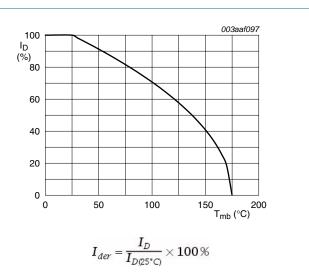


Fig 2. Normalized continuous drain current as a function of mounting base temperature

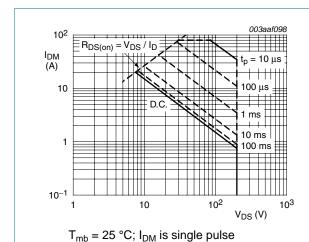


Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

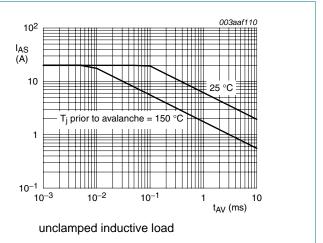


Fig 4. Single-shot avalanche rating; avalanche current as a function of avalanche period

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base		-	-	1	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W

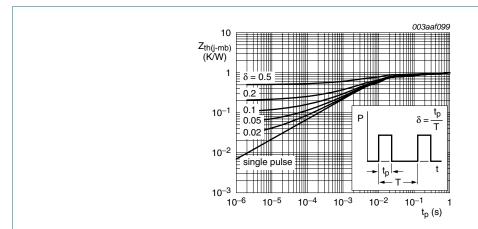


Fig 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Characteristics

Table 6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
V _{(BR)DSS}	drain-source breakdown	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	178	-	-	V
	voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	200	-	-	V
V _{GS(th)}	gate-source threshold	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$	1	-	-	V
	voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C}$	-	-	6	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	2	3	4	V
I _{DSS}	drain leakage current	$V_{DS} = 200 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.05	10	μΑ
		V _{DS} = 200 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μΑ
I _{GSS}	gate leakage current	$V_{GS} = 10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	0.02	100	nA
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 10 A; T _j = 175 °C	-	-	377	mΩ
	resistance	V _{GS} = 10 V; I _D = 10 A; T _j = 25 °C	-	120	130	mΩ
Dynamic o	characteristics	,				
Q _{G(tot)}	total gate charge	I _D = 20 A; V _{DS} = 160 V; V _{GS} = 10 V;	-	65	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	10	-	nC
Q_{GD}	gate-drain charge		-	22	-	nC
C _{iss}	input capacitance	$V_{DS} = 25 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	2470	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	207	-	pF
C _{rss}	reverse transfer capacitance		-	90	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 100 \text{ V}; R_L = 4.7 \Omega; V_{GS} = 10 \text{ V};$	-	15	-	ns
t _r	rise time	$R_{G(ext)} = 5.6 \Omega; T_j = 25 °C$	-	46	-	ns
t _{d(off)}	turn-off delay time		-	50	-	ns
t _f	fall time		-	38	-	ns
L _D	internal drain inductance	measured from tab to centre of die ; $T_j = 25 ^{\circ}\text{C}$	-	3.5	-	nΗ
		measured from drain lead to centre of die ; $T_j = 25 ^{\circ}\text{C}$	-	4.5	-	nΗ
L _S	internal source inductance	measured from source lead to source bond pad; $T_j = 25$ °C	-	7.5	-	nΗ
Source-dr	ain diode					
	source-drain voltage	$I_S = 20 \text{ A}; V_{GS} = 0 \text{ V}; T_i = 25 ^{\circ}\text{C}$	-	0.95	1.2	V
V_{SD}	Source-drain voltage	0 , 00 , 1				
V_{SD}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = -10 \text{ V};$ $V_{DS} = 25 \text{ V}; T_i = 25 \text{ °C}$	-	124	-	ns

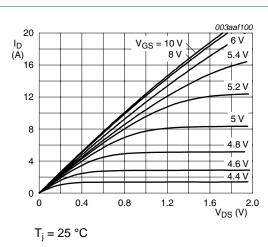


Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values

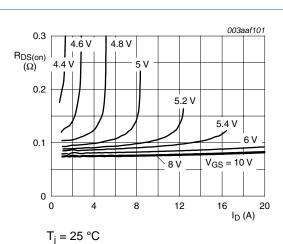


Fig 7. Drain-source on-state resistance as a function of drain current; typical values

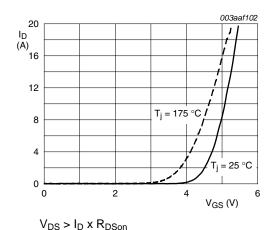


Fig 8. Transfer characteristics: drain current as a function of gate-source voltage; typical values

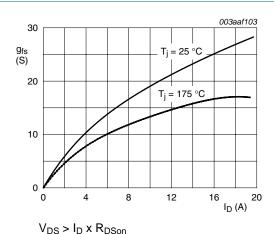


Fig 9. Forward transconductance as a function of drain current; typical values

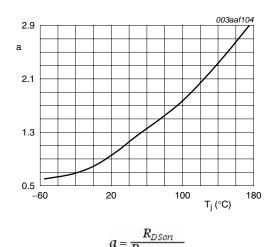
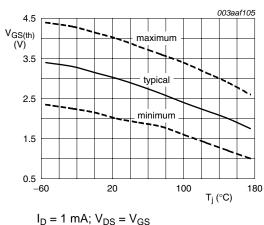


Fig 10. Normalized drain-source on-state resistance factor as a function of junction temperature



ID = 1 III/1, VDS = VGS

Fig 11. Gate-source threshold voltage as a function of junction temperature

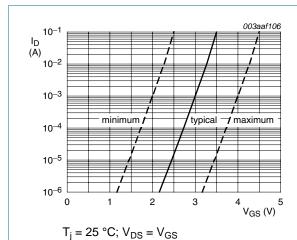
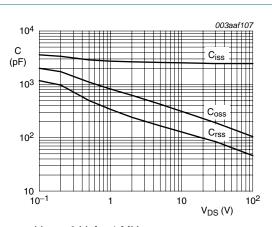


Fig 12. Sub-threshold drain current as a function of gate-source voltage



 $V_{GS} = 0 V$; f = 1 MHz

Fig 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

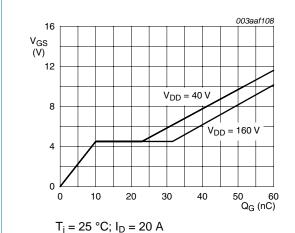
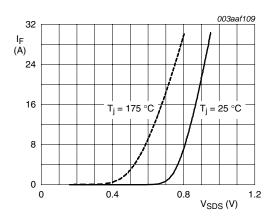


Fig 14. Gate-source voltage as a function of gate charge; typical values



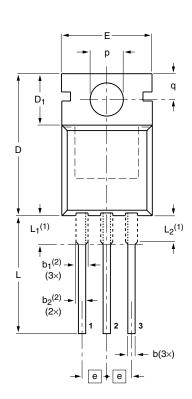
 $V_{GS} = 0 V$

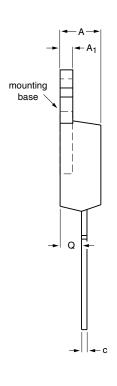
Fig 15. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

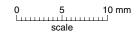
SOT78

7. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB







DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁	b	b ₁ (2)	b ₂ (2)	С	D	D ₁	E	е	L	L ₁ (1)	L ₂ ⁽¹⁾ max.	р	q	q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

Notes

- 1. Lead shoulder designs may vary.
- 2. Dimension includes excess dambar.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT78		3-lead TO-220AB	SC-46		08-04-23 08-06-13

Fig 16. Package outline SOT78 (TO-220AB)

PHP20NQ20T



8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
PHP20NQ20T v.2	20101216	Product data sheet	-	PHB_PHP20NQ20T v.1		
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guideline of NXP Semiconductors. 					
	 Legal texts 	have been adapted to th	e new company name v	where appropriate.		
	 Type numb 	er PHP20NQ20T separa	ted from data sheet PH	B_PHP20NQ20T v.1.		
PHB_PHP20NQ20T v.1	19990801	Product specification	-	-		

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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PHP20NQ20T

N-channel TrenchMOS standard level FET

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