1. General description

XC7SH04 is a high-speed Si-gate CMOS device. It provides an inverting buffer.

2. Features and benefits

- · Symmetrical output impedance
- High noise immunity
- Low power dissipation
- · CMOS input levels
- · Balanced propagation delays
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package							
	Temperature range	Name	Description	Version				
XC7SH04GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1				
XC7SH04GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753				

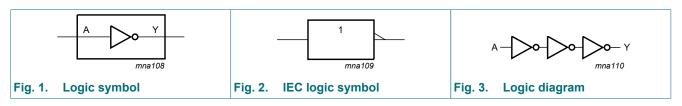
4. Marking

Table 2. Marking codes

Type number	Marking [1]
XC7SH04GW	fC
XC7SH04GV	f04

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

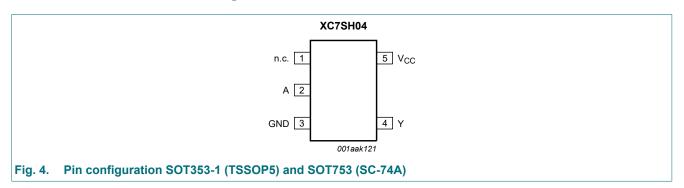




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6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A	2	data input
GND	3	ground (0 V)
Υ	4	data output
V _{CC}	5	supply voltage

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
A	Υ
L	Н
Н	L

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V	-20	-	mA
I _{OK}	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I _O	output current	-0.5 V < V _O < V _{CC} + 0.5 V	-	±25	mA
I _{CC}	supply current		-	75	mA
I_{GND}	ground current		-75	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C to } +125 ^{\circ}\text{C}$ [2]	-	250	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CC}	supply voltage		2.0	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$	-	-	100	ns/V
		V _{CC} = 5.0 V ± 0.5 V	-	-	20	ns/V

^[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P_{tot} derates linearly with 3.8 mW/K above 85 °C.

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10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level	V _{CC} = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V _{CC} = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V _{IL}	LOW-level	V _{CC} = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V _{CC} = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V _{OH}	HIGH-level	$V_I = V_{IH}$ or V_{IL}								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		$I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.40	-	V
		I _O = -8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
II	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

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11. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V. For test circuit see Fig. 6.

Symbol	Parameter	Conditions	Conditions		onditions 25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
				Min	Тур	Max	Min	Max	Min	Max	
t _{pd}	propagation	A to Y; see Fig. 5	[1]								
	delay	V _{CC} = 3.0 V to 3.6 V	[2]								
	C _L = 15 pF		-	4.3	7.1	1.0	8.5	1.0	11.0	ns	
		C _L = 50 pF		-	6.1	10.6	1.0	12	1.0	14.5	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.1	5.5	1.0	6.5	1.0	7.0	ns
		C _L = 50 pF		-	4.5	7.5	1.0	8.5	1.0	9.5	ns
C _{PD}	power dissipation capacitance	per buffer; C _L = 50 pF; f = 1 MHz; V _I = GND to V _{CC}	[4]	-	15	-	-	-	-	-	pF

- t_{pd} is the same as t_{PLH} and $t_{\text{PHL}}.$
- Typical values are measured at V_{CC} = 3.3 V.
- [3] Typical values are measured at V_{CC} = 5.0 V.
 [4] C_{PD} is used to determine the dynamic power dissipation P_D (μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz; f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V; $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of outputs.

11.1. Waveform and test circuit

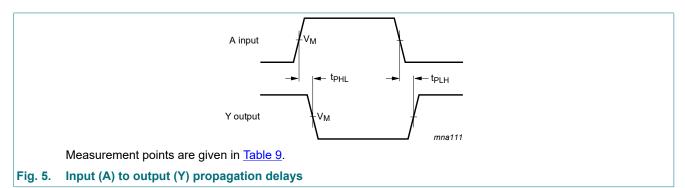
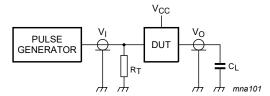


Table 9. Measurement point

Input	Output	
V _I	V _M	V _M
GND to V _{CC}	0.5 × V _{CC}	0.5 × V _{CC}

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Test data is given in Table 10.

Definitions for test circuit:

C_L = Load capacitance including jig and probe capacitance;

 R_{T} = Termination resistance should be equal to output impedance Z_{o} of the pulse generator.

Fig. 6. Test circuit for measuring switching times

Table 10. Test data

Input		Load	Test
V _I	t _r , t _f	CL	
V _{CC}	≤ 3.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}

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12. Package outline

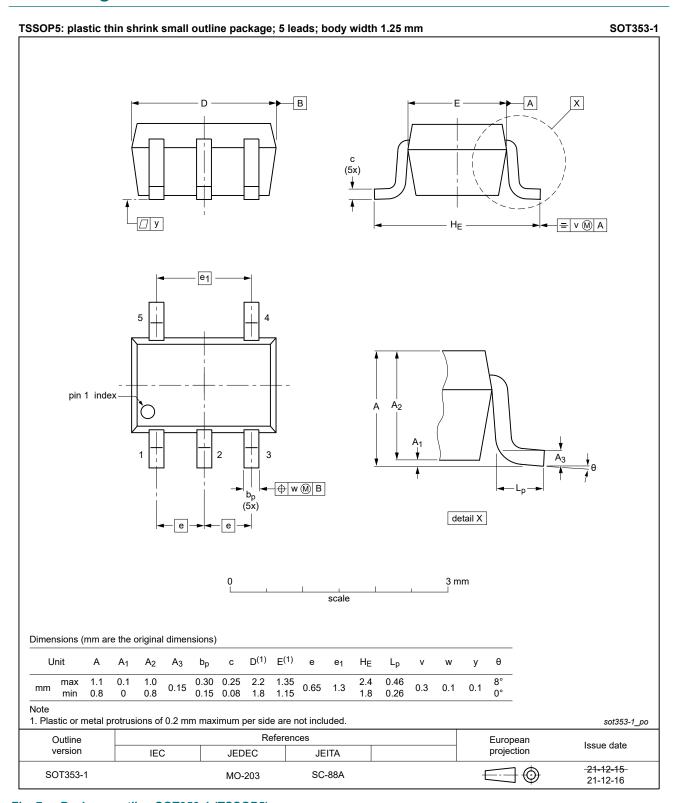
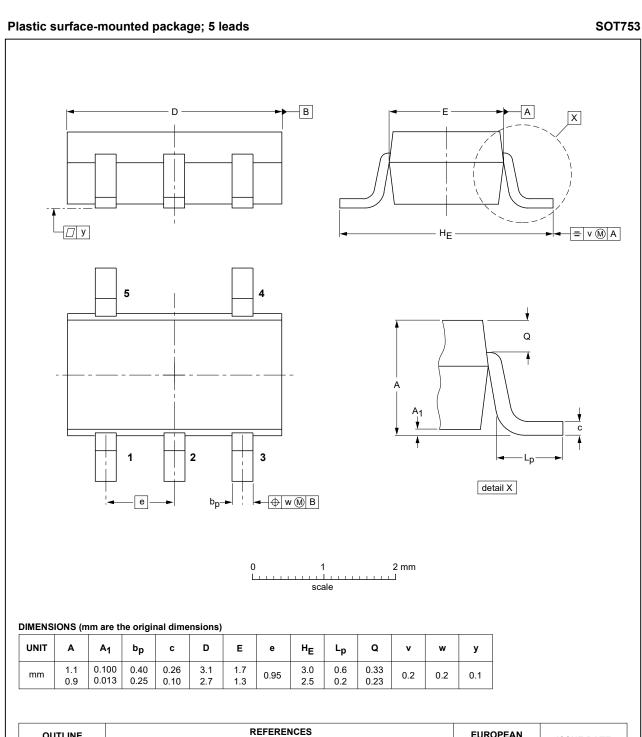


Fig. 7. Package outline SOT353-1 (TSSOP5)

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SOT753 SC-74A

EUROPEAN PROJECTION OF 102-04-16-06-03-16

Fig. 8. Package outline SOT753 (SC-74A)

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
XC7SH04 v.3	20231222	Product data sheet	-	XC7SH04 v.2			
Modifications:	<u>Section 2</u> : ESD specification updated according to the latest JEDEC standard.						
XC7SH04 v.2	20220126	Product data sheet	-	XC7SH04 v.1			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section 8: Derating values for P_{tot} total power dissipation updated. Fig. 7: Package outline drawing SOT353-1 (TSSOP5) has changed. 						
XC7SH04 v.1	20090901	Product data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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