**Product data sheet** 

# 1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

#### 2. Features and benefits

- Low threshold voltage
- Low on-state resistance
- Trench MOSFET technology
- Enhanced power dissipation capability of 1096 mW

# 3. Applications

- Relay driver
- High-speed line driver
- High-side loadswitch
- Switching circuits

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	-20	V
V <sub>GS</sub>	gate-source voltage			-12	-	12	V
I <sub>D</sub>	drain current	$V_{GS} = -4.5 \text{ V}; T_{amb} = 25 \text{ °C}; t \le 5 \text{ s}$	[1]	-	-	-4.4	Α
Static charac	Static characteristics						
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = -4.5 V; $I_D$ = -3.6 A; $T_j$ = 25 °C		-	48	60	mΩ

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



20 V, P-channel Trench MOSFET

# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2	G S S 017aaa257
			TO-236AB (SOT23)	

# 6. Ordering information

Table 3. Ordering information

Type number	Package	ackage					
	Name	Description	Version				
PMV50XP	TO-236AB	plastic surface-mounted package; 3 leads	SOT23				

## 7. Marking

Table 4. Marking codes

Table II III III II II II II II II II II II	
Type number	Marking code
	[1]
PMV50XP	%2M

[1] % = placeholder for manufacturing site code

20 V, P-channel Trench MOSFET

# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-20	V
V <sub>GS</sub>	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-4.4	Α
		V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-3.6	Α
		V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	-2.3	Α
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10$ μs		-	-14.5	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	490	mW
			[1]	-	1096	mW
		T <sub>sp</sub> = 25 °C		-	4630	mW
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drain o	liode					J.
Is	source current	T <sub>sp</sub> = 25 °C	[1]	-	-1	Α

- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

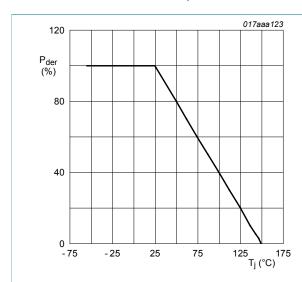


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

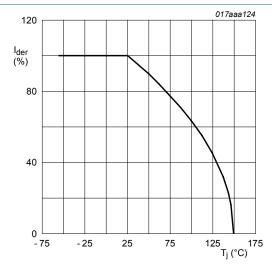


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}\text{C})}} \times 100 \%$$

PMV50XP

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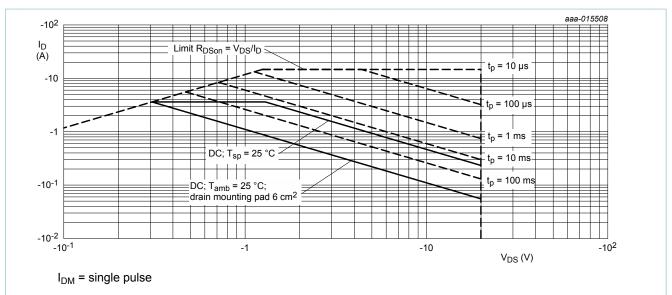


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	_	[1]	-	217	255	K/W
			<u>[2]</u>	-	97	114	K/W
		in free air; t ≤ 5 s	<u>[2]</u>	-	65	76	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	23	27	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

### 20 V, P-channel Trench MOSFET

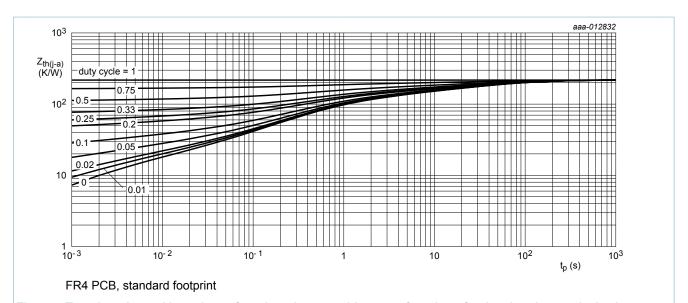


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

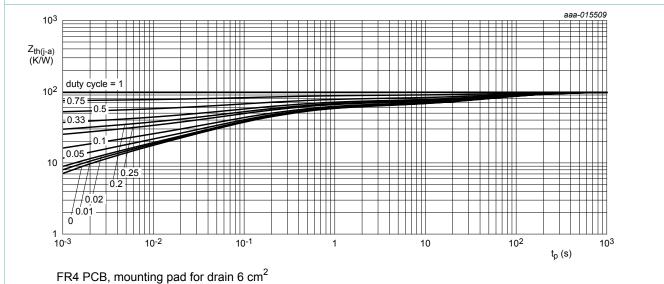


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

20 V, P-channel Trench MOSFET

# 10. Characteristics

#### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D$ = -250 $\mu$ A; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-20	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = -250 \ \mu A; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^{\circ}C$	-0.47	-0.65	-0.9	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = -20 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
		V <sub>GS</sub> = 12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
R <sub>DSon</sub>	drain-source on-state	$V_{GS}$ = -4.5 V; $I_D$ = -3.6 A; $T_j$ = 25 °C	-	48	60	mΩ
	resistance	V <sub>GS</sub> = -4.5 V; I <sub>D</sub> = -3.6 A; T <sub>j</sub> = 150 °C	-	68	86	mΩ
		$V_{GS}$ = -2.5 V; $I_D$ = -3.1 A; $T_j$ = 25 °C	-	60	80	mΩ
		V <sub>GS</sub> = -1.8 V; I <sub>D</sub> = -0.8 A; T <sub>j</sub> = 25 °C	-	82	121	mΩ
		V <sub>GS</sub> = -1.5 V; I <sub>D</sub> = -0.1 A; T <sub>j</sub> = 25 °C	-	116	250	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = -10 V; $I_{D}$ = -2 A; $T_{j}$ = 25 °C	-	9	-	S
Dynamic ch	naracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = -6 V; $I_{D}$ = -2.8 A; $V_{GS}$ = -4.5 V;	-	7.7	12	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C	-	1	-	nC
$Q_{GD}$	gate-drain charge		-	1.65	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = -20 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	744	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	65	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	53	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = -6 V; $V_{GS}$ = -4.5 V; $R_{G(ext)}$ = 6 $\Omega$ ;	-	7	-	ns
t <sub>r</sub>	rise time	T <sub>j</sub> = 25 °C; I <sub>D</sub> = -1 A	-	18	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	135	-	ns
t <sub>f</sub>	fall time		-	68	-	ns
Source-dra	in diode		l l	1	1	
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = -1 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	-0.74	-1.2	V

### 20 V, P-channel Trench MOSFET

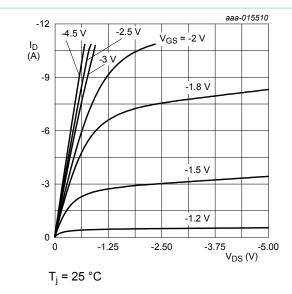


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

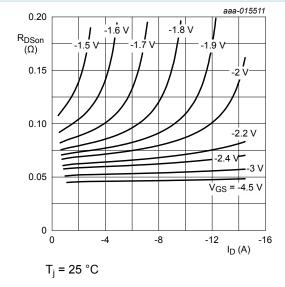


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

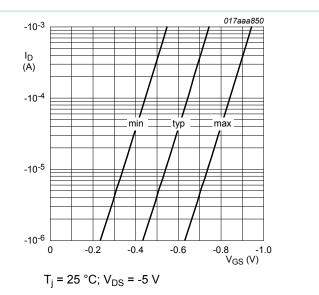


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

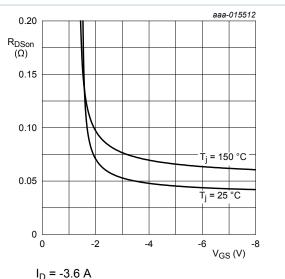


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

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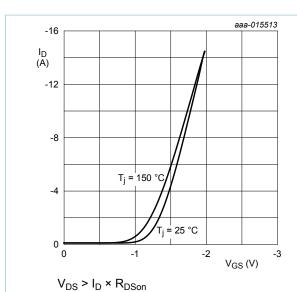


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

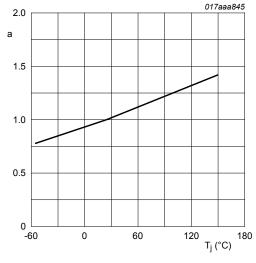


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

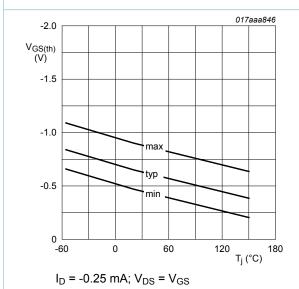


Fig. 12. Gate-source threshold voltage as a function of junction temperature

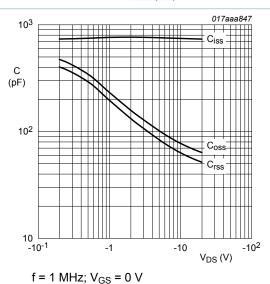


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

### 20 V, P-channel Trench MOSFET

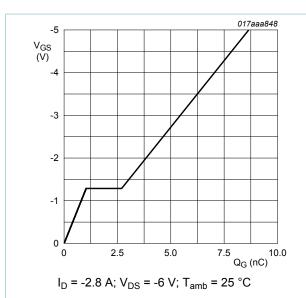


Fig. 14. Gate-source voltage as a function of gate charge; typical values

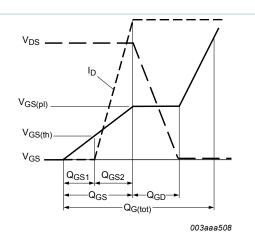
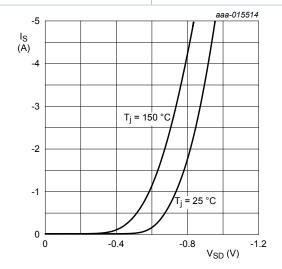


Fig. 15. MOSFET transistor: Gate charge waveform definitions



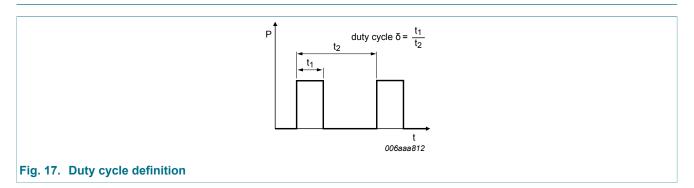
 $V_{GS} = 0 V$ (1)  $T_i = 150 \, ^{\circ}C$ (2)  $T_i = 25 \, ^{\circ}C$ 

Fig. 16. Source current as a function of source-drain voltage; typical values

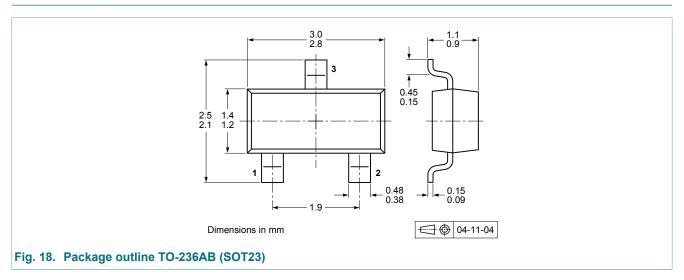
9 / 15

20 V, P-channel Trench MOSFET

# 11. Test information

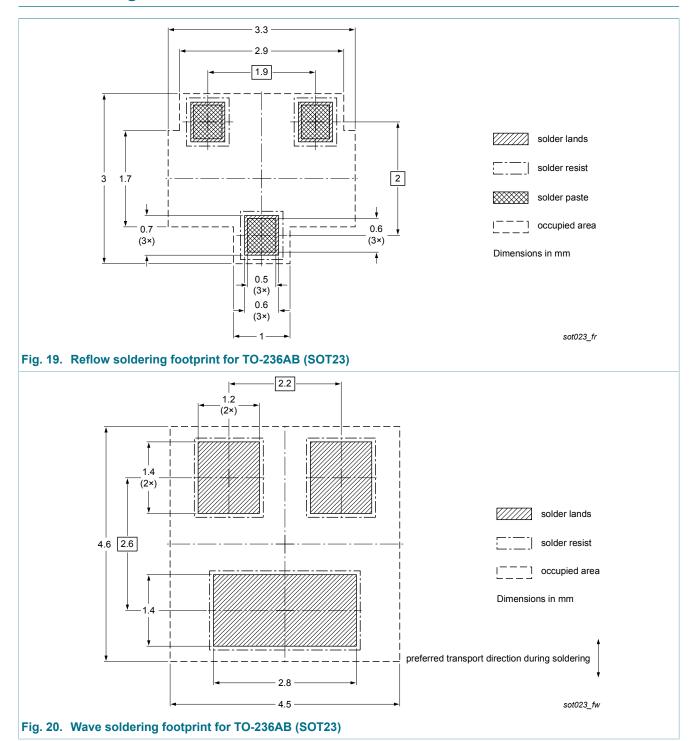


# 12. Package outline



20 V, P-channel Trench MOSFET

# 13. Soldering



20 V, P-channel Trench MOSFET

# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes					
PMV50XP v.2	20141119	Product data sheet	-	PMV50XP v.1					
Modifications:	Table 7: R <sub>DSon</sub> unit corre	Table 7: R <sub>DSon</sub> unit corrected							
PMV50XP v.1	20141111	Product data sheet	-	-					

#### 20 V, P-channel Trench MOSFET

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#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
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20 V, P-channel Trench MOSFET

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## 20 V, P-channel Trench MOSFET

## 16. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Thermal characteristics	4
10	Characteristics	6
11	Test information	10
12	Package outline	10
13	Soldering	11
14	Revision history	12
15	Legal information	13
15.1	Data sheet status	13
15.2	Definitions	13
15.3	Disclaimers	13
15.4	Trademarks	14

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