

PMEG2020AEA

20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package

23 September 2020

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD323 (SC-76) very small SMD plastic package.

2. Features and benefits

- Forward current: 2 A
- Reverse voltage: 20 V
- · Very low forward voltage
- · Very small SMD package.
- AEC-Q101 qualified

3. Applications

- · Low voltage rectification
- · High efficiency DC/DC conversion
- · Switch mode power supply
- · Inverse polarity protection
- · Low power consumption applications.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _F	forward current	$T_{sp} \le 55 ^{\circ}C$	[1]	-	-	2	Α
V_R	reverse voltage	T _j = 25 °C		-	-	20	V
V _F	forward voltage	I_F = 2 A; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; pulsed; T_j = 25 °C		-	450	525	mV

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	1 2	K - ∏—A
2	А	anode	SOD323	sym001



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6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2020AEA		plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body	SOD323

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2020AEA	S3

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134)

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	20	V
l _F	forward current	T _{sp} ≤ 55 °C	[1]	-	2	Α
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	7	А
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave		-	9	A
Tj	junction temperature		[2]	-	150	°C
T _{amb}	ambient temperature		[2]	-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
""(J-"")	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	450	K/W
			[3] [4]	-	-	210	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	90	K/W

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications, the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and I_{F(AV)} rating will be available on request.

[5] Soldering point of cathode tab.

^[2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and I_{F(AV)} rating will be available on request.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and I_{F(AV)} rating will be available on request.

^[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _F	forward voltage	I_F = 0.01 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C		-	200	220	mV
		I_F = 0.1 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C		-	265	290	mV
		$I_F = 1 \text{ A}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$		-	380	430	mV
		I_F = 2 A; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; pulsed; T_j = 25 °C		-	450	525	mV
I _R	reverse current	V _R = 5 V; pulsed; T _j = 25 °C		-	15	50	μA
		V _R = 10 V; T _j = 25 °C	[1]	-	20	80	μA
		V _R = 20 V; T _j = 25 °C		-	50	200	μA
C _d	diode capacitance	$V_R = 5 \text{ V}; f = 1 \text{ MHz}; T_j = 25 ^{\circ}\text{C}$		-	55	70	pF

[1] Pulsed test: $t_p \le 300 \,\mu s$; $\delta \le 0.02$

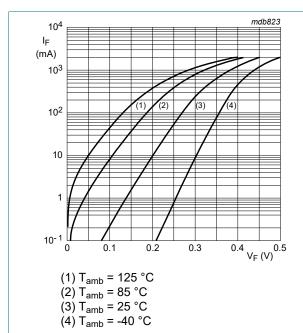


Fig. 1. Forward current as a function of forward voltage; typical values

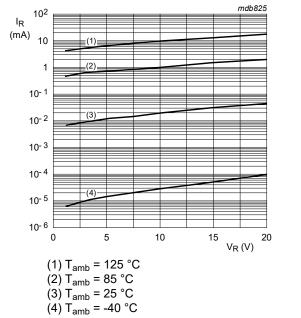
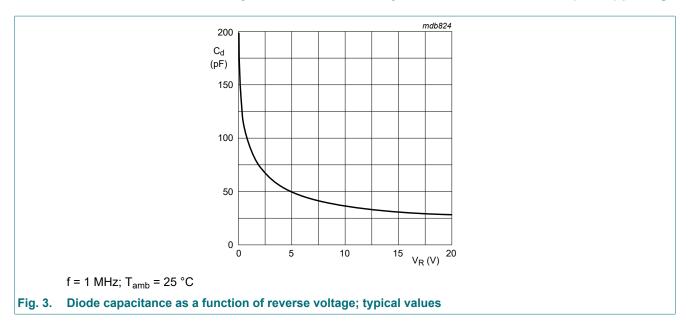
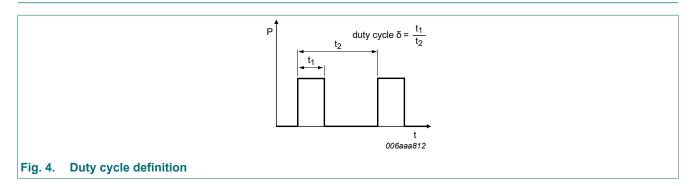


Fig. 2. Reverse current as a function of reverse voltage; typical values

20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package



11. Test information

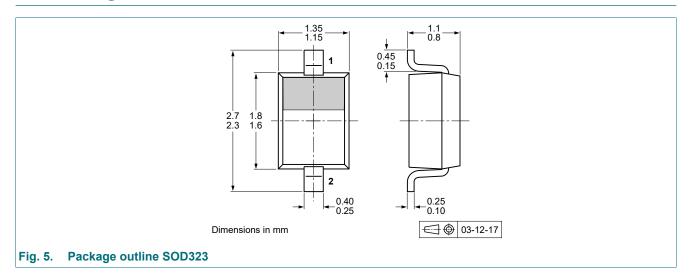


Quality information

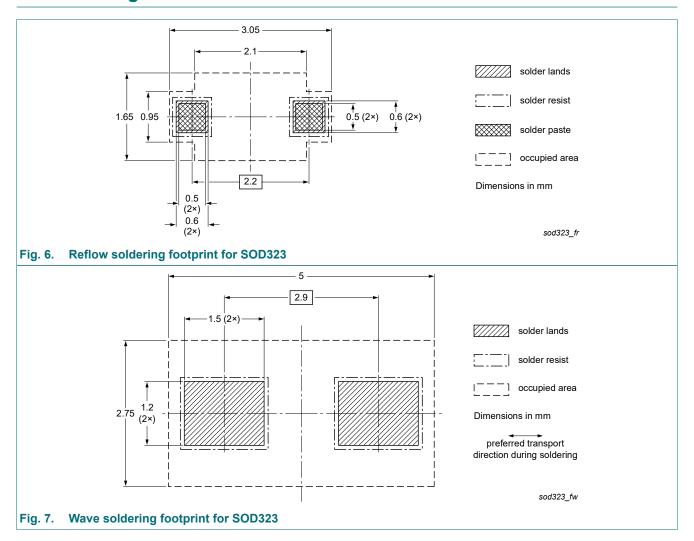
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package

12. Package outline



13. Soldering



20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG2020AEA v.2	20200923	Product data sheet	-	PMEG2010BEA v.1			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Automotive qualification added in sections: Features and benefits, Test information and Legal information 						
PMEG2020AEA v.2	20040226	Product data sheet	-	-			

20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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PMEG2020AEA

20 V, 2 A very low VF MEGA Schottky barrier rectifier in SOD323 (SC-76) package

Contents

1.	General description	. 1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	. 1
5.	Pinning information	.1
6.	Ordering information	.2
7.	Marking	. 2
8.	Limiting values	. 2
9.	Thermal characteristics	. 2
10.	Characteristics	. 3
11.	Test information	. 4
12.	Package outline	. 5
	Soldering	
	Revision history	
	Legal information	

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PMEG2020AEA,115