

# PMEG2005BELD

20 V, 0.5 A low VF MEGA Schottky barrier rectifier

August 2015 Product data sheet

# 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a leadless ultra small SOD882D (DFN1006D-2) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

#### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 0.5 A
- Reverse voltage: V<sub>R</sub> ≤ 20 V
- Low forward voltage V<sub>F</sub> ≤ 390 mV
- AEC-Q101 qualified
- Ultra small and leadless SMD plastic package
- Solderable side pads
- Package height typ. 0.37 mm

# 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- Ultra high-speed switching
- LED backlight for mobile application

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
. (,)	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \le$ 140 °C; square wave		-	-	0.5	A
		$\delta$ = 0.5 ; f = 20 kHz; $T_{amb} \le$ 115 °C; square wave	[1]	-	-	0.5	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>F</sub>	forward voltage	$I_F$ = 500 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C		-	353	390	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C		-	28	50	μA



[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

# 5. Pinning information

**Table 2.** Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		1 - 2
2	А	anode		sym001
			Transparent top view	
			DFN1006D-2 (SOD882D)	

[1] The marking bar indicates the cathode.

# 6. Ordering information

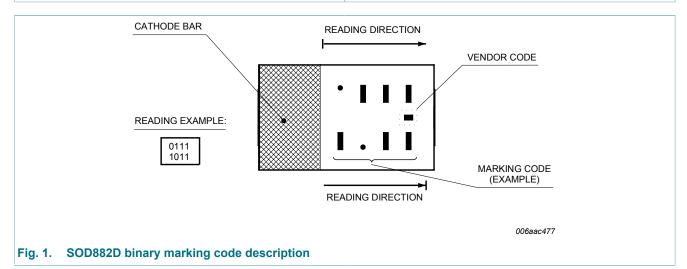
Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG2005BELD	DFN1006D-2	DFN1006D-2: leadless ultra small plastic package; 2 terminals	SOD882D		

# 7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2005BELD	0010 1000



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# **Limiting values**

Table 5. **Limiting values** 

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	20	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 140 °C		-	0.5	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \le$ 140 °C; square wave		-	0.5	А
		$\delta$ = 0.5 ; f = 20 kHz; $T_{amb} \le$ 115 °C; square wave	[1]	-	0.5	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	3	Α
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	6	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2][3]	-	370	mW
			[1][3]	-	735	mW
			[4][3]	-	1135	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

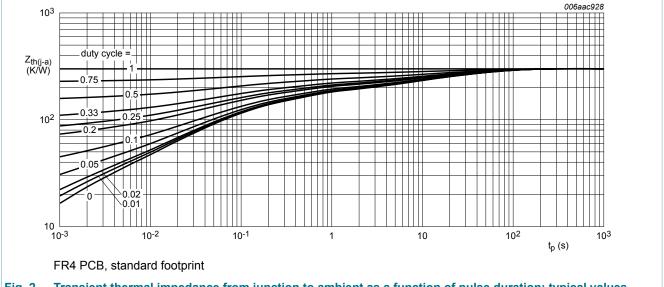
Reflow soldering is the only recommended soldering method. Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

### Thermal characteristics

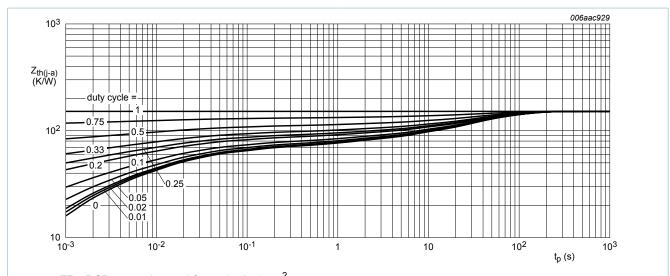
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	thermal resistance	in free air	[1][2][3]	-	-	340	K/W
		[1][4][3]	-	-	170	K/W	
	ambient		[1][5][3]	-	-	110	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[6]</u>	-	-	25	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Reflow soldering is the only recommended soldering method. [3]
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- Soldering point of cathode tab.



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 2.



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

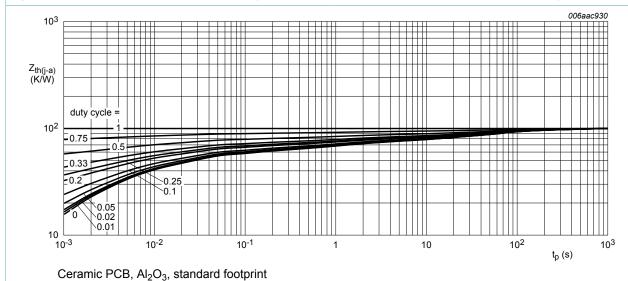


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

# 10. Characteristics

Table 7. Characteristics

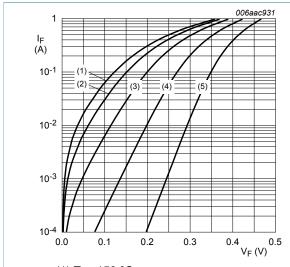
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	$I_F$ = 0.1 mA; pulsed; $t_p \le 300 \text{ μs}$ ; $δ \le 0.02 \text{ ; } T_j = 25 \text{ °C}$	-	79	105	mV
		$I_F$ = 1 mA; pulsed; $t_p \le 300 \text{ μs}$ ; $\delta \le 0.02 \text{ ; } T_j = 25 \text{ °C}$	-	137	170	mV
		$I_F$ = 10 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C	-	197	235	mV

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$I_F$ = 100 mA; pulsed; $t_p$ ≤ 300 μs; $\delta$ ≤ 0.02 ; $T_j$ = 25 °C	-	266	310	mV
		$I_F$ = 500 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C	-	353	390	mV
I <sub>R</sub> reverse current	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C	-	28	50	μA
		V <sub>R</sub> = 20 V; T <sub>j</sub> = 25 °C	-	87	200	μA
C <sub>d</sub>	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$	-	31	40	pF
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	1.6	-	ns
V <sub>FRM</sub>	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}I_F/\text{d}t = 20 \text{ A/}\mu\text{s}; T_j = 25 °C$	-	565	-	mV



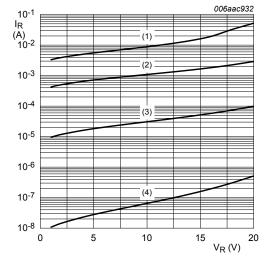
(1)  $T_j = 150 \, ^{\circ}\text{C}$ 

(3) 
$$T_i = 85 \, ^{\circ}C$$

(4) 
$$T_i = 25 \, ^{\circ}C$$

(5) 
$$T_i = -40 \, ^{\circ}C$$

Fig. 5. Forward current as a function of forward voltage; typical values



(1)  $T_j = 125 \,^{\circ}\text{C}$ 

(2) 
$$T_i = 85 \, ^{\circ}C$$

(3) 
$$T_i = 25 \,^{\circ}C$$

(4) 
$$T_i = -40 \, ^{\circ}C$$

Fig. 6. Reverse current as a function of reverse voltage; typical values

<sup>(2)</sup>  $T_i = 125 \, ^{\circ}C$ 

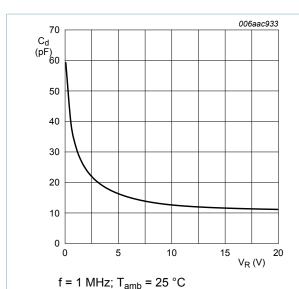
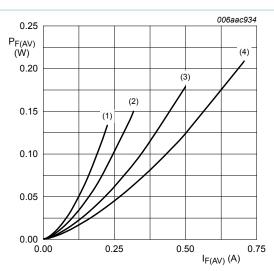


Fig. 7. Diode capacitance as a function of reverse voltage; typical values



T<sub>j</sub> = 150 °C

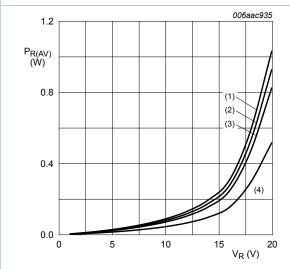
 $(1) \delta = 0.1$ 

(2)  $\delta = 0.2$ 

 $(3) \delta = 0.5$ 

 $(4) \delta = 1$ 

Fig. 8. Average forward power dissipation as a function of average forward current; typical values



T<sub>i</sub> = 125 °C

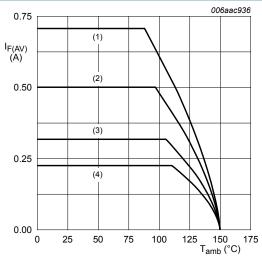
(1)  $\delta = 1$  (DC)

(2)  $\delta$  = 0.9; f = 20 kHz

(3)  $\delta$  = 0.8; f = 20 kHz

(4)  $\delta$  = 0.5; f = 20 kHz

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

 $T_j = 150 \,{}^{\circ}\text{C}$ 

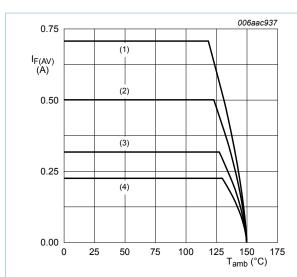
 $(1) \delta = 1$ 

 $(2) \delta = 0.5$ 

 $(3) \delta = 0.2$ 

 $(4) \delta = 0.1$ 

Fig. 10. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

T<sub>i</sub> = 150 °C

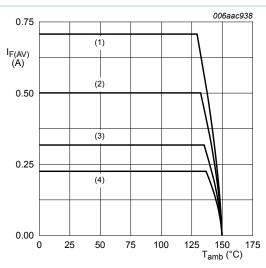
 $(1) \delta = 1$ 

 $(2) \delta = 0.5$ 

(3)  $\delta = 0.2$ 

 $(4) \delta = 0.1$ 

Fig. 11. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 150 °C

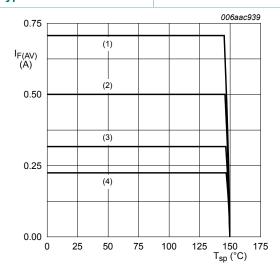
 $(1) \delta = 1$ 

 $(2) \delta = 0.5$ 

 $(3) \delta = 0.2$ 

 $(4) \delta = 0.1$ 

Fig. 12. Average forward current as a function of ambient temperature; typical values



T<sub>i</sub> = 150 °C

 $(1) \delta = 1$ 

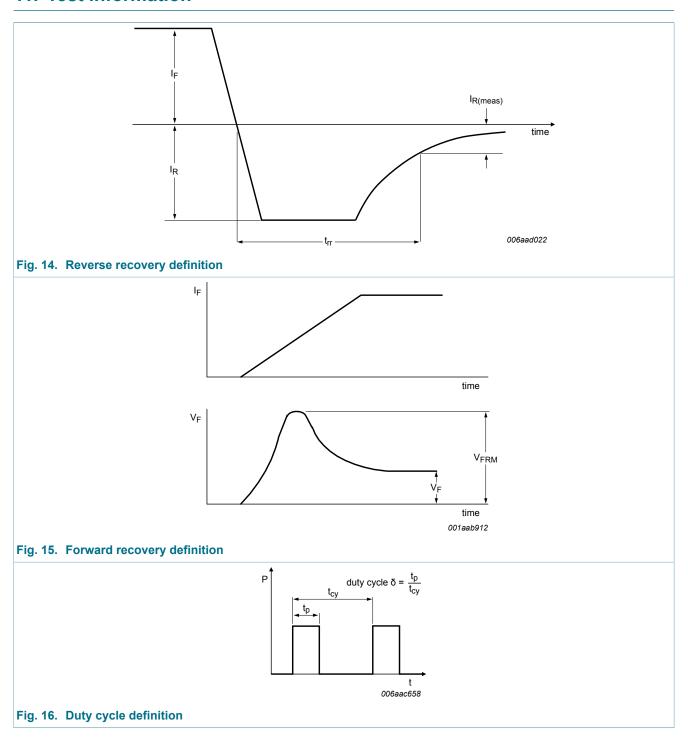
 $(2) \delta = 0.5$ 

 $(3) \delta = 0.2$ 

 $(4) \delta = 0.1$ 

Fig. 13. Average forward current as a function of solder point temperature; typical values

# 11. Test information

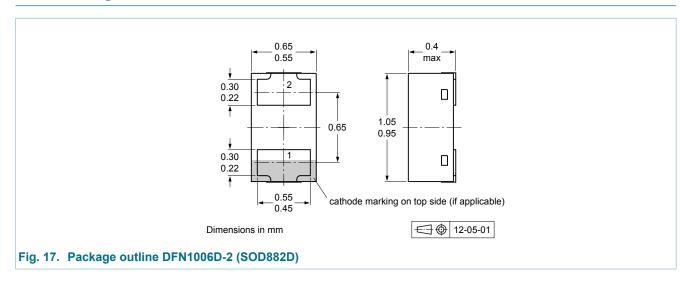


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

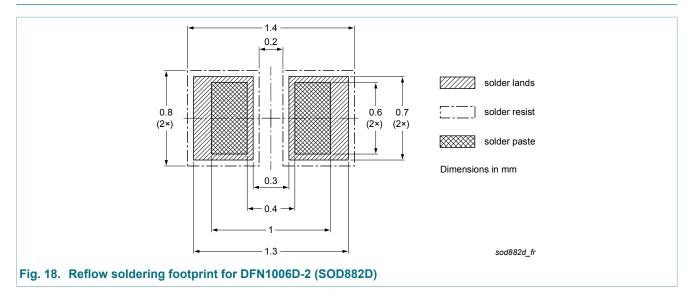
## 11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 12. Package outline



# 13. Soldering



# 14. Revision history

#### Table 8. **Revision history**

Document ID	Release date	Document status	Change notice	Supersedes
PMEG2005BELD v.4	20150804	Product data sheet	-	PMEG2005BELD v.3
Modifications:	Section "Marking":	updated Figure 1.		
PMEG2005BELD v.3	20120704	Product data sheet	-	PMEG2005BELD v.2
PMEG2005BELD v.2	20120312	Product data sheet	-	PMEG2005BELD v.1
PMEG2005BELD v.1	20120111	Preliminary data sheet	-	-

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#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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