

PMDPB85UPE 20 V dual P-channel Trench MOSFET Rev. 1 – 20 June 2012

Product data sheet

Product profile 1.

1.1 General description

Dual small-signal P-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020-6 (SOT1118) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Low threshold voltage
- Very fast switching

Trench MOSFET technology

High-side load switch

Switching circuits

2 kV ElectroStatic Discharge (ESD) protection

1.3 Applications

- Relay driver
- High-speed line driver

1.4 Quick reference data

Table 1.	Quick reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per trans	istor						
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V_{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-3.7	А
Static cha	aracteristics (per transistor)						
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -1.3 A; T _j = 25 °C		-	82	103	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

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20 V dual P-channel Trench MOSFET

2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1		
2	G1	gate TR1	6 5 4	
3	D2	drain TR2		
4	S2	source TR2	7 8	$G1 \left(\begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$
5	G2	gate TR2		
6	D1	drain TR1	1 2 3	
7	D1	drain TR1	Transparent top view	S1 S2 017aaa260
8	D2	drain TR2	DFN2020-6 (SOT1118)	

3. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PMDPB85UPE	DFN2020-6	plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1118		

4. Marking

Table 4. Marking codes	
Type number	Marking code
PMDPB85UPE	2C

5. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	tor					
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-8	8	V
I _D drain cu	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	<u>[1]</u>	-	-3.7	А
		V_{GS} = -4.5 V; T_{amb} = 25 °C	<u>[1]</u>	-	-2.9	А
		$V_{GS} = -4.5 \text{ V}; \text{ T}_{amb} = 100 ^{\circ}\text{C}$	<u>[1]</u>	-	-1.8	А
I _{DM}	peak drain current	$T_{amb} = 25 \text{ °C}$; single pulse; $t_p \le 10 \mu\text{s}$		-	-11.6	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	515	mW
			[1]	-	1170	mW
		T _{sp} = 25 °C		-	8330	mW
Source-dra	in diode					
I _S	source current	T _{amb} = 25 °C	<u>[1]</u>	-	-1.2	А
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Product data sheet		Rev. 1 — 20 June 2012				2 of 1

20 V dual P-channel Trench MOSFET

Table 5. Limiting values ...continued

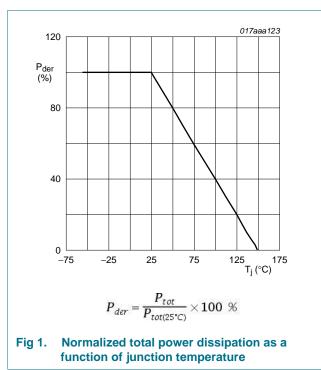
In accordance with the Absolute Maximum Rating System (IEC 60134).

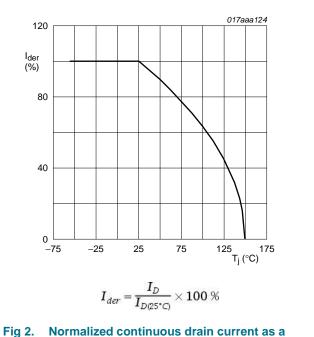
Symbol	Parameter	Conditions		Min	Мах	Unit
ESD maxim	um rating					
V _{ESD}	electrostatic discharge voltage	HBM; C = 100 pF; R = 1.5 kΩ	[3]	-	2000	V
Per device						
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[3] Measured between all pins.

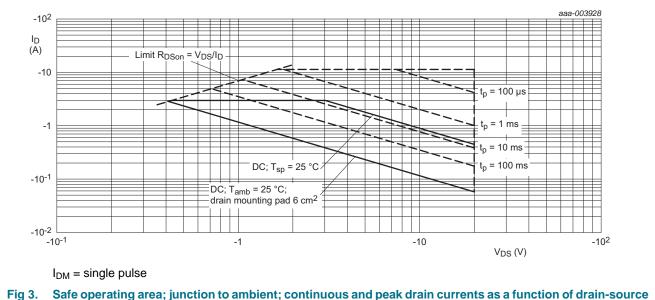




function of junction temperature

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6. Thermal characteristics

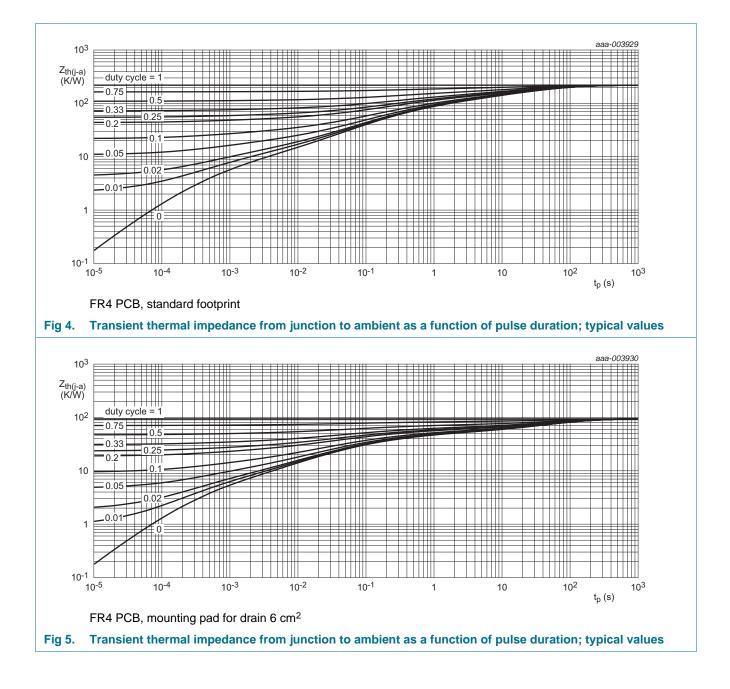
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transisto	r						
R _{th(j-a)}	thermal resistance	in free air	<u>[1]</u>	-	211	243	K/W
	from junction to ambient		[2]	-	93	107	K/W
		in free air; t ≤ 5 s	[2]	-	55	64	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	12	15	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

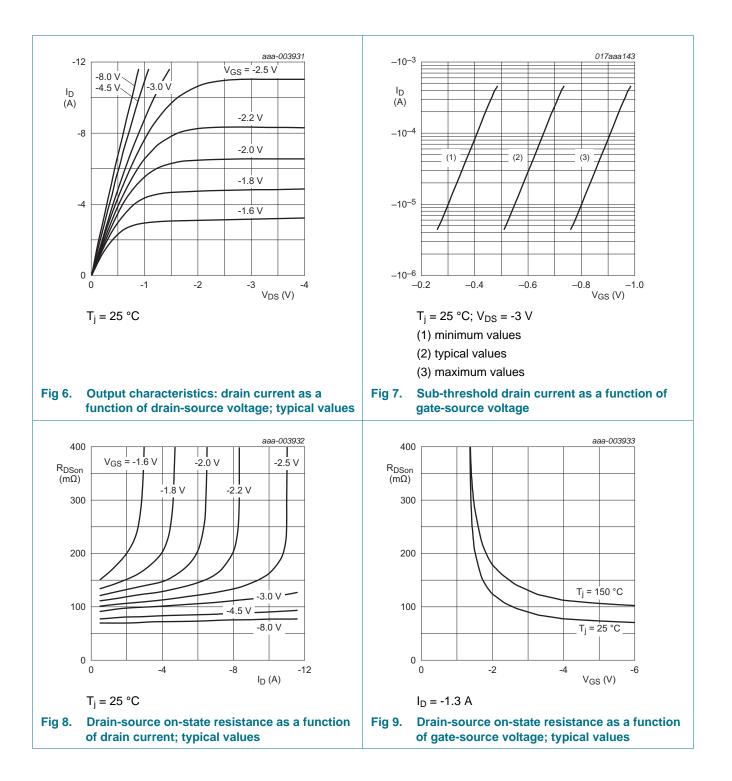
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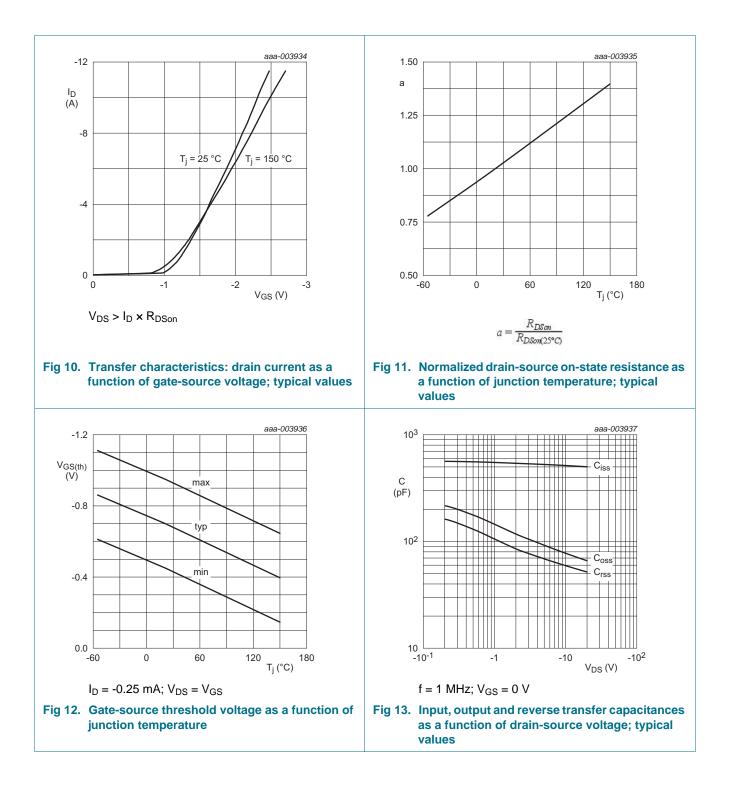
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7. Characteristics

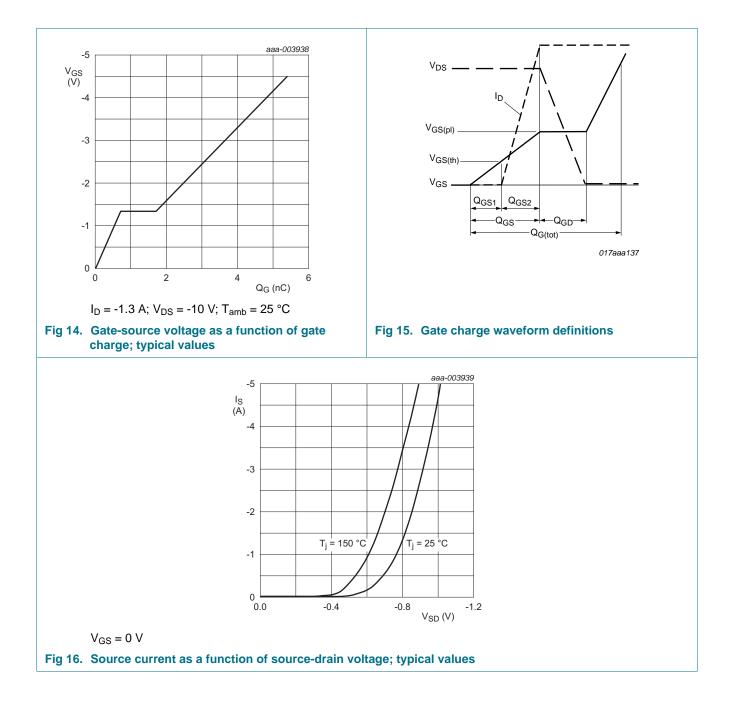
Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics (per transistor)					
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = -250 \ \mu\text{A}; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^\circ\text{C}$	-20	-	-	V
V _{GSth}	gate-source threshold voltage	$I_D = -250 \ \mu A; V_{DS} = V_{GS}; T_j = 25 \ ^{\circ}C$	-0.45	-0.7	-0.95	V
I _{DSS}	drain leakage current	$V_{DS} = -20 \text{ V}; \text{ V}_{GS} = 0 \text{ V}; \text{ T}_{j} = 25 \text{ °C}$	-	-	-1	μA
		$V_{DS} = -20 \text{ V}; \text{ V}_{GS} = 0 \text{ V}; \text{ T}_{j} = 150 \text{ °C}$	-	-	-10	μA
I _{GSS}	gate leakage current	$V_{GS} = 8 \text{ V}; V_{DS} = 0 \text{V}; \text{T}_{j} = 25 ^{\circ}\text{C}$	-	-	10	μA
		$V_{GS} = -8 \text{ V}; V_{DS} = 0 \text{ V}; \text{T}_{j} = 25 ^{\circ}\text{C}$	-	-	-10	μA
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -1.3 A; T _j = 25 °C	-	82	103	mΩ
		V_{GS} = -4.5 V; I _D = -1.3 A; T _j = 150 °C	-	114	144	mΩ
		V_{GS} = -2.5 V; I _D = -1.1 A; T _j = 25 °C	-	107	146	mΩ
		V_{GS} = -1.8 V; I _D = -0.8 A; T _j = 25 °C	-	142	210	mΩ
9 _{fs}	forward transconductance	V_{DS} = -10 V; I _D = -1.3 A; T _j = 25 °C	-	6	-	S
Dynamic	characteristics (per transist	or)				
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I_{D} = -1.3 A; V_{GS} = -4.5 V;	-	5.4	8.1	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.7	-	nC
Q _{GD}	gate-drain charge		-	1	-	nC
C _{iss}	input capacitance	V_{DS} = -10 V; f = 1 MHz; V_{GS} = 0 V;	-	514	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	78	-	pF
C _{rss}	reverse transfer capacitance		-	59	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I_{D} = -1.3 A; V_{GS} = -4.5 V;	-	6	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	12	-	ns
t _{d(off)}	turn-off delay time		-	47	-	ns
t _f	fall time		-	21	-	ns
Source-d	rain diode (per transistor)					
V _{SD}	source-drain voltage	I _S = -0.3 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.7	-1.2	V



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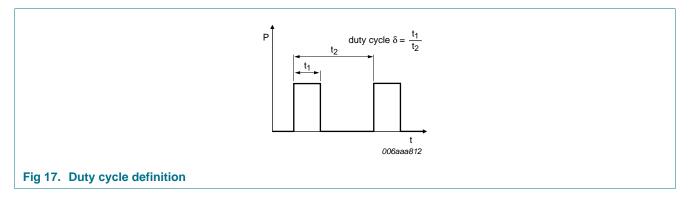


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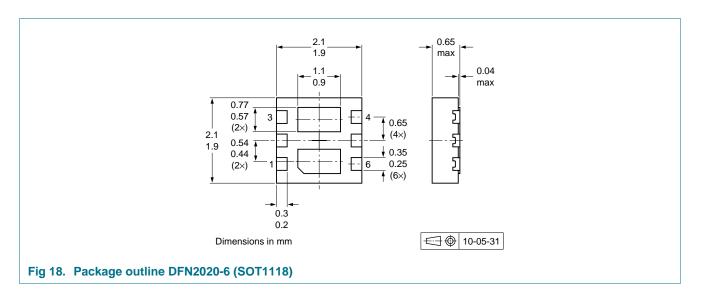


20 V dual P-channel Trench MOSFET

8. Test information

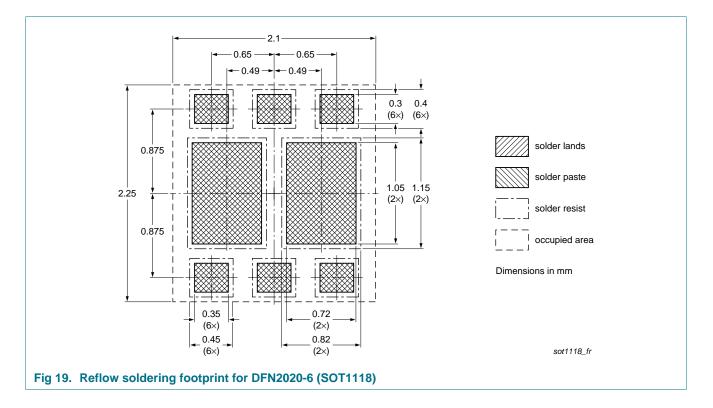


9. Package outline



20 V dual P-channel Trench MOSFET

10. Soldering



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11. Revision history

Table 8. Re	Revision history					
Document ID)	Release date	Data sheet status	Change notice	Supersedes	
PMDPB85UP	E v.1	20120620	Product data sheet	-	-	

20 V dual P-channel Trench MOSFET

12. Legal information

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Document status[1] [2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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20 V dual P-channel Trench MOSFET

14. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Marking
5	Limiting values2
6	Thermal characteristics4
7	Characteristics6
8	Test information10
9	Package outline10
10	Soldering11
11	Revision history12
12	Legal information13
12.1	Data sheet status
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks14
13	Contact information14

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