

### 1. General description

ESD protection device in a leadless ultra small DFN1006BD-2 (SOD882BD) Surface-Mounted Device (SMD) plastic package with side-wettable flanks, designed to protect automotive In-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

### 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 24 V
- Low clamping voltage: V<sub>CL</sub>= 33 V at I<sub>PP</sub> = 3.5 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- ESD protection up to 30 kV (ISO 10605: C = 330 pF, R = 330 Ω)
- Ultra low leakage current: I<sub>RM</sub> < 1 nA</li>
- Qualified according to AEC-Q101 / Automotive grade

### 3. Applications

ESD protection for In-vehicle network lines in automotive enviroments

- CAN
- LIN
- FlexRay
- SENT

### 4. Quick reference data

Table T. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	24	V
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	-	3.5	A
V <sub>CL</sub>	clamping voltage	$I_{PPM}$ = 3.5 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1]	-	33	42	V

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

# nexperia

# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)		006aab041
			Transparent top view	
			DFN1006BD-2 (SOD882BD)	

# 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PESD1IVN24-LS		Leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.47 mm body	SOD882BD			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
PESD1IVN24-LS	8н

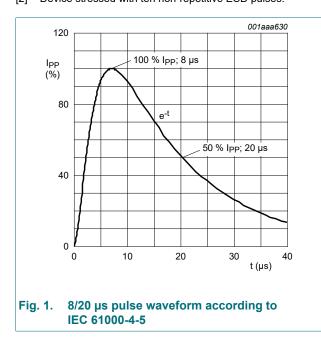
### 8. Limiting values

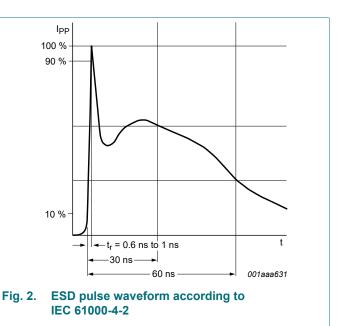
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	3.5	А
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximu	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-	30	kV
	voltage	ISO 10605: contact discharge; C = 330 pF, R = 330 $\Omega$	[2]	-	30	kV
		ISO 10605: contact discharge; C = 150 pF, R = 330 $\Omega$	[2]	-	30	kV

Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.
 Device stressed with ten non-repetitive ESD pulses.



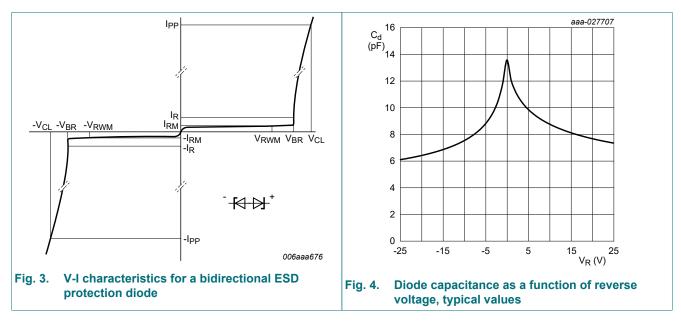


### 9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	24	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C		25.5	30.5	35.5	V
I <sub>RM</sub>	reverse leakage current	V <sub>R</sub> = 24 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	14	17	pF
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1]	-	31	40	V
		I <sub>PPM</sub> = 3.5 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1]	-	33	42	V
		I <sub>PP</sub> = 16 A; t <sub>p</sub> = TLP; T <sub>amb</sub> = 25 °C	[2]	-	32	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[2]	-	0.2	-	Ω

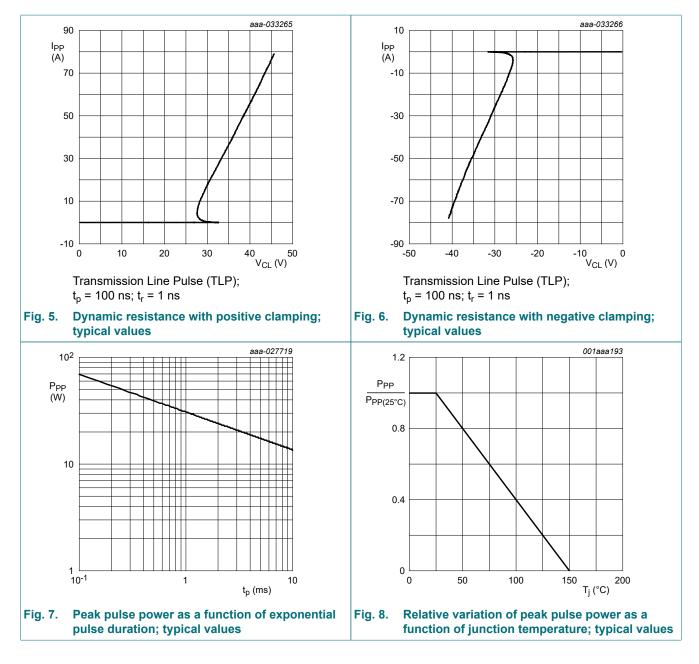
[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008



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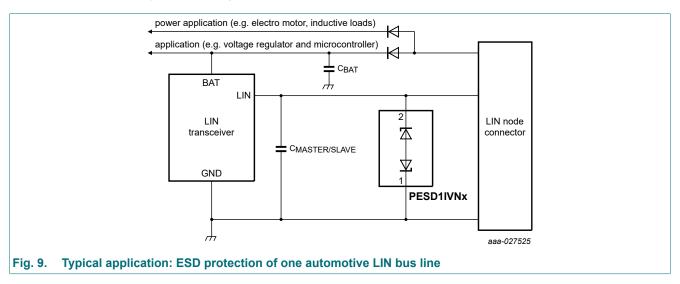
#### ESD protection for In-vehicle networks



**Product data sheet** 

### **10.** Application information

The device is designed for the protection of one automotive IVN bus line from the damage caused by ESD and surge pulses.



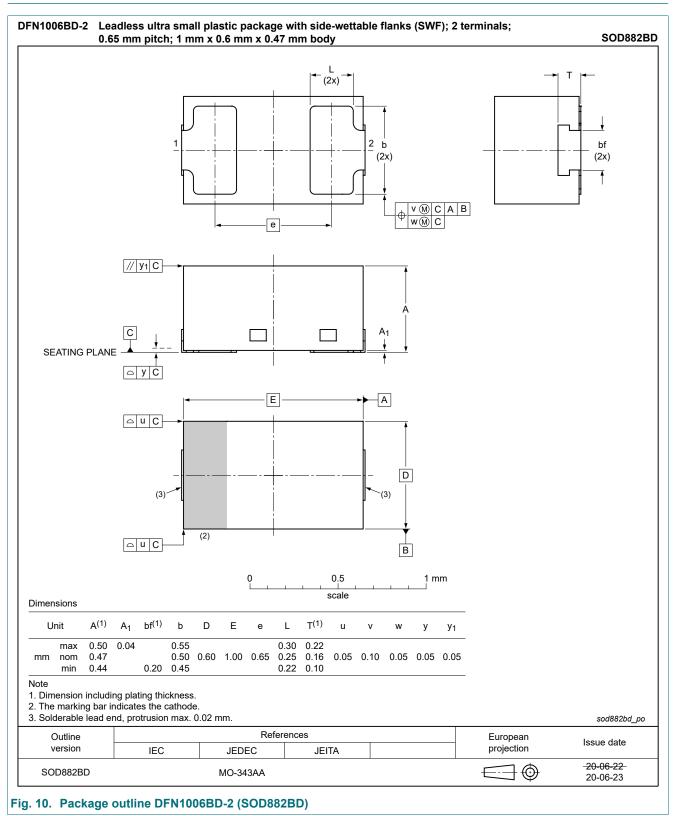
#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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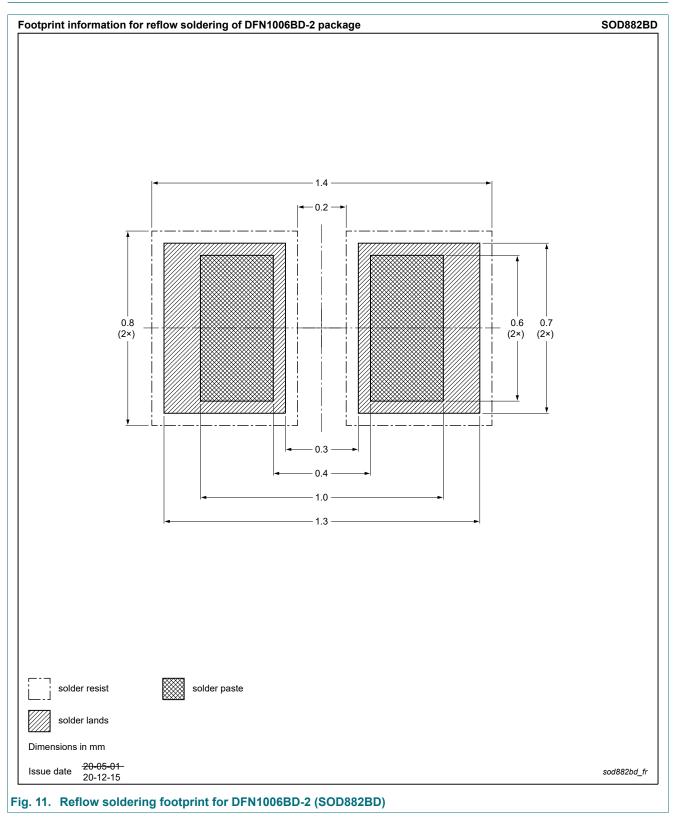
### 11. Package outline



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#### ESD protection for In-vehicle networks

# 12. Soldering



# **13. Revision history**

Table 7. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD1IVN24-LS v.1	20210409	Product data sheet	-	-	

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# 14. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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