



PBSS5160T-Q

60 V, 1 A PNP low VCEsat transistor

4 October 2023

Product data sheet

1. General description

PNP low VCEsat transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4160T-Q

2. Features and benefits

- Low collector-emitter saturation voltage VCEsat
- High collector current capability IC and ICM
- High efficiency due to less heat generation
- Reduces Printed-Circuit Board (PCB) area required
- Cost-effective replacement for medium power transistors BCP52 and BCX52
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Major application segments:
 - Automotive
 - Telecom infrastructure
 - Industrial
- Power management:
 - JDC-to-DC conversion
 - Supply line switching
- Peripheral driver:
 - Driver in low supply voltage applications (e.g. lamps and LEDs)
 - Inductive load drivers (e.g. relays, buzzers and motors)

4. Quick reference data

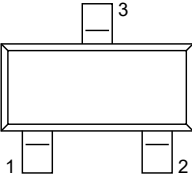
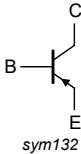
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-60	V
I _C	collector current		[1]	-	-	-1	A
I _{CM}	peak collector current	limited by T _{j(max)} ; t _p = 1 ms		-	-	-2	A
R _{CEsat}	collector-emitter saturation resistance	I _C = -1 A; I _B = -100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C		-	220	330	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym132
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS5160T-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PBSS5160T-Q	%U6

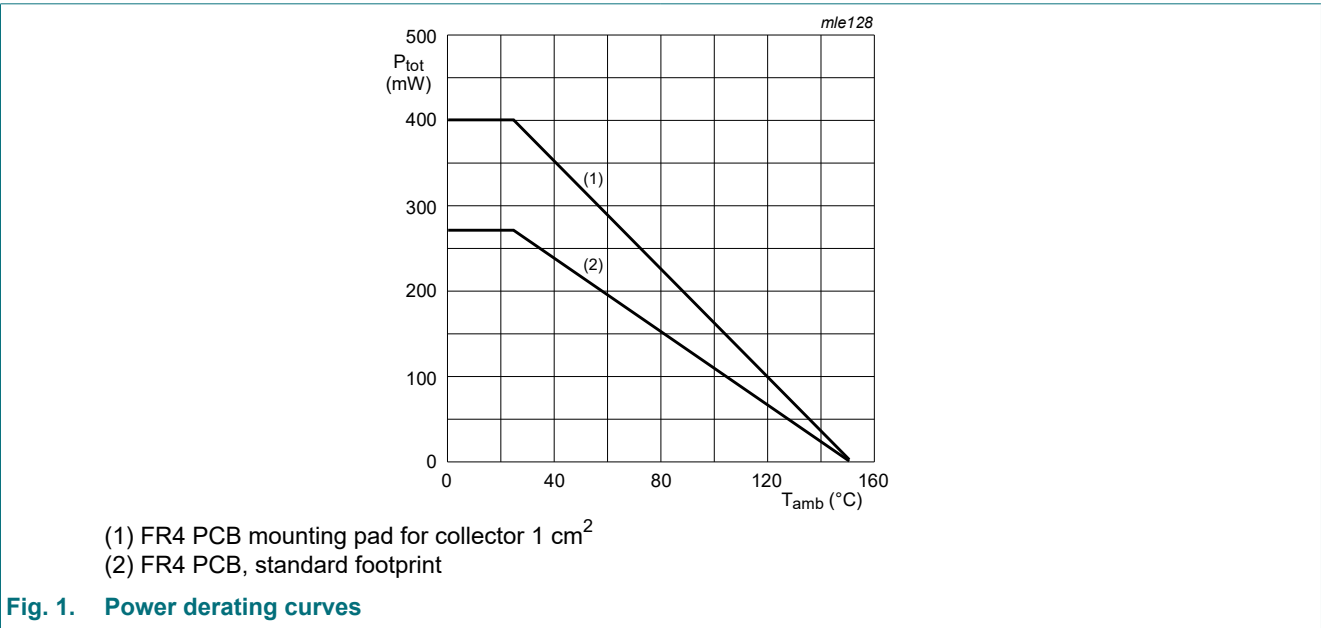
[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-80	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current		[1]	-	-0.9	A
			[2]	-	-1	A
I _{CM}	peak collector current	limited by T _{j(max)} ; t _p = 1 ms		-	-2	A
I _B	base current			-	-300	mA
I _{BM}	peak base current	t _p ≤ 300 μs; δ ≤ 0.02; pulsed		-	-1	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	270	mW
			[2]	-	400	mW
			[1] [3]	-	1.25	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
[3] Operated under pulse conditions: duty cycle δ ≤ 20 %, pulse width t_p ≤ 10 ms.

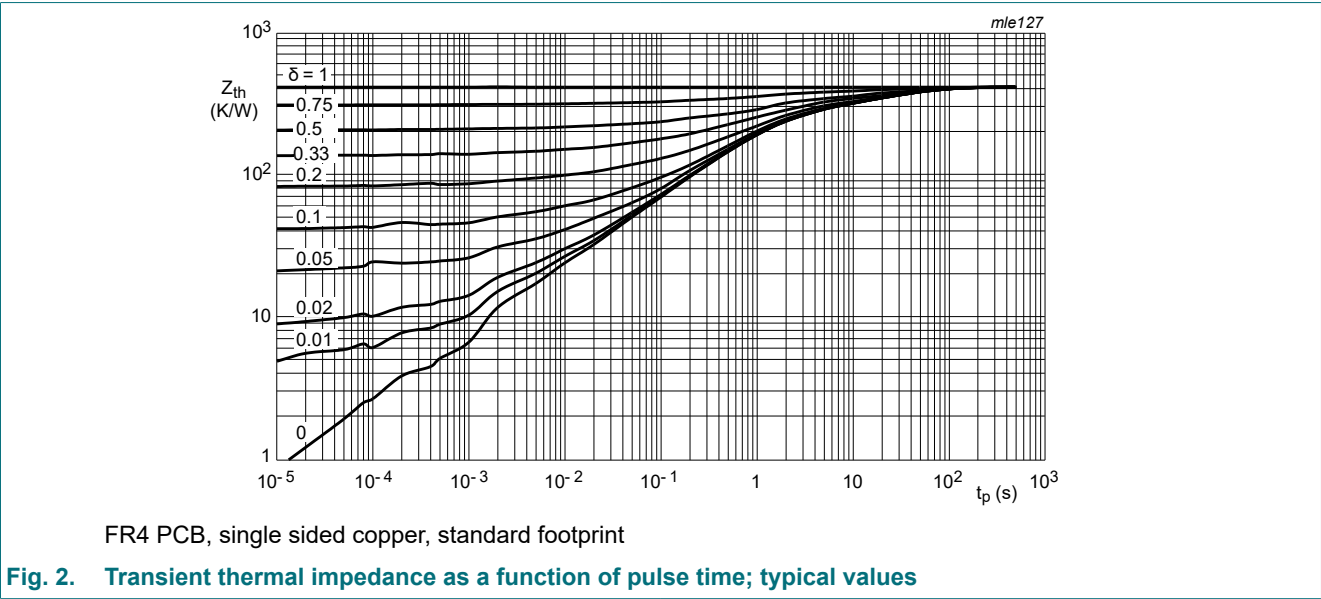


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	465	K/W
			[2]	-	-	312	K/W
			[1] [3]	-	-	100	K/W

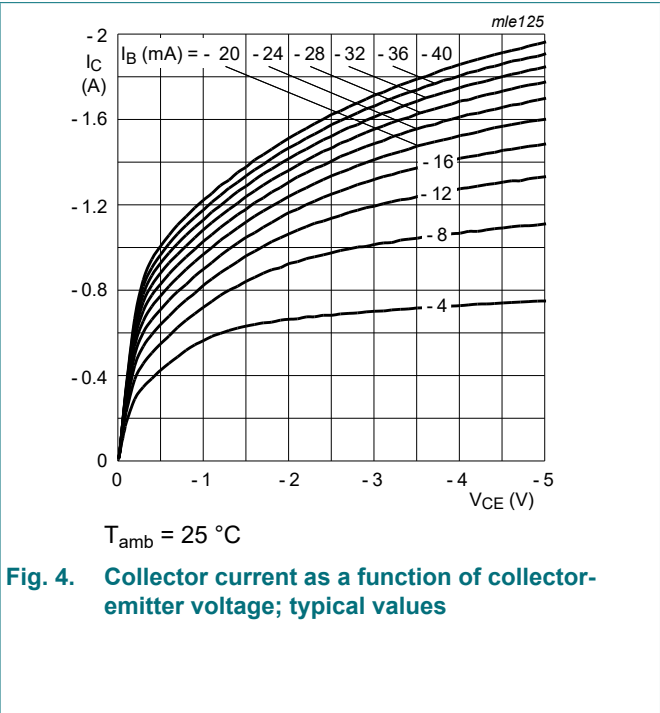
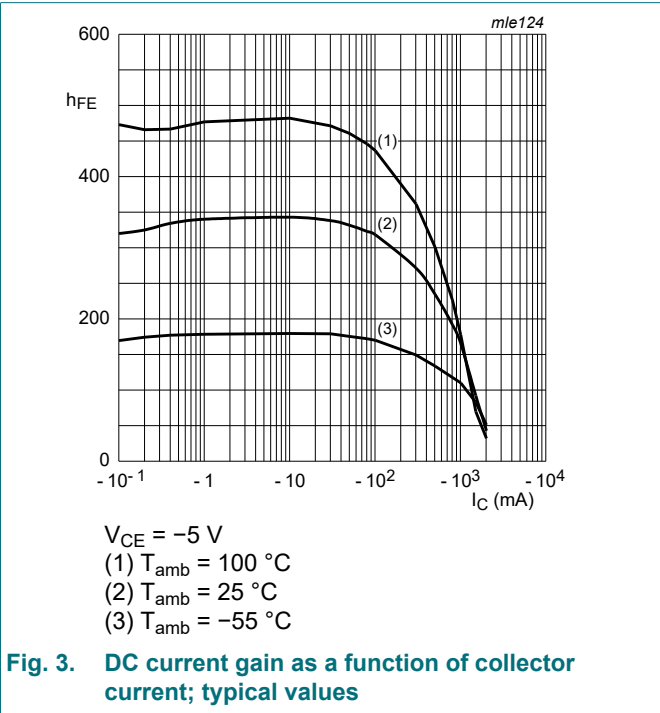
- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
[3] Operated under pulse conditions: duty cycle $\delta \leq 20\%$, pulse width $t_p \leq 10\text{ ms}$.



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
		V _{CB} = -60 V; I _E = 0 A; T _j = 150 °C	-	-	-50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = -60 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -1 mA; T _{amb} = 25 °C	200	350	-	
		V _{CE} = -5 V; I _C = -500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	150	250	-	
		V _{CE} = -5 V; I _C = -1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	160	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -100 mA; I _B = -1 mA; T _{amb} = 25 °C	-	-110	-160	mV
		I _C = -500 mA; I _B = -50 mA; T _{amb} = 25 °C	-	-120	-175	mV
		I _C = -1 A; I _B = -100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-220	-330	mV
R _{CEsat}	collector-emitter saturation resistance		-	220	330	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; T _{amb} = 25 °C	-	-0.95	-1.1	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -5 V; I _C = -1 A; T _{amb} = 25 °C	-	-0.82	-0.9	V
f _T	transition frequency	V _{CE} = -10 V; I _C = -50 mA; f = 100 MHz; T _{amb} = 25 °C	150	220	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	9	15	pF



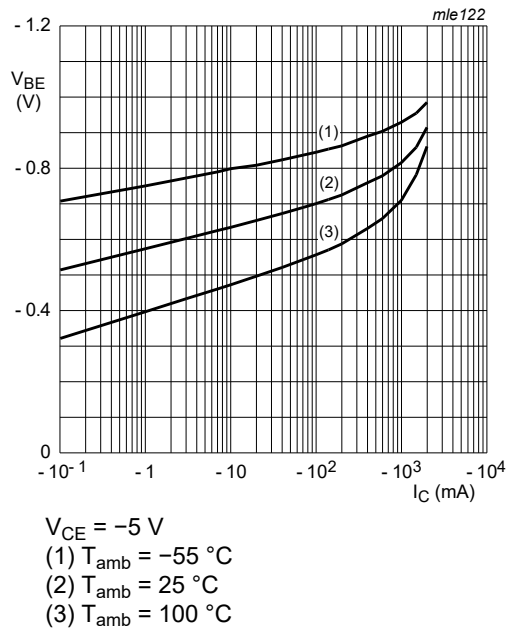


Fig. 5. Base-emitter voltage as a function of collector current; typical values

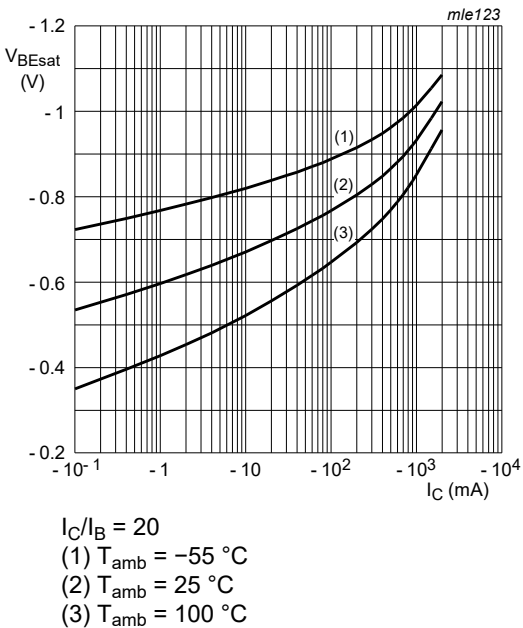


Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

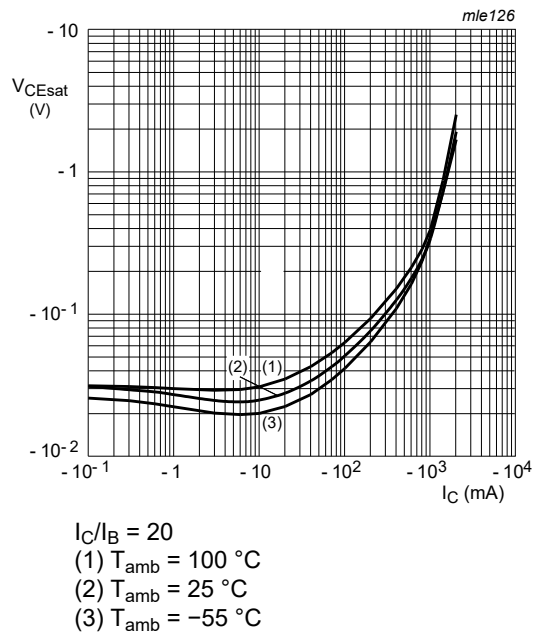


Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

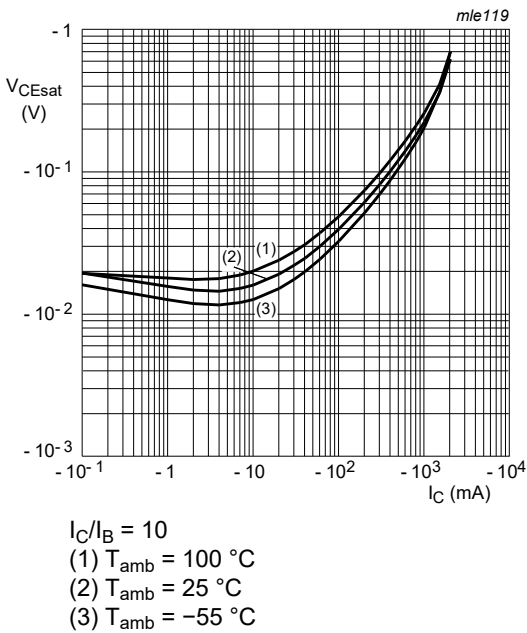
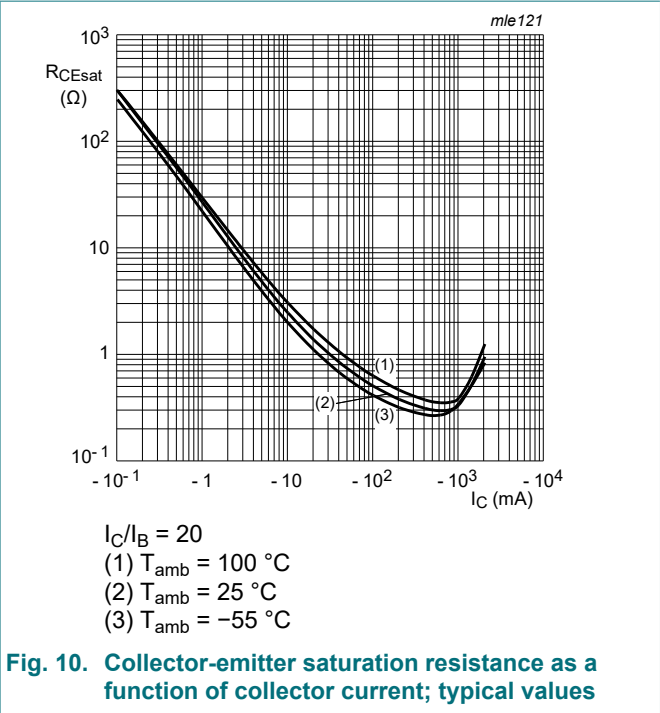
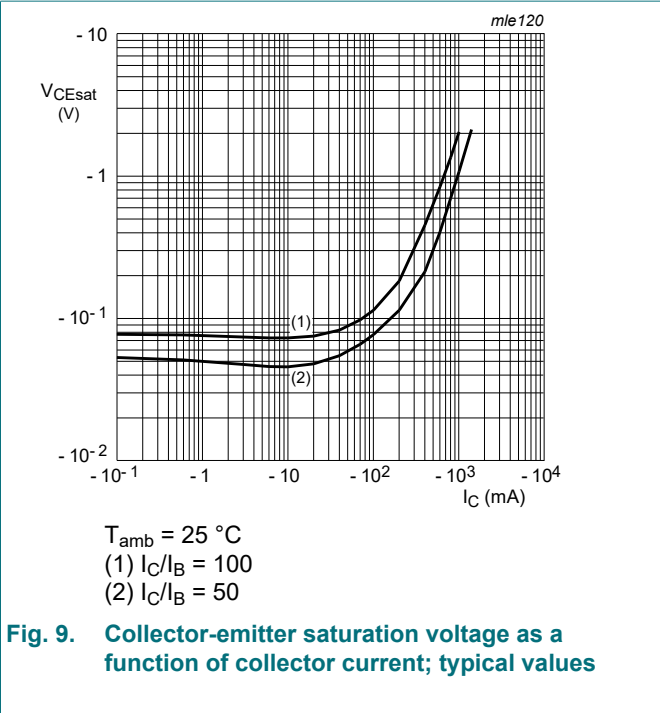


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

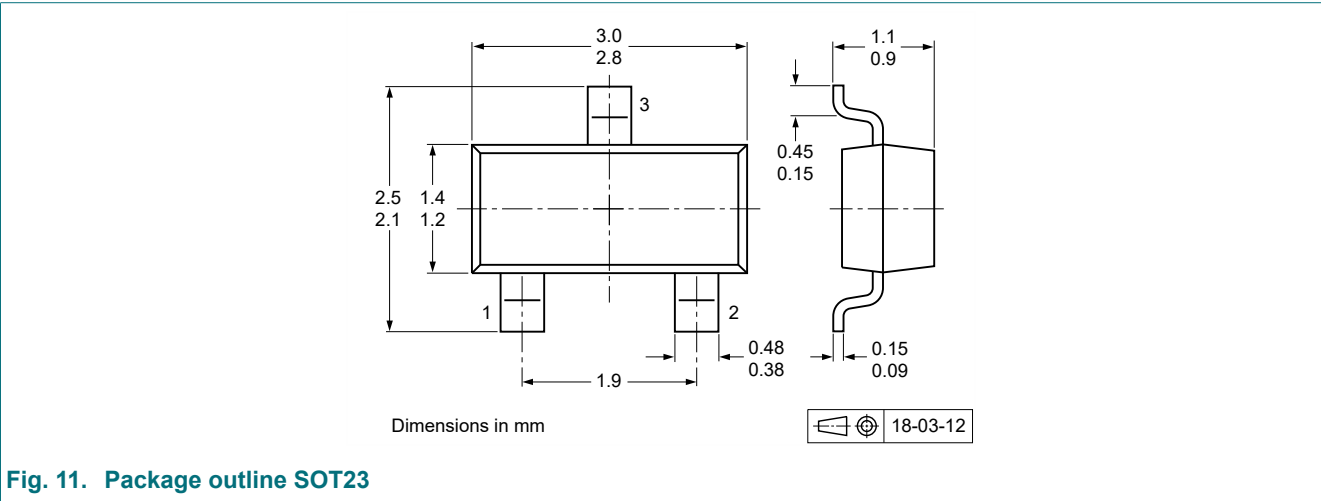


11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



Fig. 12. Reflow soldering footprint for SOT23

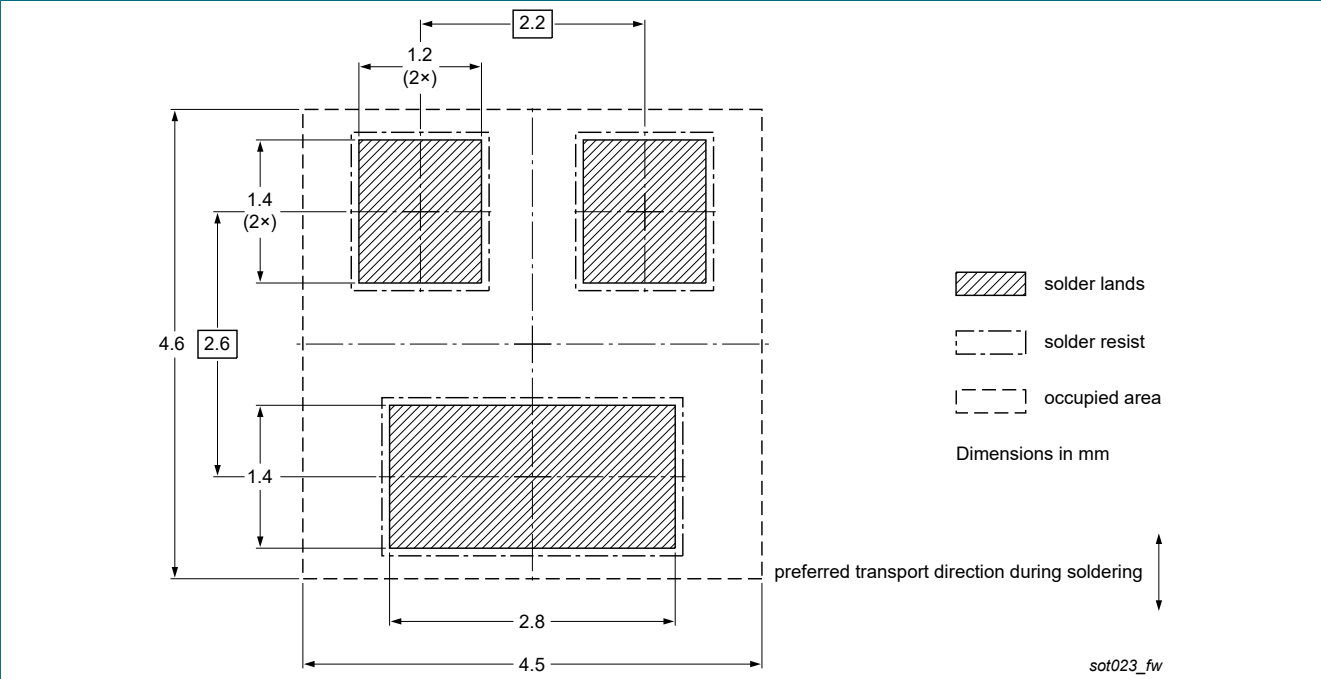


Fig. 13. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS5160T-Q v.1	20231004	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 3

9. Thermal characteristics..... 4

10. Characteristics.....5

11. Test information..... 7

12. Package outline..... 7

13. Soldering..... 8

14. Revision history.....9

15. Legal information.....10

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