# **HEF40106B**

# **Hex inverting Schmitt trigger**

Rev. 9 — 22 November 2021

**Product data sheet** 

### 1. General description

The HEF40106B is a hex inverter with Schmitt-trigger inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{DD}$ .

#### 2. Features and benefits

- Wide supply voltage range from 3.0 V to 15.0 V
- CMOS low power dissipation
- · High noise immunity
- · Schmitt trigger input discrimination
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Complies with JEDEC standard JESD 13-B
- · ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-B exceeds 200 V

### 3. Applications

- Wave and pulse shapers
- Astable multivibrators
- Monostable multivibrators

## 4. Ordering information

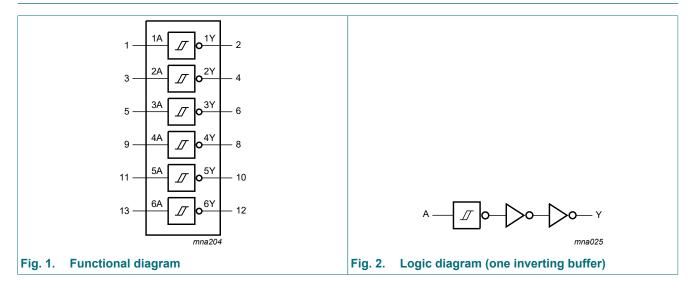
**Table 1. Ordering information** 

Type number	Package										
	Temperature range	Name	Description	Version							
HEF40106BT	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1							
HEF40106BTT	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1							



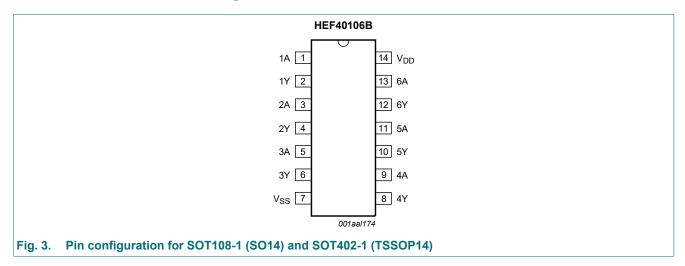
**Hex inverting Schmitt trigger** 

## 5. Functional diagram



## 6. Pinning information

### 6.1. Pinning



#### 6.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	input
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	output
$V_{DD}$	14	supply voltage
V <sub>SS</sub>	7	ground (0 V)

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## 7. Functional description

#### Table 3. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input	Output
nA	nY
L	Н
Н	L

## 8. Limiting values

#### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to  $V_{SS} = 0 \text{ V}$  (ground).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DD}$	supply voltage		-0.5	+18	V
I <sub>IK</sub>	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
VI	input voltage		-0.5	V <sub>DD</sub> + 0.5	V
I <sub>OK</sub>	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{DD} + 0.5 \text{ V}$	-	±10	mA
I <sub>I/O</sub>	input/output current		-	±10	mA
$I_{DD}$	supply current		-	50	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>amb</sub>	ambient temperature		-40	+125	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$ [1]	-	500	mW
Р	power dissipation	per output	-	100	mW

<sup>[1]</sup> For SOT108-1 (SO14) package:  $P_{tot}$  derates linearly with 10.1 mW/K above 100 °C. For SOT402-1 (TSSOP14) package:  $P_{tot}$  derates linearly with 7.3 mW/K above 81 °C.

# 9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DD}$	supply voltage		3	15	V
VI	input voltage		0	$V_{DD}$	V
T <sub>amb</sub>	ambient temperature	in free air	-40	+125	°C

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### 10. Static characteristics

#### **Table 6. Static characteristics**

 $V_{SS} = 0 \ V$ ;  $V_{I} = V_{SS} \ or \ V_{DD}$ ; unless otherwise specified.

Symbol	Parameter	Conditions	$V_{DD}$	T <sub>amb</sub> =	-40 °C	T <sub>amb</sub> =	+25 °C	T <sub>amb</sub> =	+85 °C	T <sub>amb</sub> = +125 °C		Unit
				Min	Max	Min	Max	Min	Max	Min	Max	
V <sub>OH</sub>	HIGH-level	I <sub>O</sub>   < 1 μA	5 V	4.95	-	4.95	-	4.95	-	4.95	-	V
	output voltage		10 V	9.95	-	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	14.95	-	V
V <sub>OL</sub>	LOW-level output voltage	I <sub>O</sub>   < 1 μΑ	5 V	-	0.05	-	0.05	-	0.05	-	0.05	V
			10 V	-	0.05	-	0.05	-	0.05	-	0.05	V
			15 V	-	0.05	-	0.05	-	0.05	-	0.05	V
I <sub>OH</sub>	HIGH-level output current	V <sub>O</sub> = 2.5 V	5 V	-	-1.7	-	-1.4	-	-1.1	-	-1.1	mA
		V <sub>O</sub> = 4.6 V	5 V	-	-0.64	-	-0.5	-	-0.36	-	-0.36	mA
		V <sub>O</sub> = 9.5 V	10 V	-	-1.6	-	-1.3	-	-0.9	-	-0.9	mΑ
		V <sub>O</sub> = 13.5 V	15 V	-	-4.2	-	-3.4	-	-2.4	-	-2.4	mA
I <sub>OL</sub>	LOW-level	V <sub>O</sub> = 0.4 V	5 V	0.64	-	0.5	-	0.36	-	0.36	-	mΑ
	output current	V <sub>O</sub> = 0.5 V	10 V	1.6	-	1.3	-	0.9	-	0.9	-	mA
		V <sub>O</sub> = 1.5 V	15 V	4.2	-	3.4	-	2.4	-	2.4	-	mΑ
l <sub>l</sub>	input leakage current		15 V	-	±0.1	-	±0.1	-	±1.0	-	±1.0	μΑ
I <sub>DD</sub>	supply current	all valid input	5 V	-	0.25	-	0.25	-	7.5	-	7.5	μΑ
		combinations; I <sub>O</sub> = 0 A	10 V	-	0.5	-	0.5	-	15.0	-	15.0	μΑ
			15 V	-	1.0	-	1.0	-	30.0	-	30.0	μΑ
C <sub>I</sub>	input capacitance			-	-	-	7.5	-	-	-	-	pF

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## 11. Dynamic characteristics

#### **Table 7. Dynamic characteristics**

 $T_{amb}$  = 25 °C;  $C_L$  = 50 pF;  $t_r$  =  $t_f \le$  20 ns unless otherwise specified.

For waveforms see Fig. 4; for test circuit see Fig. 5;

Symbol	Parameter	Conditions	$V_{DD}$	Extrapolation formula [1]	Min	Тур	Max	Unit
t <sub>PHL</sub>	HIGH to LOW	nA or nB to nY	5 V	63 ns + (0.55 ns/pF)C <sub>L</sub>	-	90	180	ns
	propagation delay		10 V	29 ns + (0.23 ns/pF)C <sub>L</sub>	-	35	70	ns
			15 V	22 ns + (0.16 ns/pF)C <sub>L</sub>	-	30	60	ns
t <sub>PLH</sub>	LOW to HIGH	nA or nB to nY	5 V	58 ns + (0.55 ns/pF)C <sub>L</sub>	-	75	150	ns
p	propagation delay		10 V	29 ns + (0.23 ns/pF)C <sub>L</sub>	-	35	70	ns
			15 V	22 ns + (0.16 ns/pF)C <sub>L</sub>	-	30	60	ns
t <sub>THL</sub>	HIGH to LOW output	nY to LOW	5 V	10 ns + (1.00 ns/pF)C <sub>L</sub>	-	60	120	ns
	transition time		10 V	9 ns + (0.42 ns/pF)C <sub>L</sub>	-	30	60	ns
			15 V	6 ns + (0.28 ns/pF)C <sub>L</sub>	-	20	40	ns
t <sub>TLH</sub>	LOW to HIGH output	nA or nB to	5 V	10 ns + (1.00 ns/pF)C <sub>L</sub>	-	60	120	ns
	transition time	HIGH	10 V	9 ns + (0.42 ns/pF)C <sub>L</sub>	-	30	60	ns
			15 V	6 ns + (0.28 ns/pF)C <sub>L</sub>	-	20	40	ns

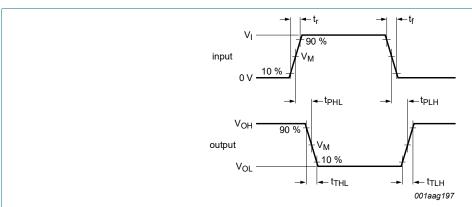
<sup>[1]</sup> Typical value of the propagation delay and output transition time can be calculated with the extrapolation formula (C<sub>L</sub> in pF).

#### Table 8. Dynamic power dissipation

 $V_{SS} = 0 \ V; \ t_r = t_f \le 20 \ ns; \ T_{amb} = 25 \ ^{\circ}C.$ 

Symbol	Parameter	$V_{DD}$	Typical formula	where:
$P_D$	dynamic power	5 V	. (0 2) 22 (. )	f <sub>i</sub> = input frequency in MHz;
	dissipation	10 V	F D = 30000 ^ 1; T / 11° ^ (7) 1 ^ V DD	f <sub>o</sub> = output frequency in MHz; C <sub>L</sub> = output load capacitance in pF;
		15 V		$\Sigma(f_o \times C_L)$ = sum of the outputs; $V_{DD}$ = supply voltage in V.

#### 11.1. Waveforms and test circuit



Measurement points are given in Table 9.

Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage levels that occur with the output load.

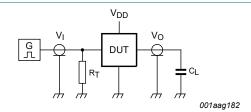
 $t_r$ ,  $t_f$  = input rise and fall times.

Fig. 4. Propagation delay and output transition time

#### **Hex inverting Schmitt trigger**

**Table 9. Measurement points** 

Supply voltage	Input	Output
$V_{DD}$	V <sub>M</sub>	V <sub>M</sub>
5 V to 15 V	0.5 V <sub>DD</sub>	0.5 V <sub>DD</sub>



Test data given in Table 10.

Definitions for test circuit:

C<sub>L</sub> = load capacitance including jig and probe capacitance.

 $R_T$  = termination resistance should be equal to the output impedance  $Z_0$  of the pulse generator.

#### Fig. 5. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Input	Input					
$V_{DD}$	V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	C <sub>L</sub>				
5 V to 15 V	V <sub>SS</sub> or V <sub>DD</sub>	≤ 20 ns	50 pF				

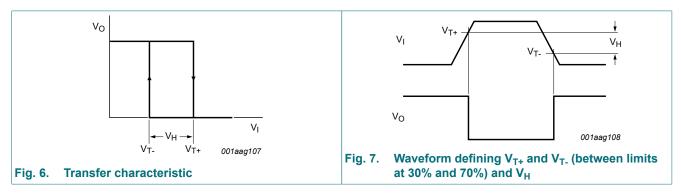
#### 12. Transfer characteristics

#### **Table 11. Transfer characteristics**

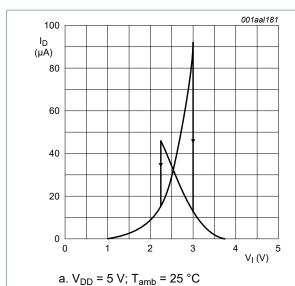
 $V_{SS} = 0 V$ ; see Fig. 6 and Fig. 7.

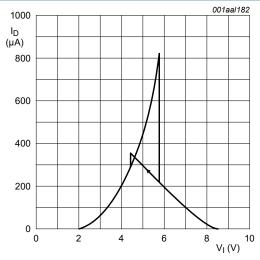
Symbol	Parameter	Conditions	$V_{DD}$	T <sub>amb</sub> =	-40 °C to	+85 °C	T <sub>amb</sub> = -40 °(	C to +125 °C	Unit
				Min	Typ [1]	Max	Min	Max	
$V_{T+}$	positive-going		5 V	2.0	3.0	3.5	2.0	3.5	V
	threshold voltage		10 V	3.7	5.8	7.0	3.7	7.0	V
			15 V	4.9	8.3	11.0	4.9	11.0	V
	negative-going		5 V	1.5	2.2	3.0	1.5	3.0	V
	threshold voltage		10 V	3.0	4.5	6.3	3.0	6.3	V
			15 V	4.0	6.5	10.1	4.0	10.1	V
$V_{H}$	hysteresis voltage		5 V	0.5	0.8	-	0.5	-	V
			10 V	0.7	1.3	-	0.7	-	V
			15 V	0.9	1.8	-	0.9	-	V

#### [1] All typical values are measured at $T_{amb}$ = 25 °C.



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b. V<sub>DD</sub> = 10 V; T<sub>amb</sub> = 25 °C

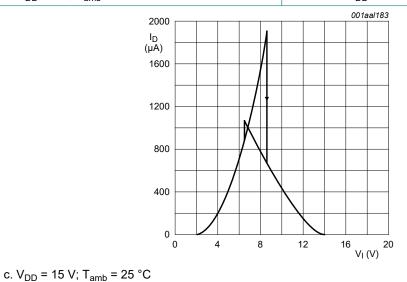


Fig. 8. Typical drain current as a function of input

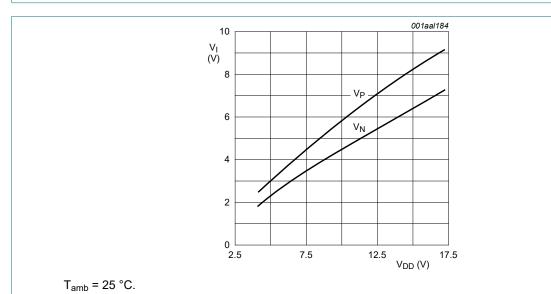


Fig. 9. Typical switching levels as a function of supply voltage

**Hex inverting Schmitt trigger** 

## 13. Application information

Some examples of applications for the HEF40106B are:

- Wave and pulse shapers
- Astable multivibrators
- · Monostable multivibrators

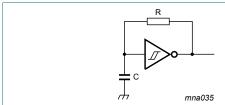


Fig. 10. Astable multivibrator

Fig. 11. Schmitt trigger driven via a high-impedance input

If a Schmitt trigger is driven via a high-impedance (R > 1 k $\Omega$ ), then it is necessary to incorporate a capacitor C with a value of  $\frac{C}{C_P} > \frac{V_{DD} V_{SS}}{V_H}$ ; otherwise oscillation can occur on the edges of a pulse.

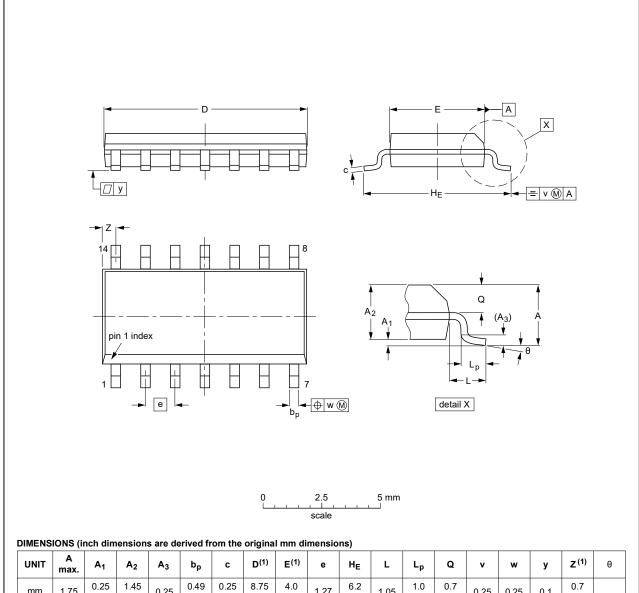
 $C_p$  is the external parasitic capacitance between inputs and output; the value depends on the circuit board layout.

#### **Hex inverting Schmitt trigger**

# 14. Package outline

#### SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	HE	L	Lp	Q	v	w	у	Z <sup>(1)</sup>	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01	I	0.0100 0.0075	0.35 0.34	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	0°

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

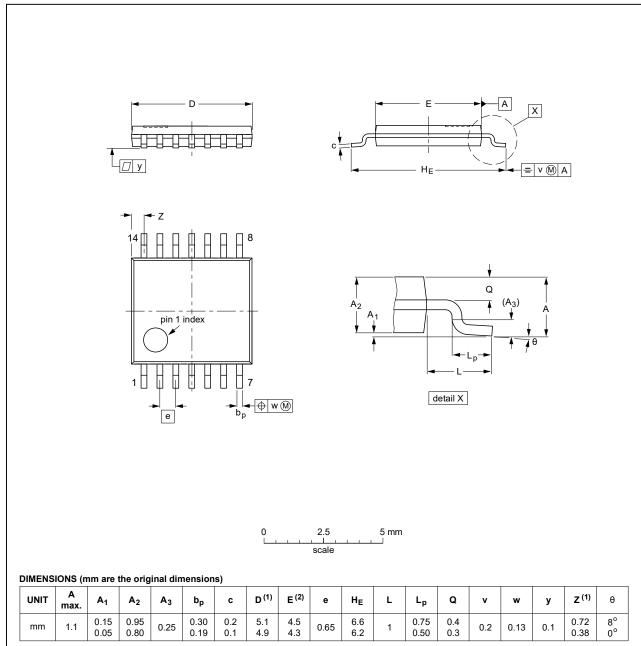
OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT108-1	076E06	MS-012				<del>99-12-27</del> 03-02-19

Fig. 12. Package outline SOT108-1 (SO14)

#### **Hex inverting Schmitt trigger**

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



#### Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFERENCES				EUROPEAN	ISSUE DATE
	VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
	SOT402-1		MO-153				<del>99-12-27</del> 03-02-18

Fig. 13. Package outline SOT402-1 (TSSOP14)

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### 15. Abbreviations

#### **Table 12. Abbreviations**

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

## 16. Revision history

#### **Table 13. Revision history**

Table 13. Revision history						
Document ID	Release date	Data sheet status	Change notice	Supersedes		
HEF40106B v.9	20211122	Product data sheet	-	HEF40106B v.8		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>					
	Section 1 and Section 2 updated.					
	Table 4: Derating values for P <sub>tot</sub> total power dissipation updated.					
HEF40106B v.8	20151210	Product data sheet	-	HEF40106B v.7		
Modifications:	Type number HEF40106BP (SOT27-1) removed.					
HEF40106B v.7	20111121	Product data sheet	-	HEF40106B v.6		
Modifications:	<ul> <li>Legal pages updated.</li> <li>Changes in <u>Section 1</u> and <u>Section 2</u>.</li> </ul>					
HEF40106B v.6	20110823	Product data sheet	-	HEF40106B v.5		
HEF40106B v.5	20110511	Product data sheet	-	HEF40106B v.4		
HEF40106B v.4	20101115	Product data sheet	-	HEF40106B_CNV v.3		
HEF40106B_CNV v.3	19950101	Product specification	-	HEF40106B_CNV v.2		
HEF40106B_CNV v.2	19950101	Product specification	-	-		

#### Hex inverting Schmitt trigger

### 17. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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