**Product data sheet** 

# 1. General description

PNP Darlington transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

NPN complement: BST51

### 2. Features and benefits

- · Integrated diode and resistor
- AEC-Q101 qualified

## 3. Applications

- Industrial switching applications such as:
  - Print hammer
  - Solenoid
  - Relay and lamp driving

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-60	V
Ic	collector current		-	-	-1	Α
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -10 V; $I_{C}$ = -150 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	1000	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		2
2	С	collector		3-1-
3	В	base	3 2 1 SOT89	sym081



### **PNP Darlington transistor**

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
BST61		plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	<u>SOT89</u>			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BST61	BS2

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-1	Α
I <sub>CM</sub>	peak collector current			-	-2	Α
I <sub>B</sub>	base current			-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.3	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

### **PNP Darlington transistor**

## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-50	nA
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = -60 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	-	-50	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -10 V; $I_{C}$ = -150 mA; pulsed; $t_{p} \le$ 300 μs; $δ \le$ 0.02; $T_{amb}$ = 25 °C	1000	-	-	
		$V_{CE}$ = -10 V; $I_{C}$ = -500 mA; pulsed; $t_{p} \le$ 300 μs; $δ \le$ 0.02; $T_{amb}$ = 25 °C	2000	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C}$ = -500 mA; $I_{B}$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-	-1.3	V
		$I_C$ = 500 mA; $I_B$ = -0.5 mA; $T_j$ = 150 °C	-	-	-1.3	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C}$ = -500 mA; $I_{B}$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-	-1.9	V
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; $I_{C}$ = -500 mA; f = 100 MHz; $T_{amb}$ = 25 °C	-	200	-	MHz
Switching t	imes (between 10% and 90	% levels)		•	•	
t <sub>on</sub>	turn-on time	I <sub>Bon</sub> = -0.5 mA; I <sub>Boff</sub> = 0.5 mA; I <sub>Con</sub> =	-	500	-	ns
t <sub>off</sub>	turn-off time	-500 mA; T <sub>amb</sub> = 25 °C	-	700	-	ns

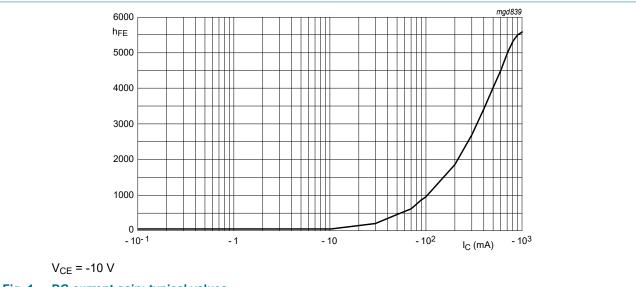
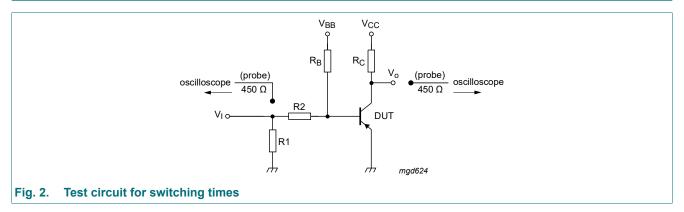


Fig. 1. DC current gain; typical values

**PNP Darlington transistor** 

## 11. Test information

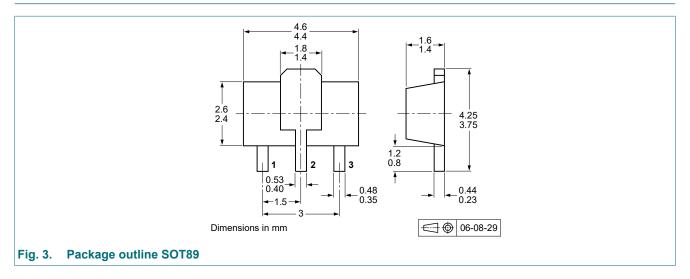


 $V_i$  = -10 V; T = 200 μs; tp = 6 μs; t<sub>r</sub> = t<sub>f</sub> ≤ 3 ns R1 = 56 Ω; R2 = 10 kΩ; R<sub>B</sub> = 10 kΩ; R<sub>C</sub> = 18 Ω V<sub>BB</sub> = 1.8 V; V<sub>CC</sub> = -10.7 V Oscilloscope: input impedance  $Z_i$  = 50 Ω

#### **Quality information**

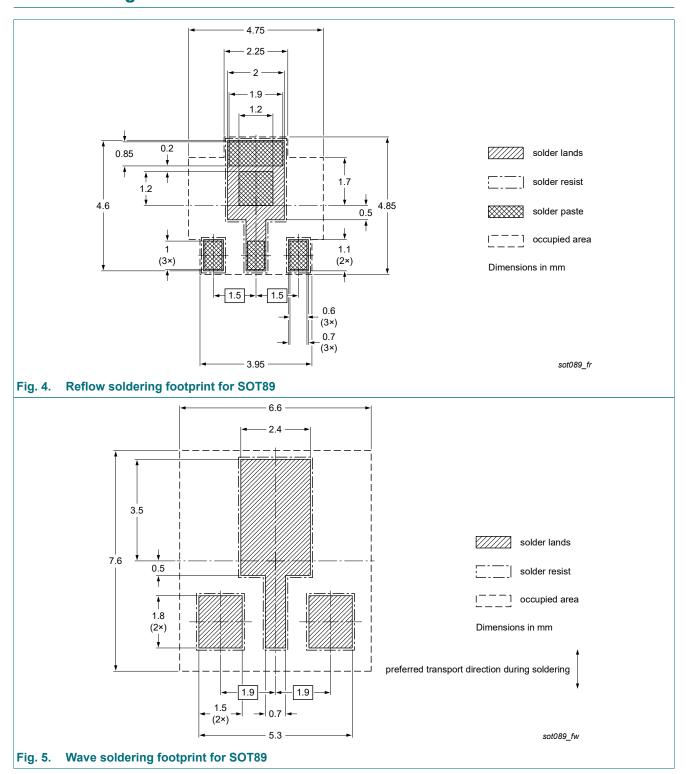
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



### **PNP Darlington transistor**

# 13. Soldering



## **PNP** Darlington transistor

# 14. Revision history

### **Table 8. Revision history**

Table 6. Revision mistory	<i>f</i>					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BST61 v.3	20231027	Product data sheet	-	BST60_61_62 v.2		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Family data sheet splitted to single type data sheet.</li> </ul>					
BST60_61_62 v.2	20041209	Product data sheet	-	BST60_61_62 v.1		
BST60_61_62 v.1	20010220	Product specification	-	-		

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### **PNP Darlington transistor**

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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