Product data sheet

1. General description

PNP Darlington transistor in an SOT223 plastic package.

NPN complement: BSP52

2. Features and benefits

- High current of -1 A
- Low voltage of -80 V
- Integrated diode and resistor
- AEC-Q101 qualified

3. Applications

- Industrial switching applications such as:
 - Print hammer
 - Solenoid
 - Relay and lamp drivers

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-	-90	V
V _{CES}	collector-emitter voltage	base short-circuited to emitter		-	-	-80	V
I _C	collector current			-	-	-1	Α
I _{CM}	peak collector current			-	-	-2	Α
h _{FE}	DC current gain	$V_{CE} = -10 \text{ V}; I_{C} = -150 \text{ mA}$	[1]	1000	-	-	

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	
2	С	collector		
3	E	emitter		
4	С	collector	☐1 ☐2 ☐3 SC-73 (SOT223)	E aaa-027605

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BSP62	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 4.6 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
BSP62	BSP62

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-90	V
V _{CES}	collector-emitter voltage	base short-circuited to emitter		-	-80	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-1	Α
I _{CM}	peak collector current			-	-2	Α
I _{Blim}	limiting base current			-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.25	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	98	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	17	K/W

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm².

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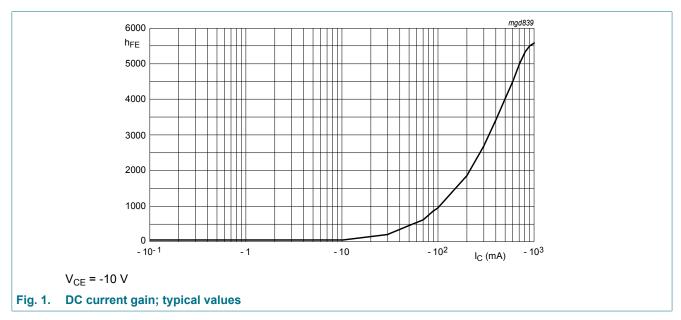
10. Characteristics

Table 7. Characteristics

 T_i = 25 °C unless otherwise specified

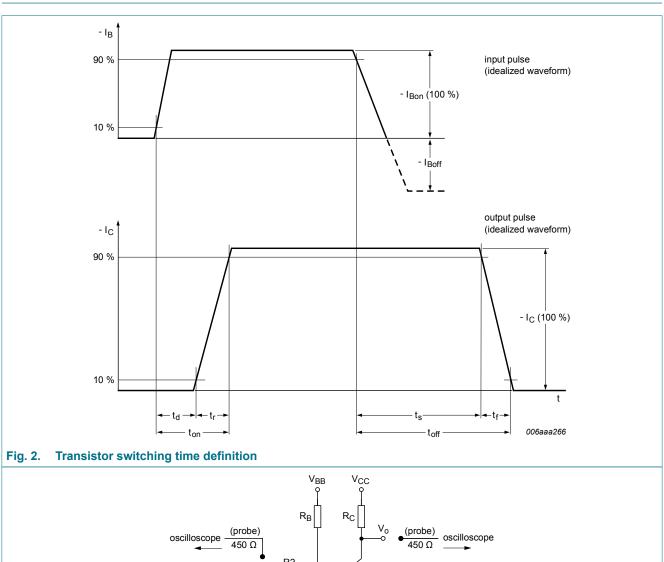
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-90	-	-	V
$V_{(BR)CES}$	collector-emitter breakdown voltage	$I_C = -2 \text{ mA}; V_{BE} = 0 \text{ V}$		-80	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_C = 0 \text{ A}; I_E = -100 \mu\text{A}$		-5	-	-	V
I _{CES}	collector-emitter cut-off current	V _{BE} = 0 V; V _{CE} = -80 V		-	-	-50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -4 V; I _C = 0 A		-	-	-50	nA
h _{FE} [DC current gain	V _{CE} = -10 V; I _C = -150 mA	[1]	1000	-	-	
		V _{CE} = -10 V; I _C = -500 mA	[1]	2000	-	-	
V _{CEsat}	collector-emitter	I _C = -500 mA; I _B = -0.5 mA		-	-	-1.3	V
	saturation voltage	I_C = -500 mA; I_B = -0.5 mA; T_j = 150 °C		-	-	-1.3	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -500 \text{ mA}; I_B = -0.5 \text{ mA}$		-	-	-1.9	V
t _{on}	turn-on time	I _C = -500 mA; I _{Bon} = -0.5 mA;		-	400	-	ns
t _{off}	turn-off time	I _{Boff} = 0.5 mA		-	1500	-	ns
f _T	transition frequency	V _{CE} = -5 V; I _C = -500 mA; f = 100 MHz		-	200	-	MHz

[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$



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11. Test information



VIO DUT

mgd624 V_I = -10 V; T = 200 μ s; t_p = 6 μ s; t_r = $t_f \le 3$ ns. R1 = 56 Ω ; R2 = 10 k Ω ; R_B = 10 k Ω ; R_C = 18 Ω . $V_{BB} = 1.8 \text{ V}; V_{CC} = -10.7 \text{ V}.$

Oscilloscope: input impedance $Z_i = 50 \Omega$.

Fig. 3. Test circuit or switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline

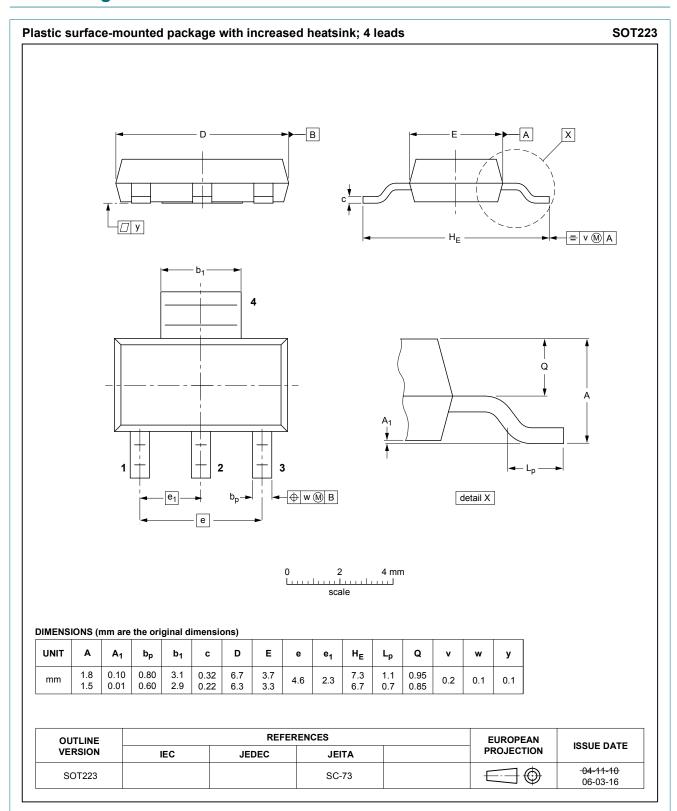
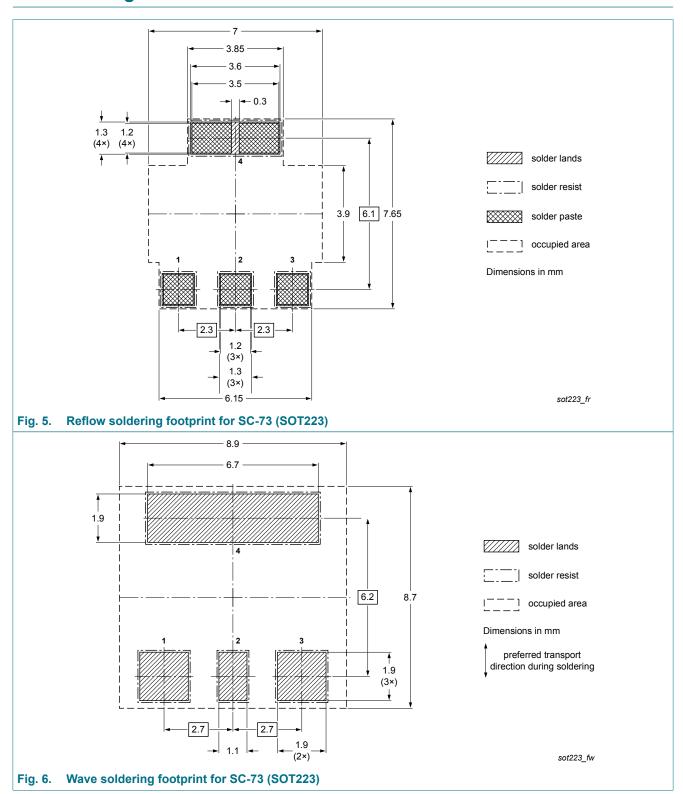


Fig. 4. Package outline SC-73 (SOT223)

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
BSP62 v.4	20180502	Product data sheet	-	BSP62 v.3				
Modifications:	I _{CES} values correcte	I _{CES} values corrected.						
BSP62 v.3	20180216	Product data sheet	-	BSP60_61_62 v.2				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BSP62

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