BC847xQC series

45 V, 100 mA NPN general-purpose transistor

Rev. 1 — 4 January 2021

Product data sheet

1. General description

NPN general-purpose transistor in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview

Type number	Package		PNP complement:
	Nexperia	JEDEC	
BC847AQC	SOT8009	MO-340CA	BC857AQC
BC847BQC			BC857BQC
BC847CQC			BC857CQC

2. Features and benefits

- · High power dissipation capability
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- · Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- Space restricted applications

4. Quick reference data

Table 2. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	45	V
I _C	collector current		-	-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	200	mA
h _{FE}	DC current gain					
	BC847AQC	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}$	110	-	220	
	BC847BQC		200	-	450	
	BC847CQC		420	-	800	



5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	E	emitter		, , , , , , , , , , , , , , , , , , ,
3	С	collector		B—L
			3	Ė
				sym021
			Bottom view	

6. Ordering information

Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
BC847AQC		plastic, leadless ultra small outline package with	SOT8009			
BC847BQC	I	sidewettable flanks (SWF); 3 terminals; 0.8 mm pitch; 1.4 mm x 1.2 mm x 0.48 mm body				
BC847CQC		THILLY 1.2 HILLY 0.40 HILL BOOK				

7. Marking

Table 5. Marking

- table of manning					
Type number	Marking code				
BC847AQC	9C				
BC847BQC	9D				
BC847CQC	9E				

8. Limiting values

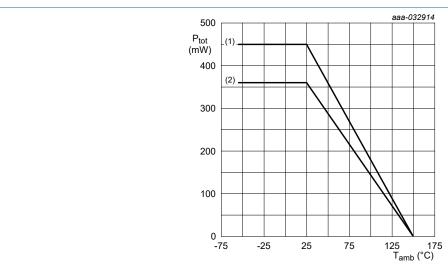
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	360	mW
			[2]	-	450	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 35 µm copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 70 µm copper; tin-plated and standard footprint.



- (1) FR4 PCB; single-sided 70 µm copper, standard footprint
- (2) FR4 PCB; single-sided 35 µm copper, standard footprint

Fig. 1. Power derating curves DFN1412D-3 (SOT8009)

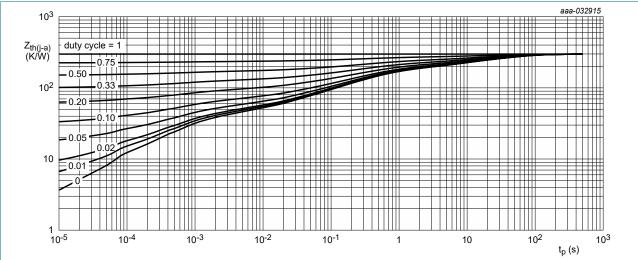
9. Thermal characteristics

Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

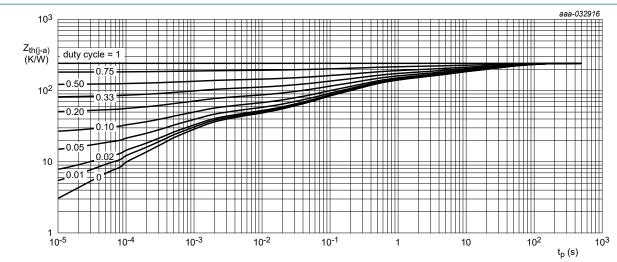
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	348	K/W
			[2]	-	-	278	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 35 µm copper; tin-plated and standard footprint.
- 2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 70 µm copper; tin-plated and standard footprint.



FR4 PCB; single-sided 35 µm copper, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided 70 µm copper, standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

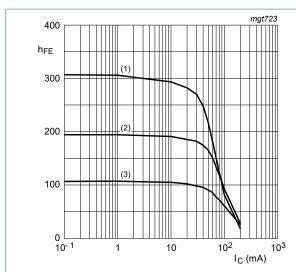
Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	, , ,		45	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	$I_E = 100 \mu A; I_C = 0 A$		6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A		-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain				'	'	
BC847AQC BC847BQC	BC847AQC	V _{CE} = 5 V; I _C = 2 mA		110	-	220	
			200	-	450		
	BC847CQC			420	-	800	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA		-	-	200	mV
	saturation voltage	I _C = 100 mA; I _B = 5 mA	[1]	-	-	400	mV
V_{BE}	base-emitter voltage	V _{CE} = 5 V ; I _C = 2 mA	[2]	580	-	700	mV
		V _{CE} = 5 V ; I _C = 10 mA	[2]	-	-	770	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA ; I _B = 0.5 mA		-	760	-	mV
	voltage	I _C = 100 mA ; I _B = 5 mA	[1]	-	900	-	mV
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz		100	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	-	1.5	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _E = i _e = 0 A; f = 1 MHz		-	11	-	pF
NF	noise figure	V_{CE} = 5 V; I_{C} = 200 μ A; R_{S} = 2 k Ω ; f = 1 kHz; B = 200 Hz		-	-	10	dB

5 / 13

 $[\]begin{array}{ll} [1] & \text{pulsed; } t_p \leq 300 \; \mu\text{s; } \delta \leq 0.02 \\ [2] & V_{BE} \; \text{decreases by about 2 mV/K with increasing temperature.} \end{array}$

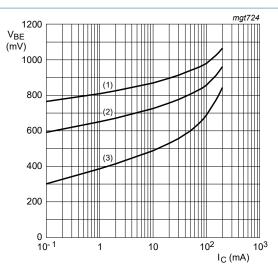


$$V_{CE} = 5 V$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 4. BC847AQC: DC current gain as a function of collector current; typical values



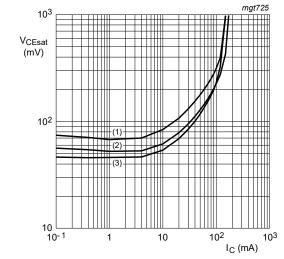
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. BC847AQC: Base-emitter voltage as a function of collector current; typical values



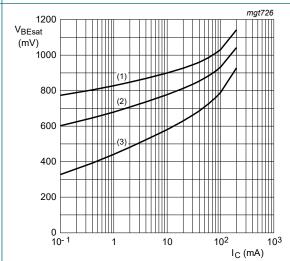
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 6. BC847AQC: Collector-emitter saturation voltage as a function of collector current; typical values



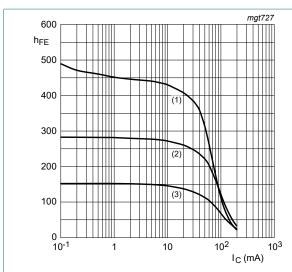
$$I_{\rm C}/I_{\rm B}$$
= 10

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 150 °C

ig. 7. BC847AQC: Base-emitter saturation voltage as a function of collector current; typical values



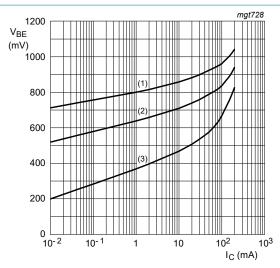
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

BC847BQC: DC current gain as a function of Fig. 8. collector current; typical values



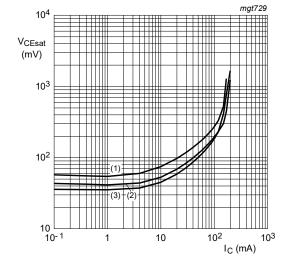
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 150 °C

BC847BQC: Base-emitter voltage as a function Fig. 9. of collector current; typical values



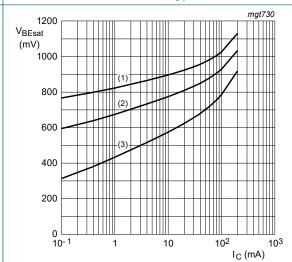
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 10. BC847BQC: Collector-emitter saturation voltage | Fig. 11. BC847BQC: Base-emitter saturation voltage as as a function of collector current; typical values



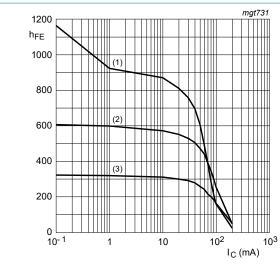
$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

a function of collector current; typical values



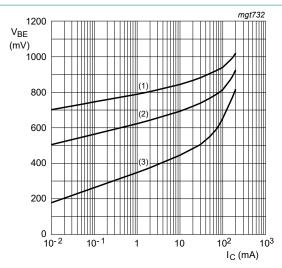
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 12. BC847CQC: DC current gain as a function of collector current; typical values



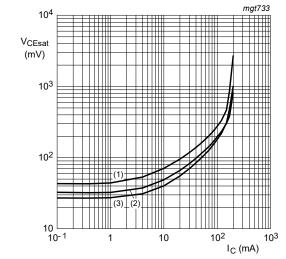
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb}$$
 = 150 °C

Fig. 13. BC847CQC: Base-emitter voltage as a function of collector current; typical values



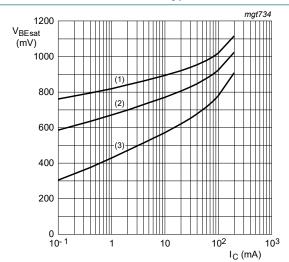
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb}$$
 = 150 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 14. BC847CQC: Collector-emitter saturation voltage | Fig. 15. BC847CQC: Base-emitter saturation voltage as as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline

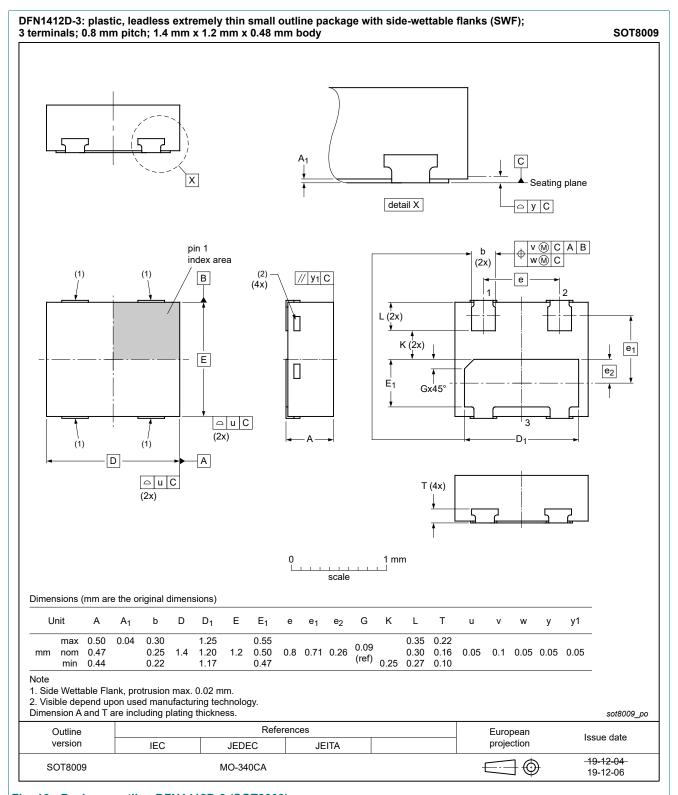
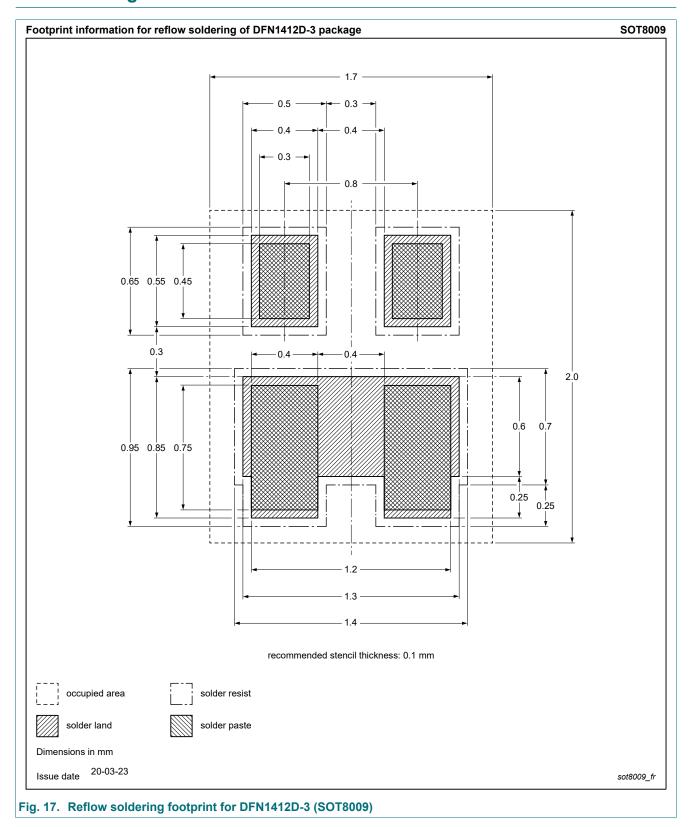


Fig. 16. Package outline DFN1412D-3 (SOT8009)

13. Soldering



14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC847XQC_SER v.1	20210104	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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